How to update the K-Frame Host Address Decoder (HAD) firmware.

1. Connect a computer to the K-Frame via the RS-232 serial port on the front edge of the controller board. The serial port is the 9 pin DB connector located next to the power switch. Serial com port settings are:
2. Baud rate: 115200
3. Data: 8 bit
4. Parity: None
5. Stop: 1 bit
6. Flow Control: None
7. At the command prompt type “consoleMenu” enter. Type what’s between the quotes. Don’t type the actually ””, okay. Enter is the Enter Key on the keyboard.
8. Type “9” enter to pick the debug menu.
9. Type “15” enter to pick Flash HADs
10. Type “m” enter to pick Multi Boot Configuration Image. Do NOT pick G for golden image. This is the backup image in case there are any problems with reprogramming the multi boot image.
11. Type “y” enter to start the update.
12. Sit back and relax, this is going to take about 7 minutes. Do NOT turn off power until the update is done.
13. At the end of programming all boards should have been updated successfully. If you have any failures try repeating steps 4-8. If there is still a problem continue on to step 9.
14. At this point cycle power for the new HADs to reload themselves. A reset will NOT do it. It must be a power cycle.
15. If there were any failures or if you want to verify that all the HADs are up to date go back into “consoleMenu” enter, type “9” enter for Debug, type “14” enter for Dump HAD Info. Depending on the number of boards in your system you should see something like:

Debug> 14

HAD Information Report:

HAD E5E00000: CPU Board 6, Num.Ver.Rev 2000.b.1, TimeStamp 05/17/2013 14:01, DMA:Yes

HAD E0800000: M/E Board 0, Num.Ver.Rev 3000.c.0, TimeStamp 10/30/2012 16:54, DMA:Yes

HAD E0C00000: M/E Board 1, Num.Ver.Rev 3000.c.0, TimeStamp 10/30/2012 16:54, DMA:Yes

HAD E1000000: M/E Board 2, Num.Ver.Rev 3000.c.0, TimeStamp 10/30/2012 16:54, DMA:Yes

HAD E1400000: M/E Board 3, Num.Ver.Rev 3000.c.0, TimeStamp 10/30/2012 16:54, DMA:Yes

HAD E0400000: M/E Board 6, Num.Ver.Rev 3000.c.0, TimeStamp 10/30/2012 16:54, DMA:Yes

HAD E2400000: Input Board 0, Num.Ver.Rev 1000.b.0, TimeStamp 08/20/2012 15:18, DMA:N/A

HAD E2800000: Input Board 1, Num.Ver.Rev 1000.b.0, TimeStamp 08/20/2012 15:18, DMA:N/A

HAD E2C00000: Input Board 2, Num.Ver.Rev 1000.b.0, TimeStamp 08/20/2012 15:18, DMA:N/A

HAD E3000000: Input Board 3, Num.Ver.Rev 1000.b.0, TimeStamp 08/20/2012 15:18, DMA:N/A

HAD E3400000: Input Board 4, Num.Ver.Rev 1000.b.0, TimeStamp 08/20/2012 15:18, DMA:N/A

HAD E1C00000: Output Board 0, Num.Ver.Rev 1800.b.1, TimeStamp 04/15/2013 09:41, DMA:Yes

HAD E3800000: Output Board 2, Num.Ver.Rev 1800.b.1, TimeStamp 04/15/2013 09:41, DMA:Yes

HAD E3C00000: Output Board 3 ,Num.Ver.Rev 1800.b.1, TimeStamp 04/15/2013 09:41, DMA:Yes

HAD E5800000: Modular I/O 6, Num.Ver.Rev 4000.b.1, TimeStamp 04/15/2013 10:19, DMA:Yes

HAD E5C00000: Modular I/O 7, Num.Ver.Rev 4000.b.1, TimeStamp 04/15/2013 10:19, DMA:Yes

HAD E1800000: Still Store 1, Num.Ver.Rev 5000.b.0, TimeStamp 05/21/2013 16:23, DMA:N/A

What’s important to note is the time stamp of like boards should match the above info. CPU board X and ME Board X (X is different between compact and standard frames) are really the controller and the ME90/Control ME that is also on the controller board. There are two different HADs on the controller board. One for the ME90/Control ME and the other is for the controller sync gen and xpoint control. All other boards start numbering at zero, not 1. What can I say, it must have been a software guy.

Even though the update failed the board should still be functional in the system. Boards that fail to update after the several attempts should be replaced.

Graphically HAD update steps:

-> consoleMenu

[1] Status [2] Controller [3] ME

[4] Input [5] Output [6] ModIO

[7] Image Store [8] Multi Viewer [9] Debug

[10] Software Options [11] Fan Controller [P] Power Supply

[H] Boot Sw Help [Z] Test Diagnostics [D] DisableAllUpdates

[T] Prod Test Setup [M] DisableAllMonitoring [x] Exit to shell

> 9

[1] GPI [2] Tally [3] MessageControl

[4] TimeThreads [5] Router Control [6] RDP/CPL Tests

[7] Show image info [8] Show DMA info [9] Feature Tests

[10] Transition [11] Switch Image DMA [12] Temp Show All

[13] Temp Adjust Parms [14] Dump HAD Info [15] Flash HADs

[16] NP Aux Control [17] Field Timing [18] CPL Msg Timing

[19] Display Re-entries [20] DebugNetwork [21] DebugStillStore

[22] Debug Event Control [x] Exit

Debug> 15

Select FLASH Type

[G] Golden Configuration Image

[M] Multi Boot Configuration Image

[x] Exit (At any time, 'x' will exit)

> m

Confirm flashing all off HAD's?

Summary:

Flash Type: MultiBoot

Is this correct? (Y or N)> y