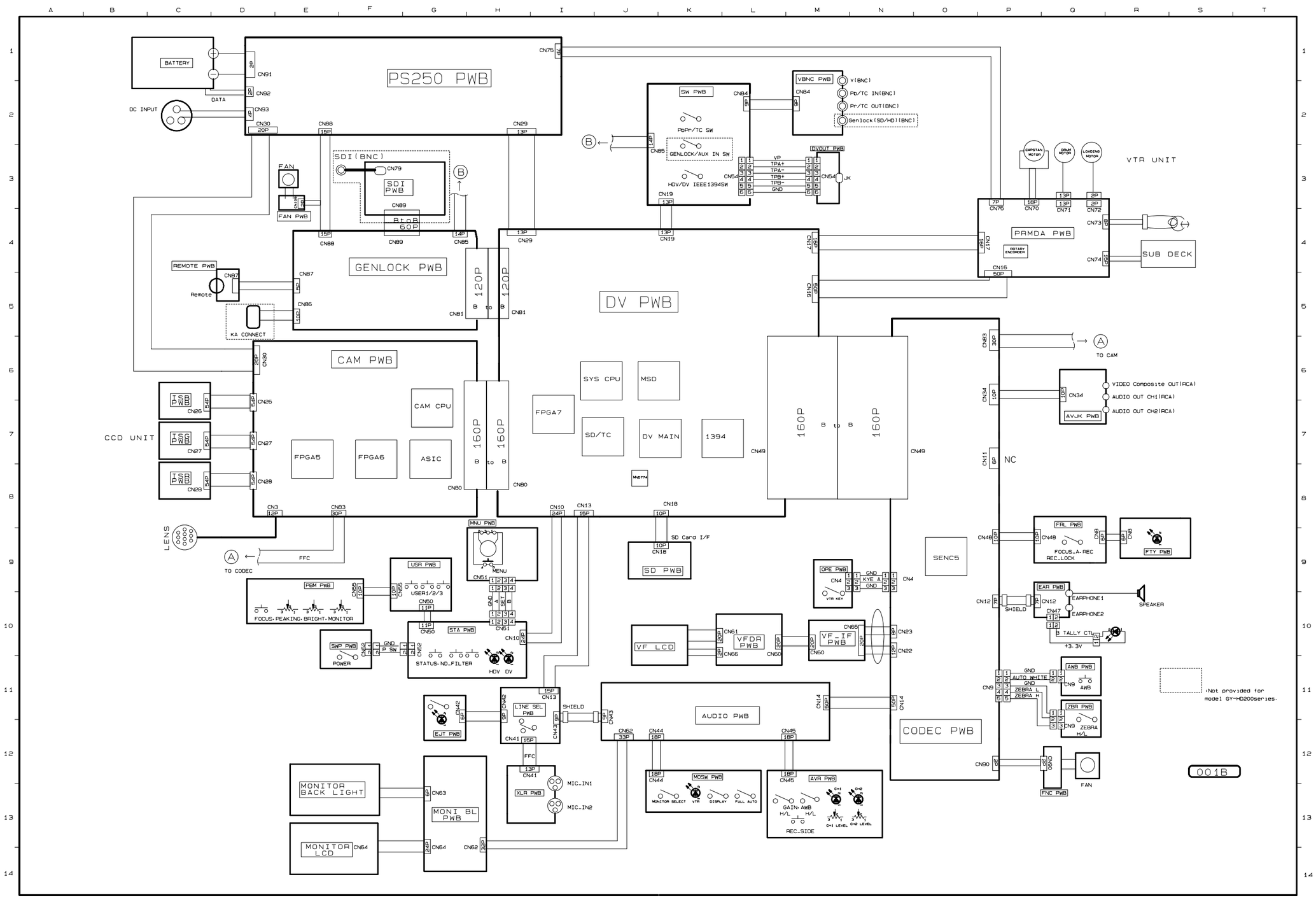
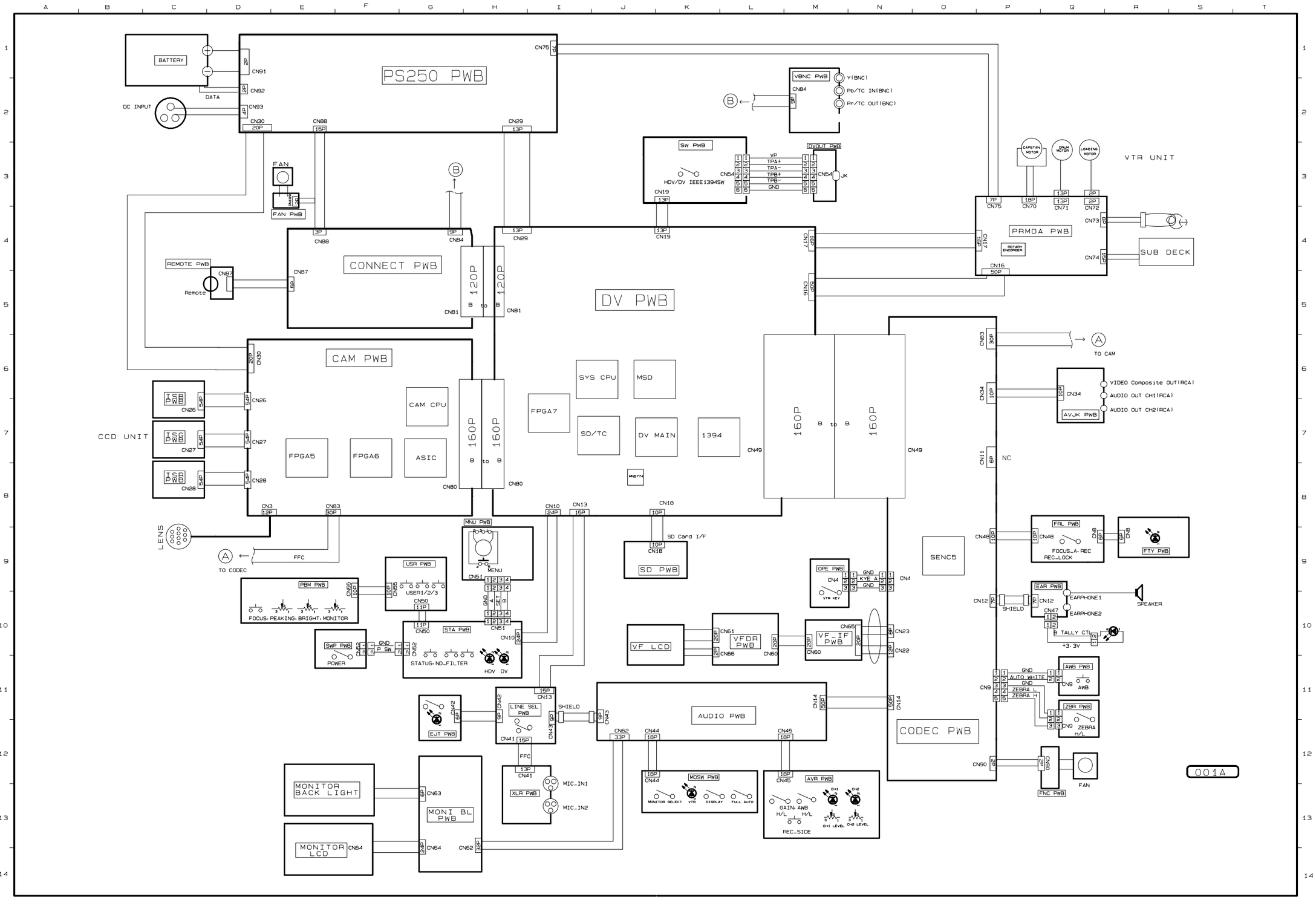
SECTION 2
DIAGRAMS AND CIRCUIT BOARDS

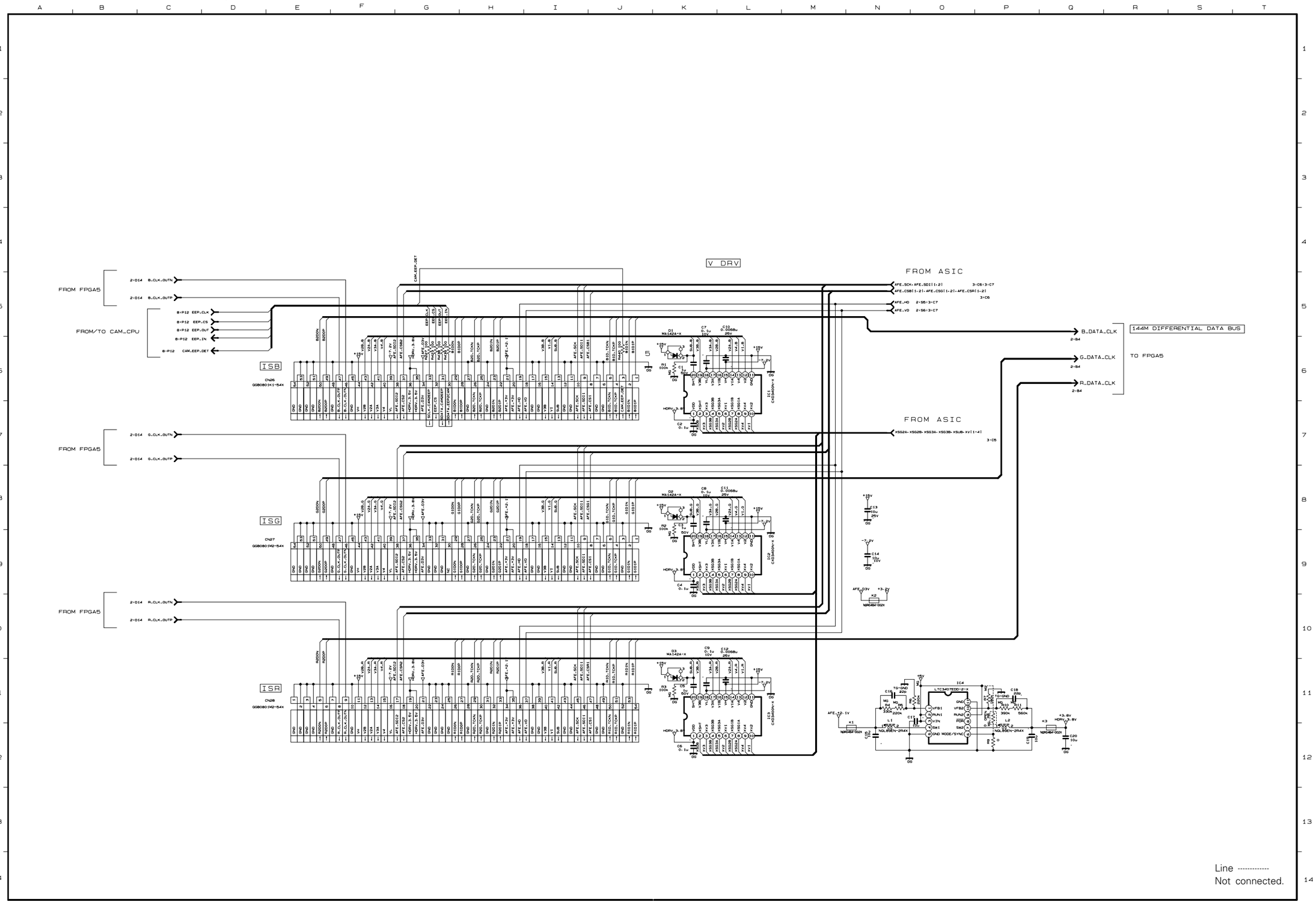
2.1 OVERALL WIRING DIAGRAM 1/2 (GY-HD200(A) STEP-1)



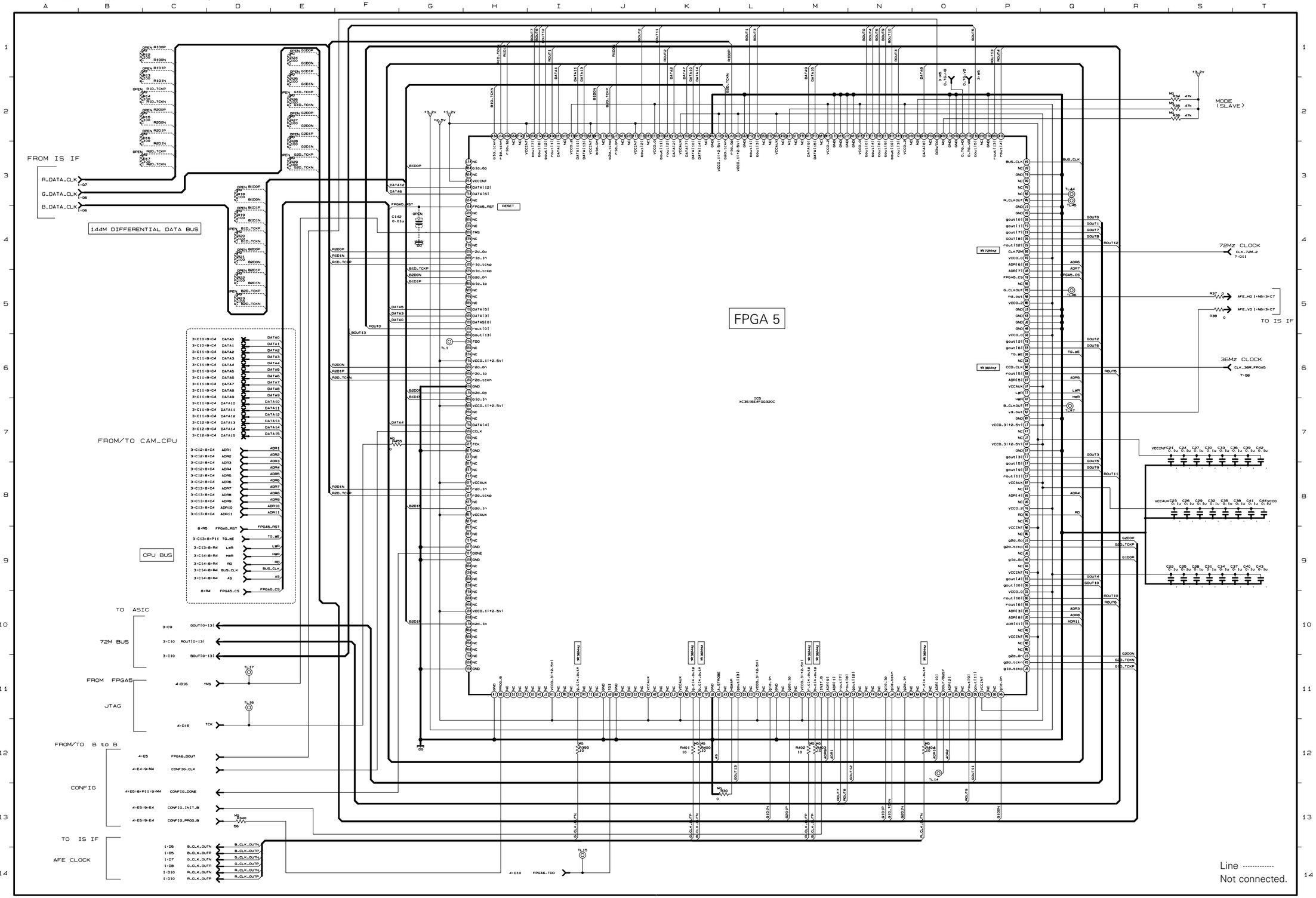
2.2 OVERALL WIRING DIAGRAM 2/2 (GY-DH200(A) STEP-2)



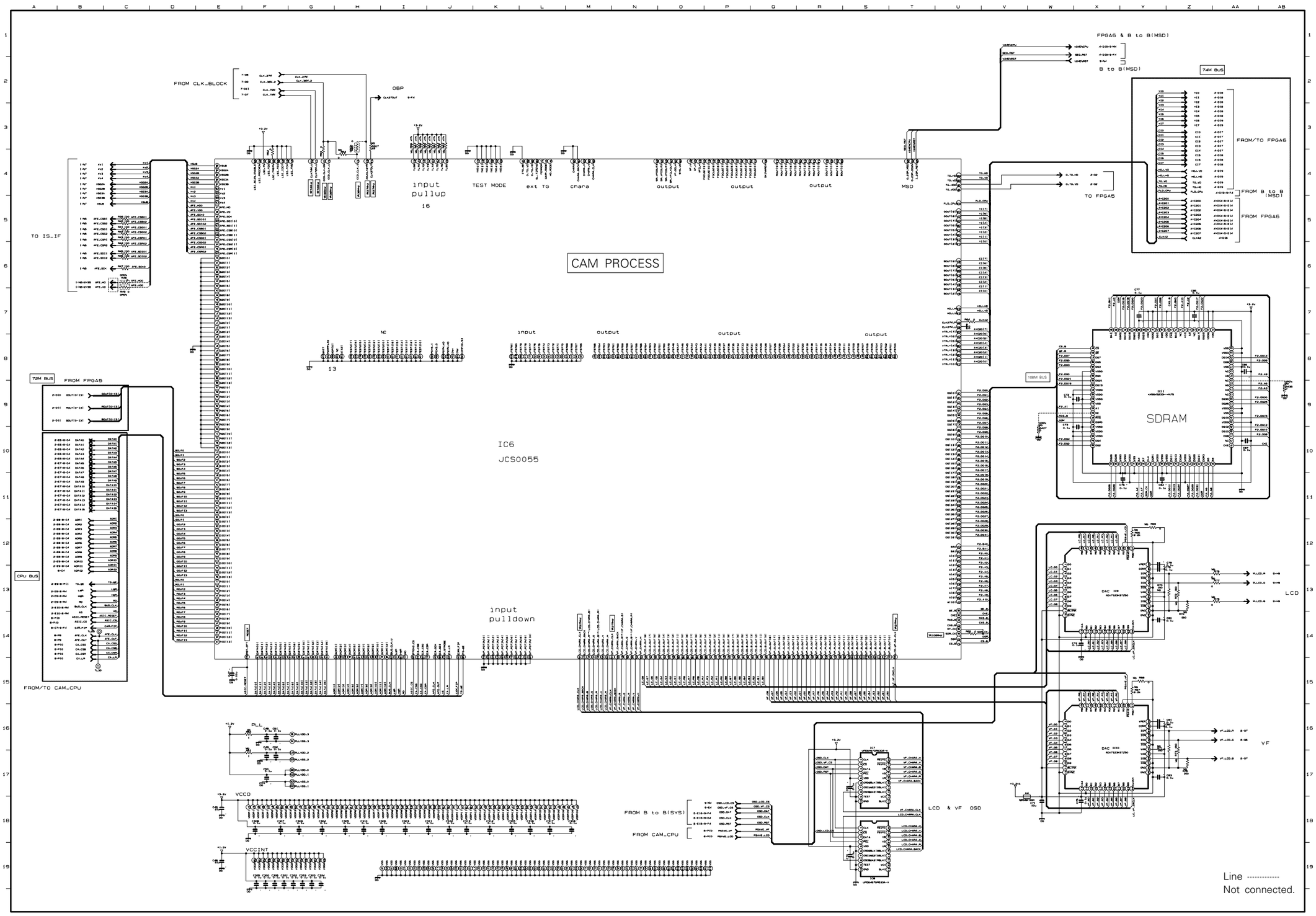
2.3 CAM SCHEMATIC DIAGRAM 11 (1/9) (IS IF)



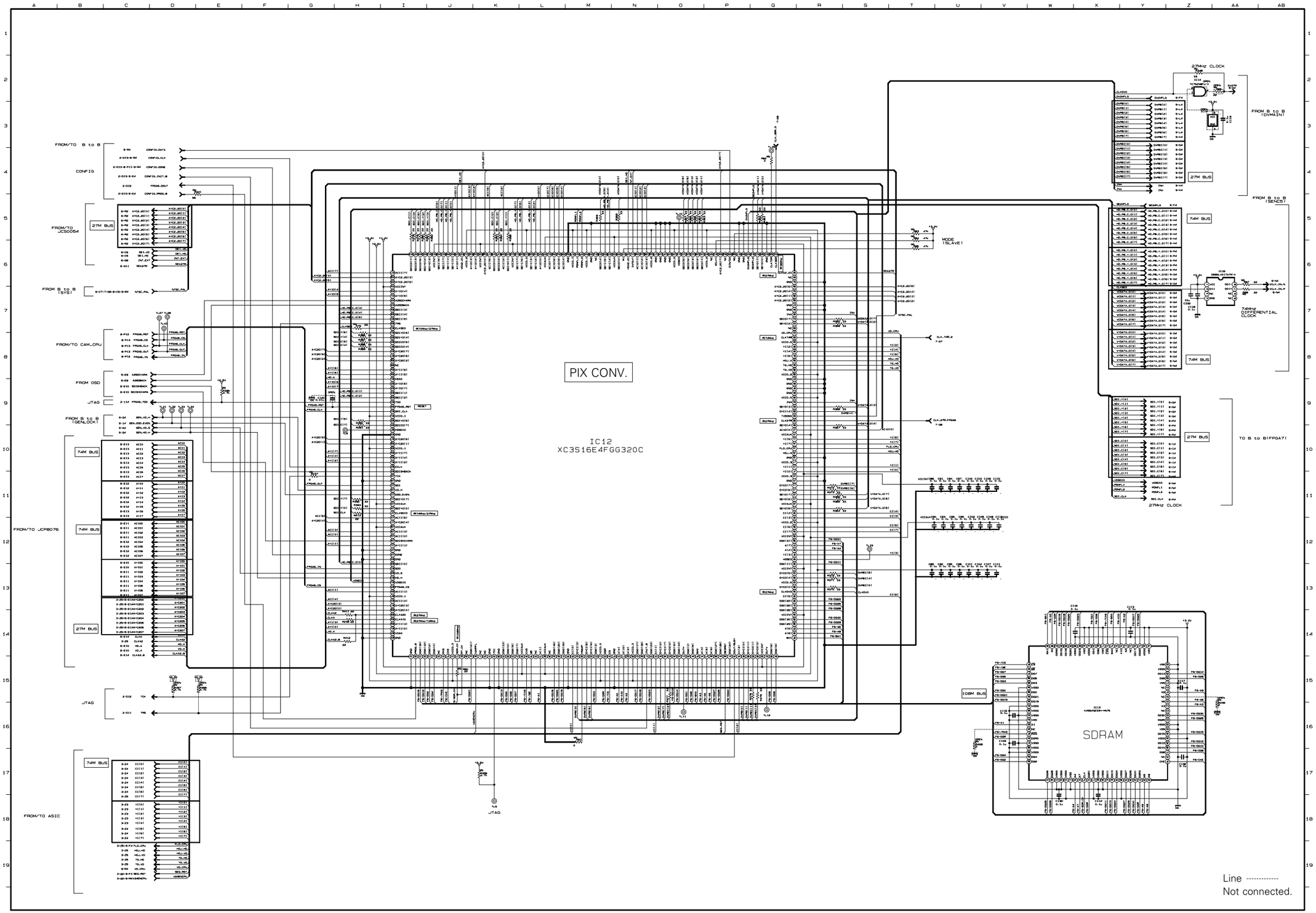
— CAM SCHEMATIC DIAGRAM 11 (2/9) (FPGA 5) —



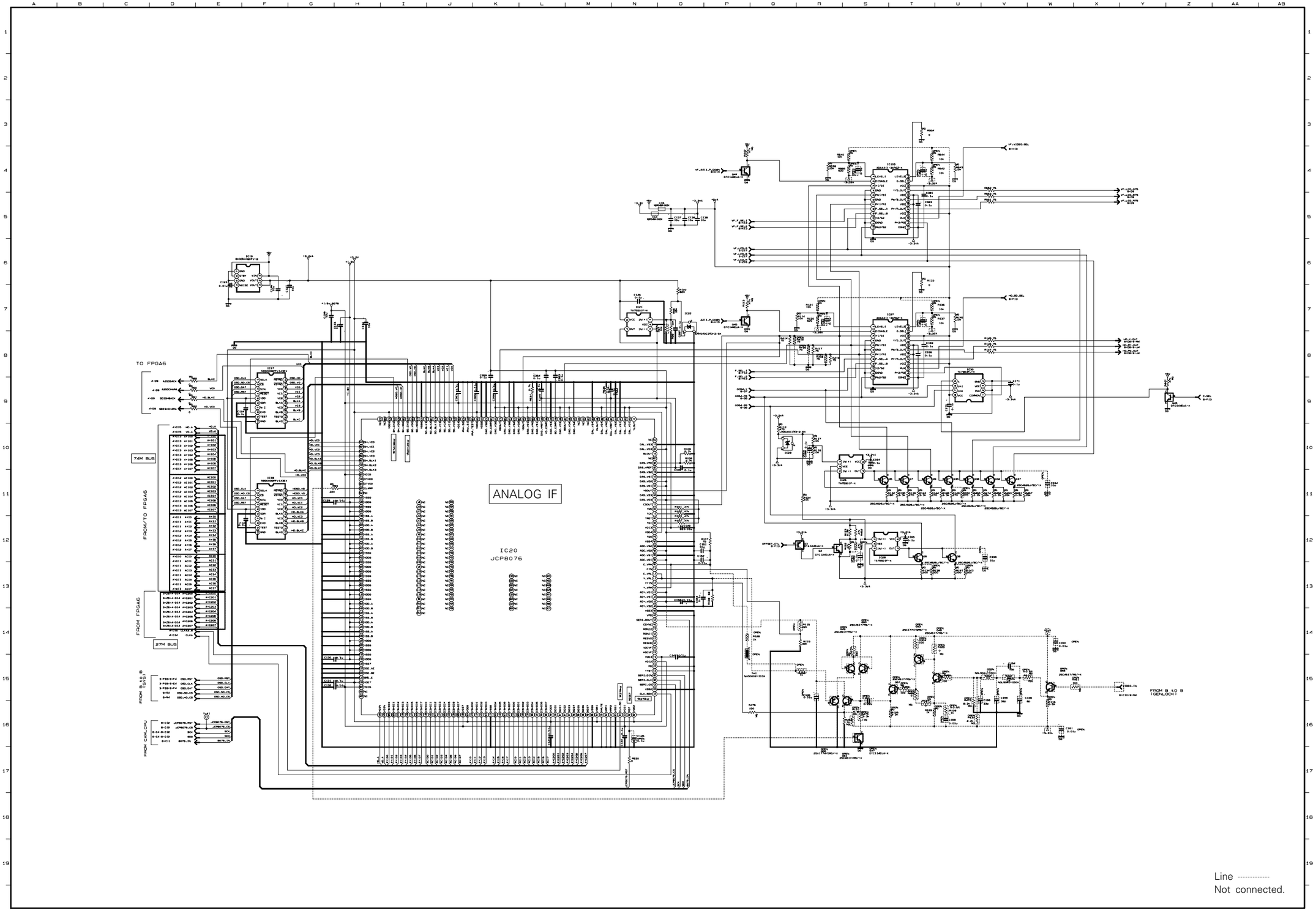
— CAM SCHEMATIC DIAGRAM 11 (3/9) (ASIC) —



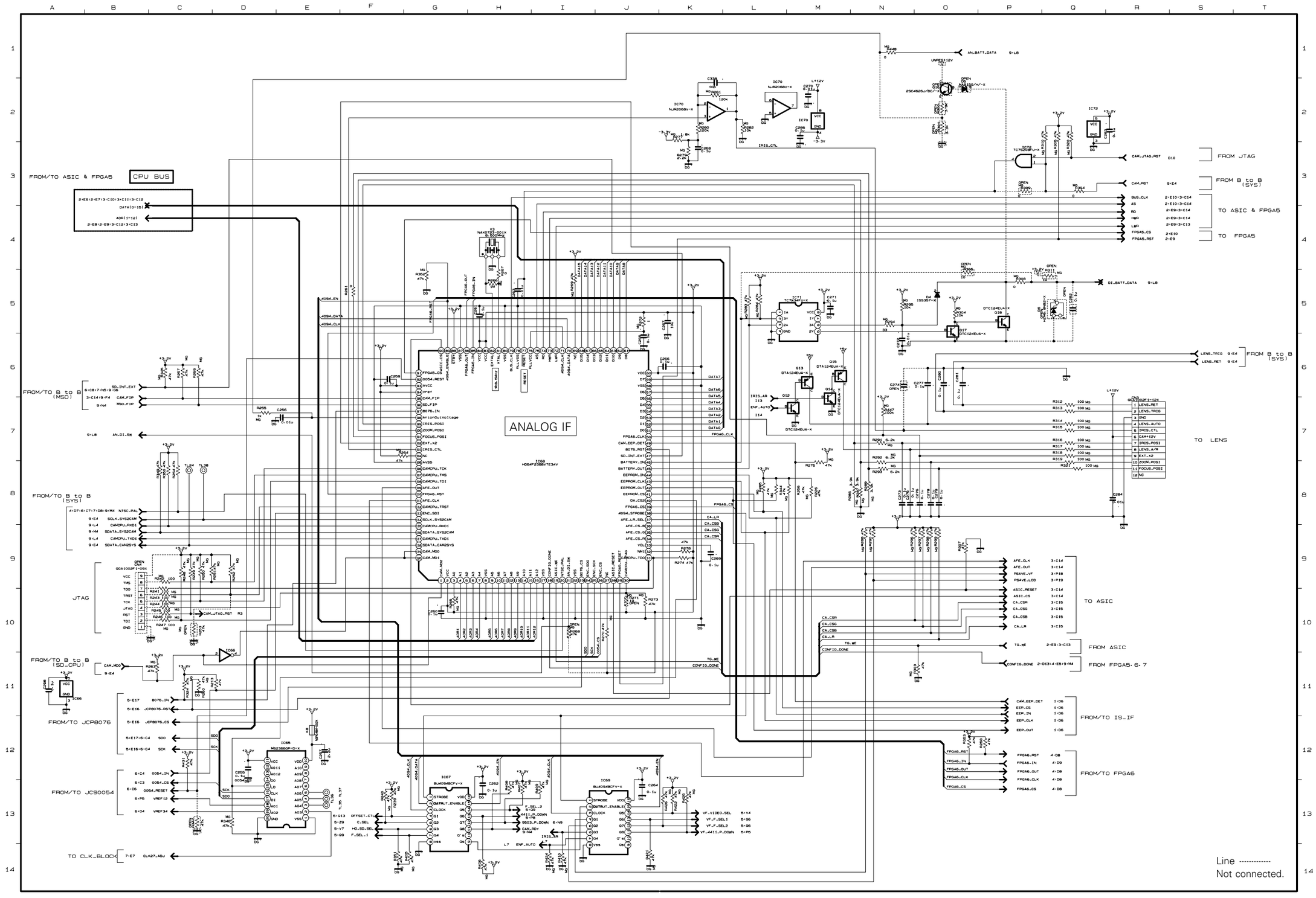
— CAM SCHEMATIC DIAGRAM 11 (4/9) (FPGA 6) —



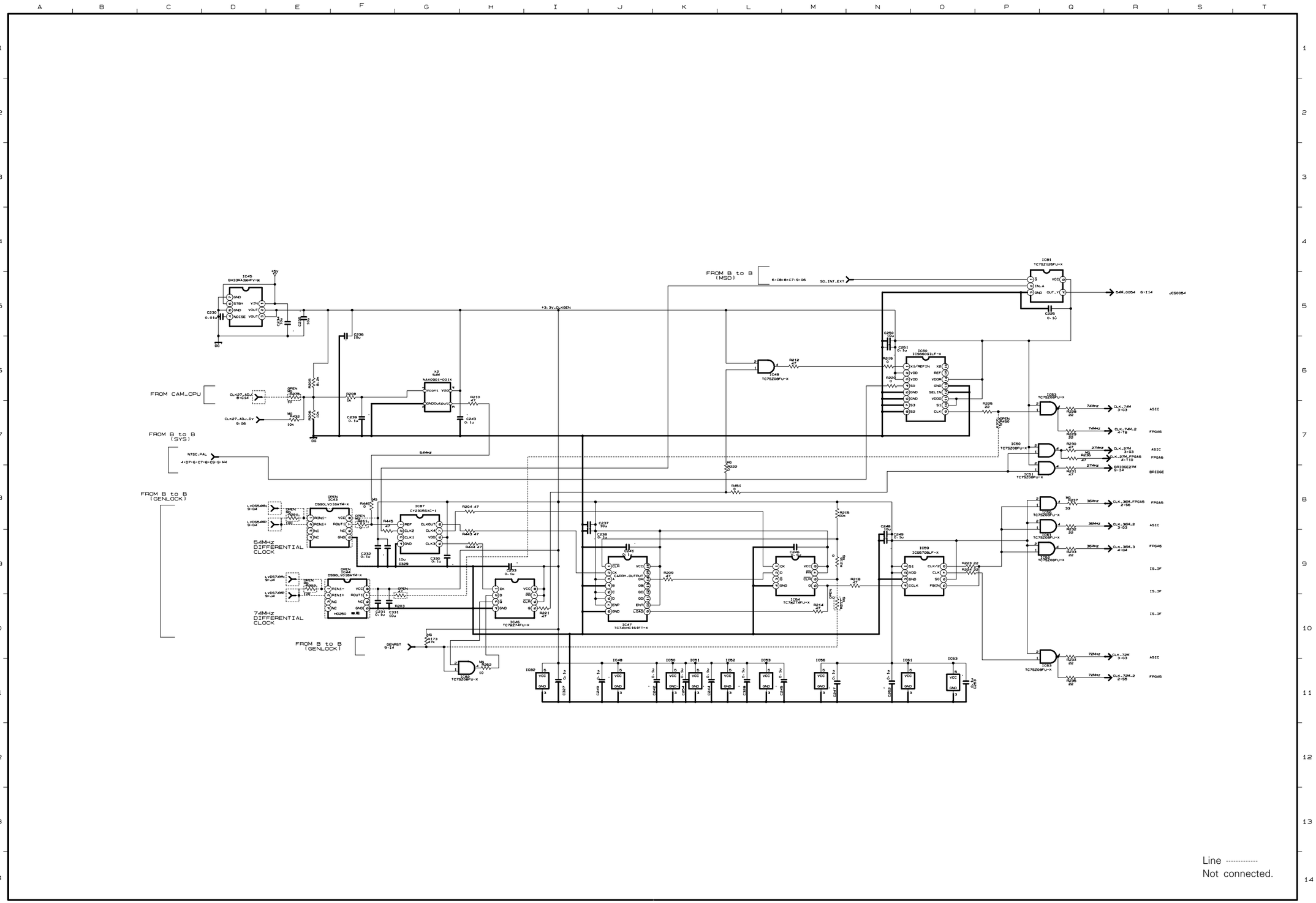
— CAM SCHEMATIC DIAGRAM 11 (5/9) (JCP8076) —



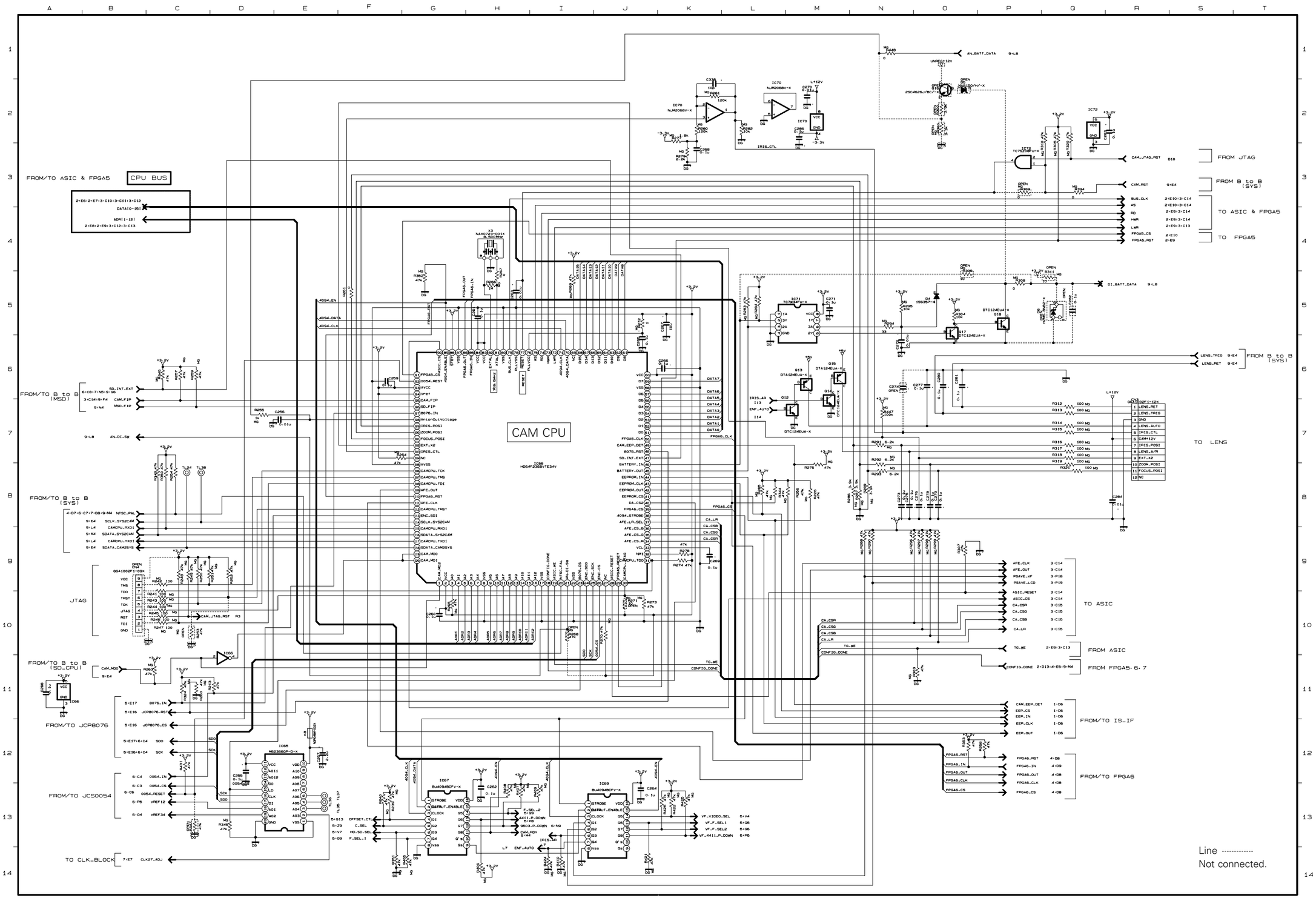
— CAM SCHEMATIC DIAGRAM 11 (6/9) (JCS0054) —



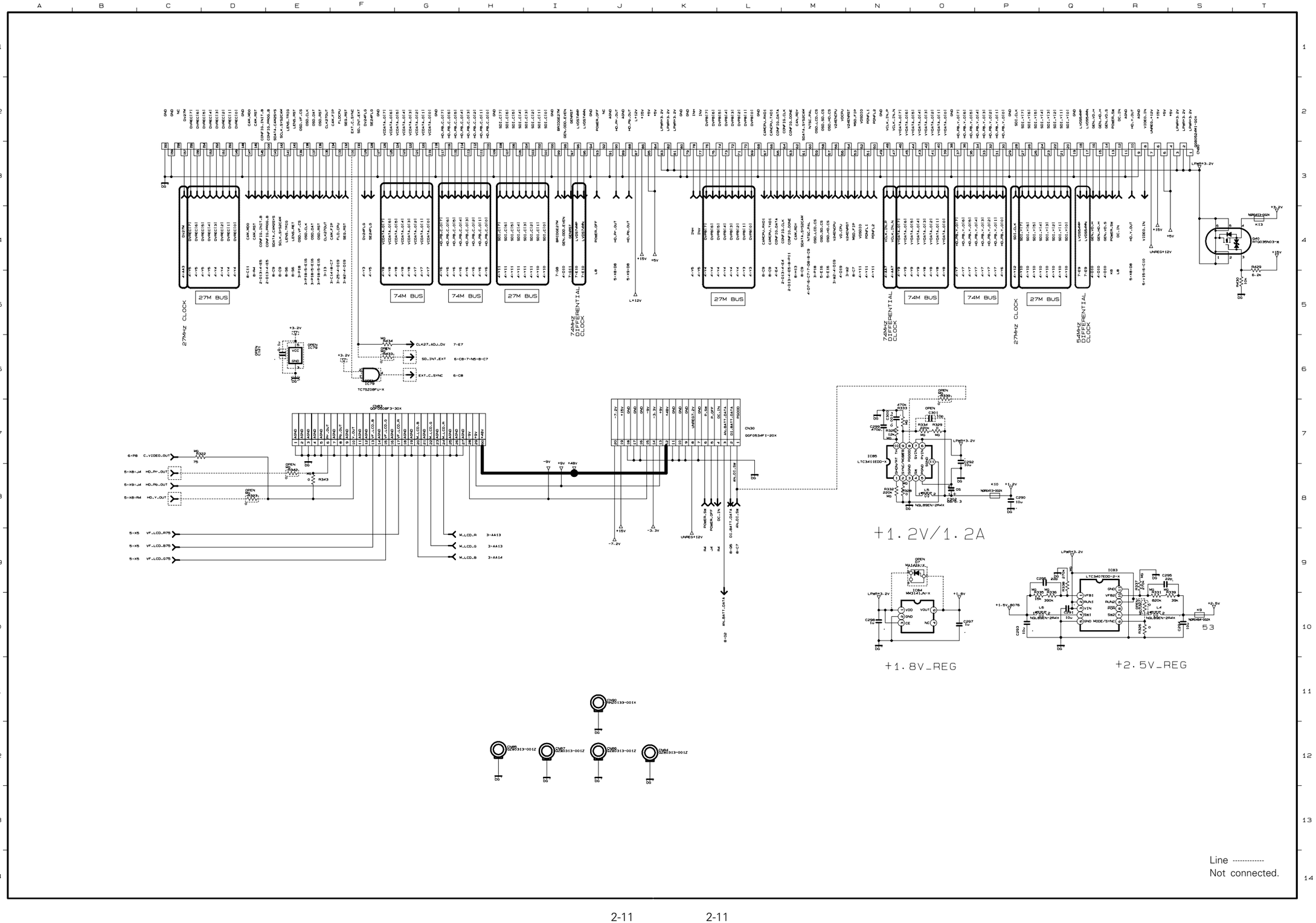
— CAM SCHEMATIC DIAGRAM 11 (7/9) (CLK BLOCK) —



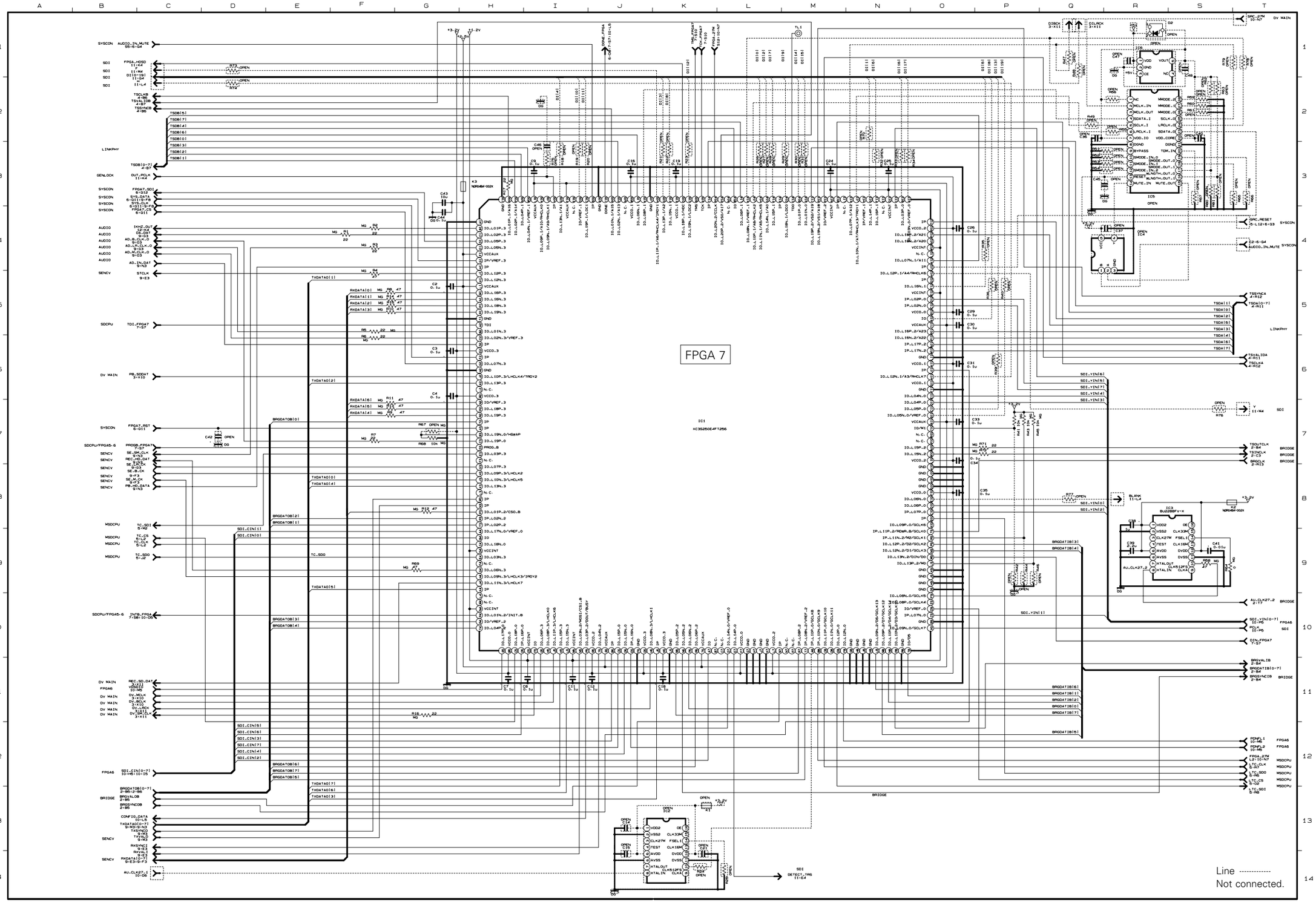
— CAM SCHEMATIC DIAGRAM 11 (8/9) (CAM CPU) —



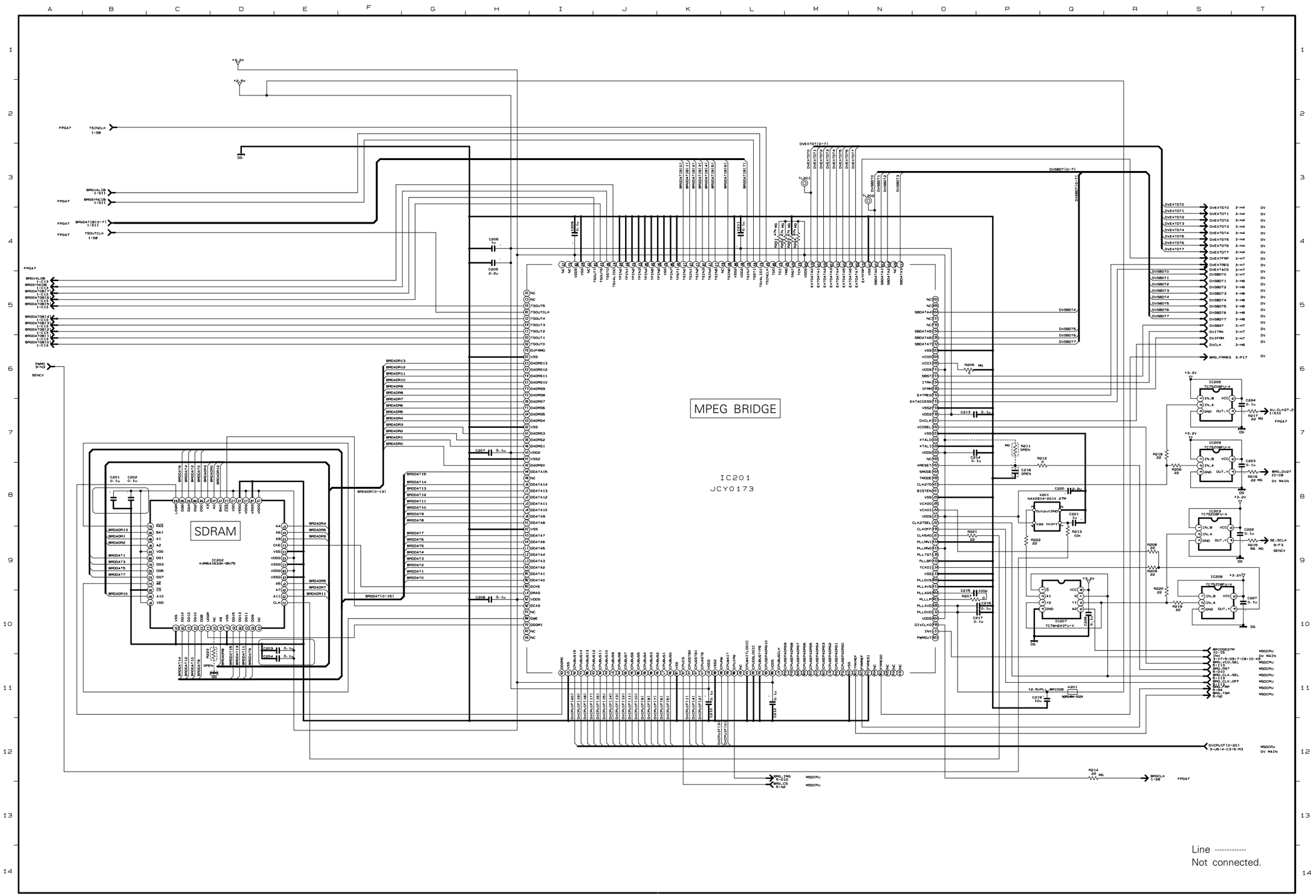
— CAM SCHEMATIC DIAGRAM 11 (9/9) (B TO POW) —



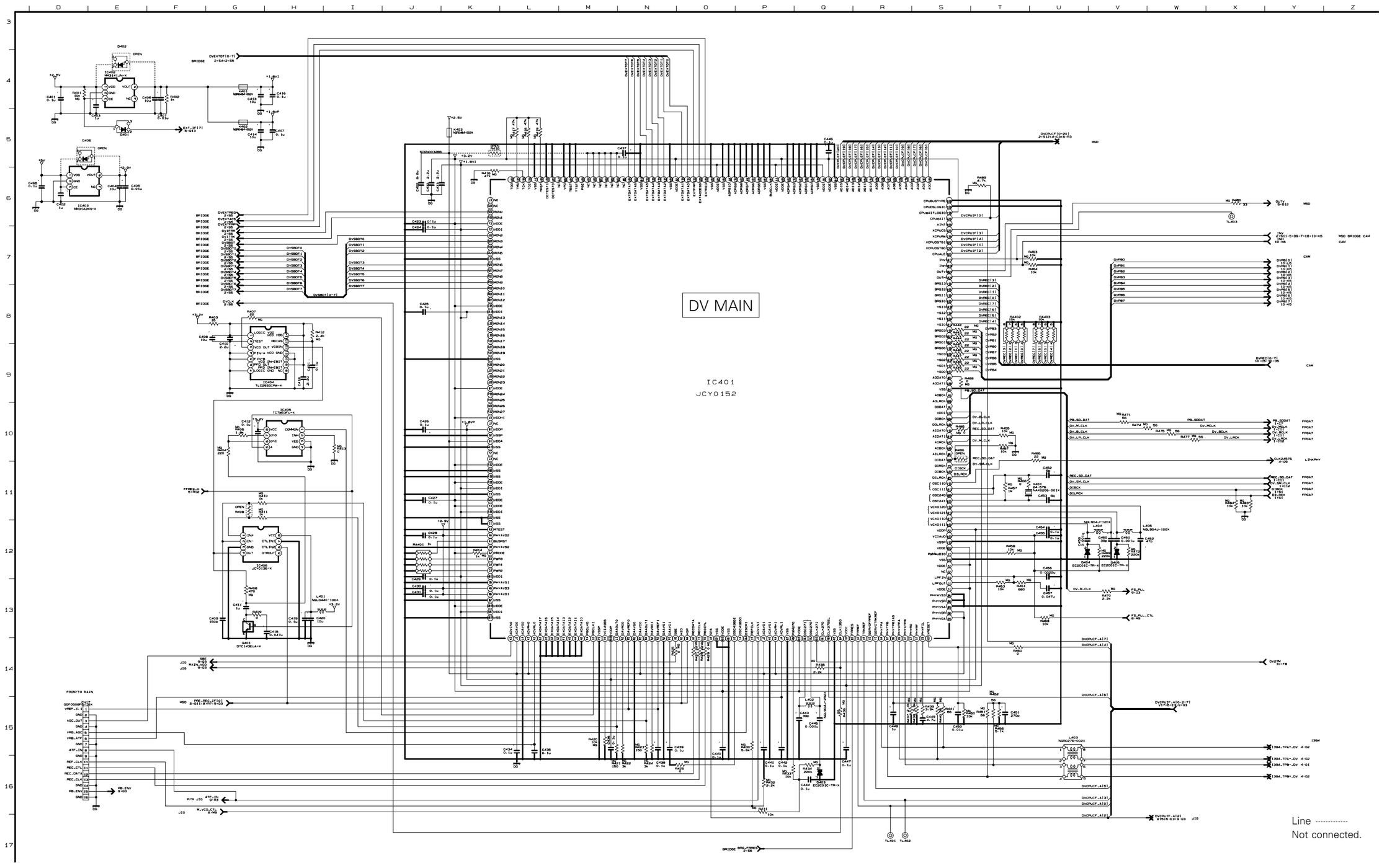
2.4 DV SCHEMATIC DIAGRAM 1 2 (1/12) (FPGA 7)



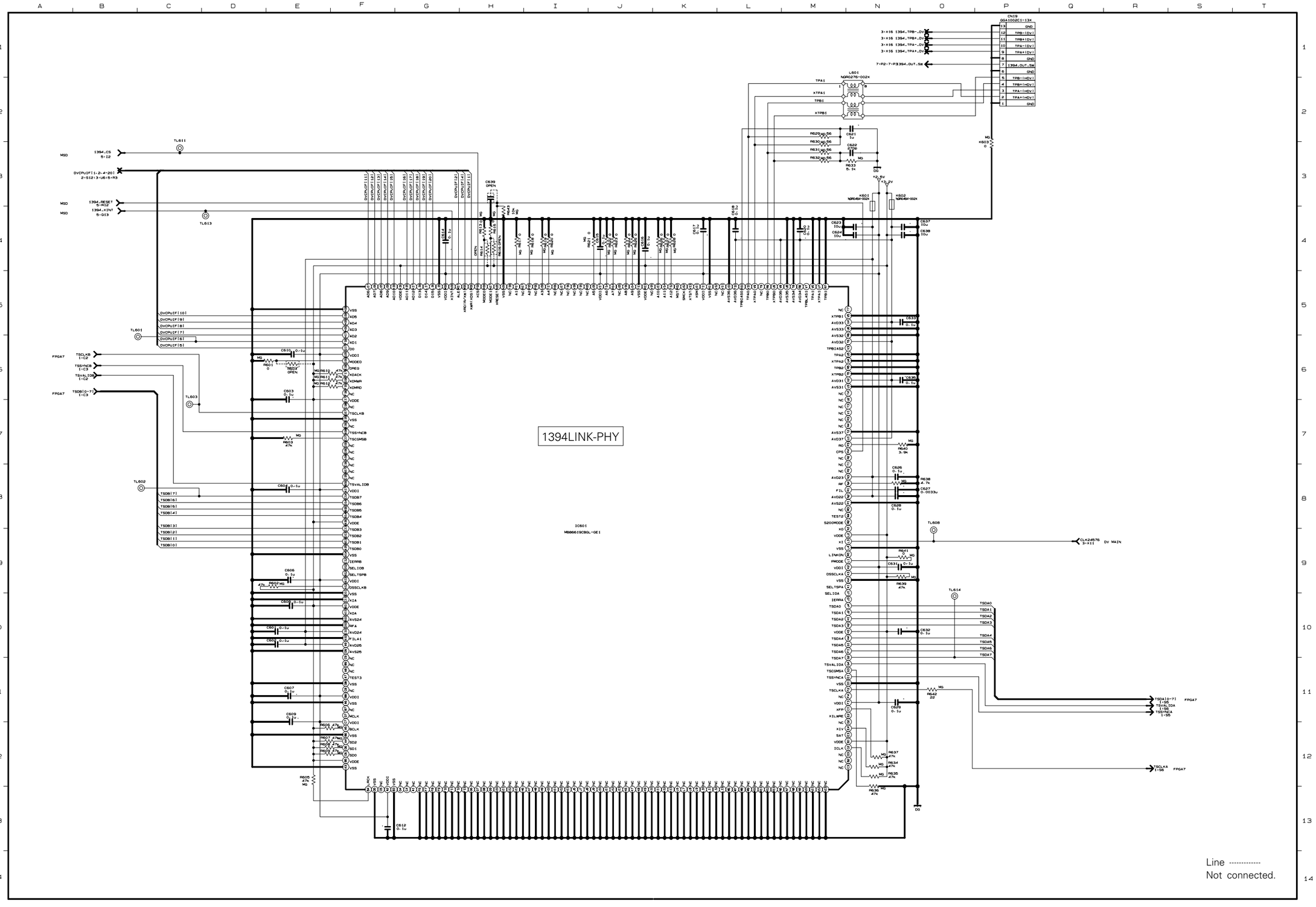
— DV SCHEMATIC DIAGRAM 12 (2/12) (BRIDGE) —



— DV SCHEMATIC DIAGRAM 1 2 (3/12) (DV MAIN) —

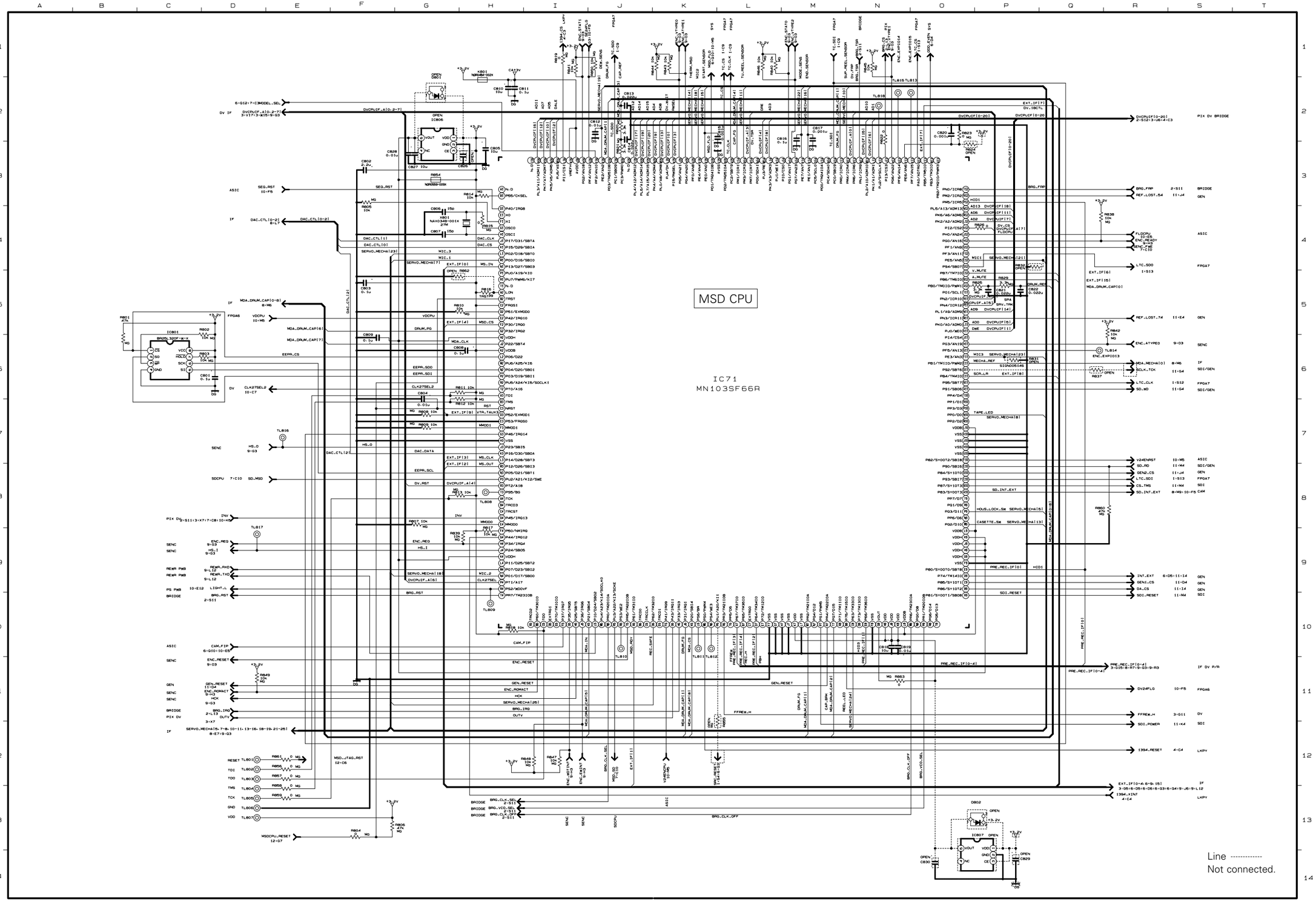


— DV SCHEMATIC DIAGRAM 12 (4/12) (1394LINK-PHY) —

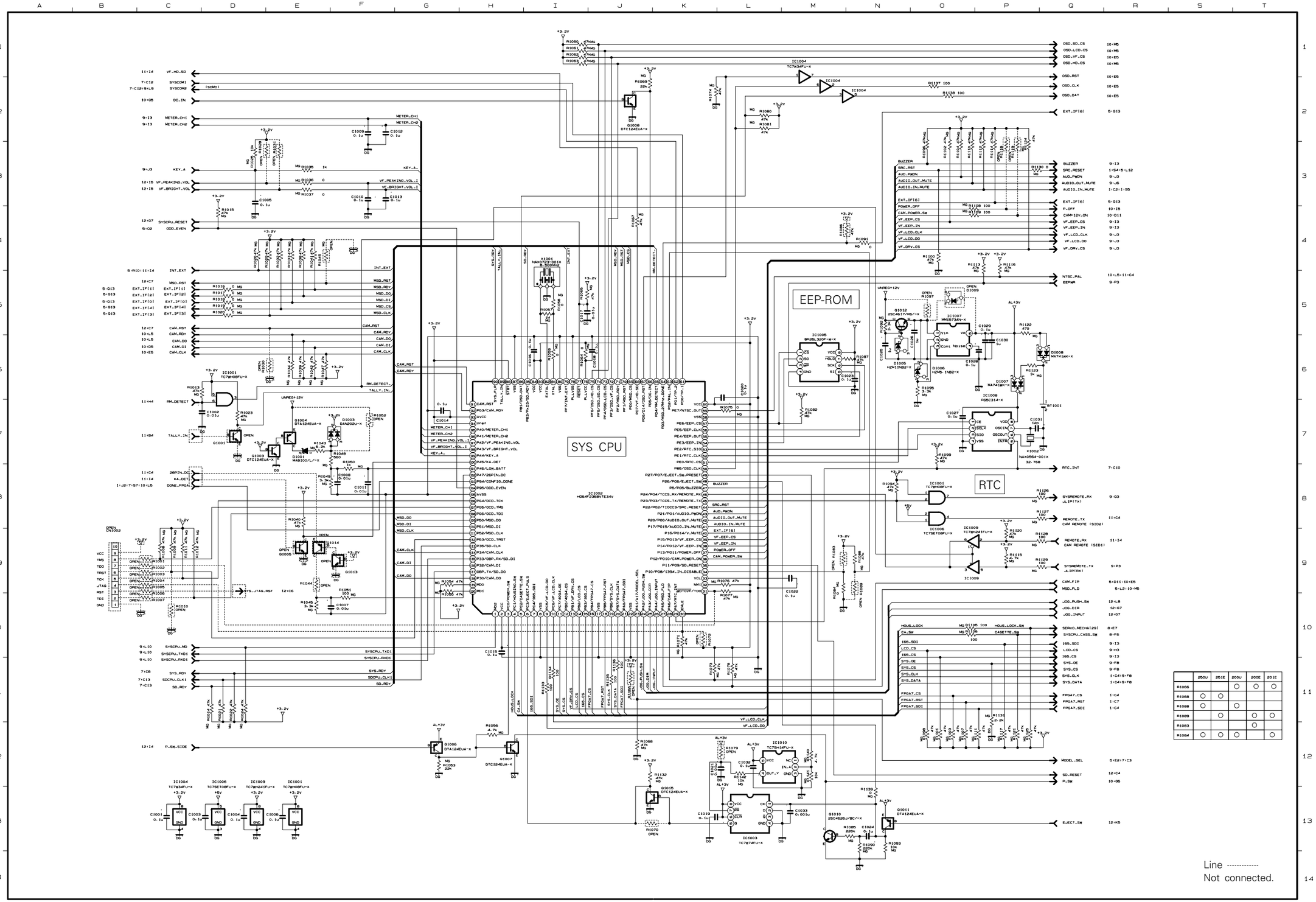


Line ----------
Not connected.

— DV SCHEMATIC DIAGRAM 1 2 (5/12) (MSD CPU) —

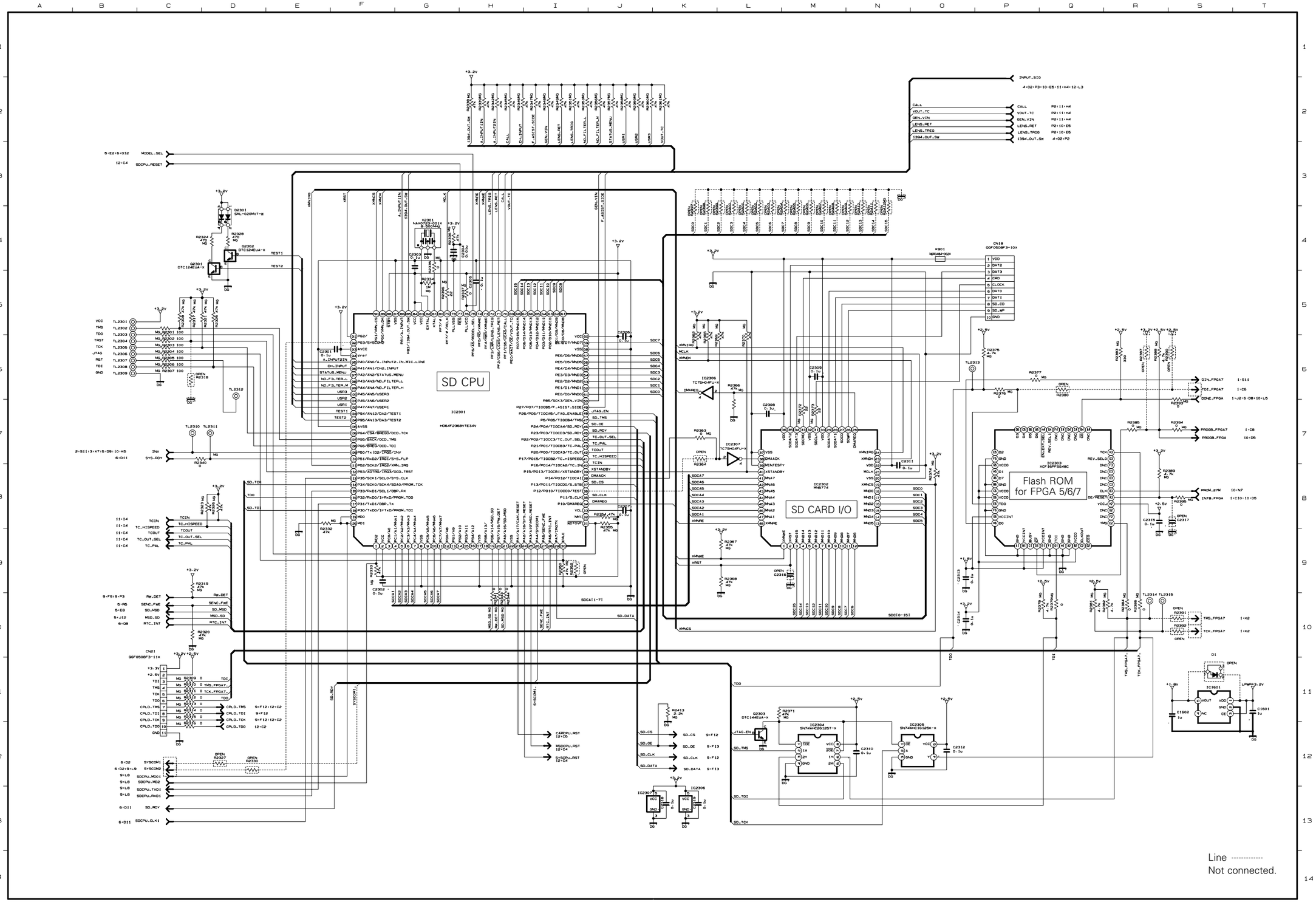


— DV SCHEMATIC DIAGRAM 12 (6/12) (SYS CPU) —

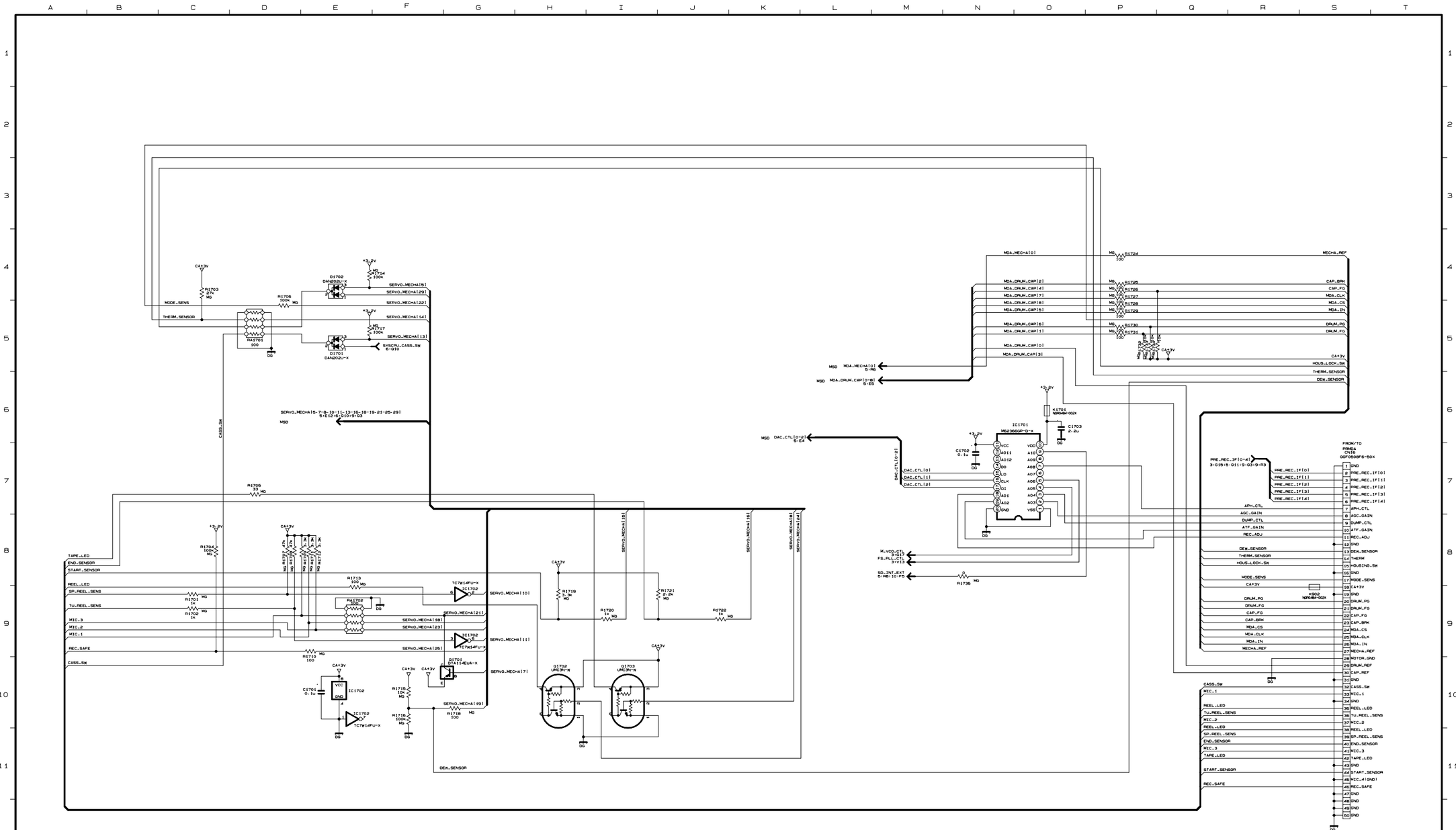


	250U	251E	200U	200E	201E
R1066			○	○	○
R1068	○	○			
R1088	○		○		
R1089		○		○	○
R1083				○	
R1084	○	○	○		○

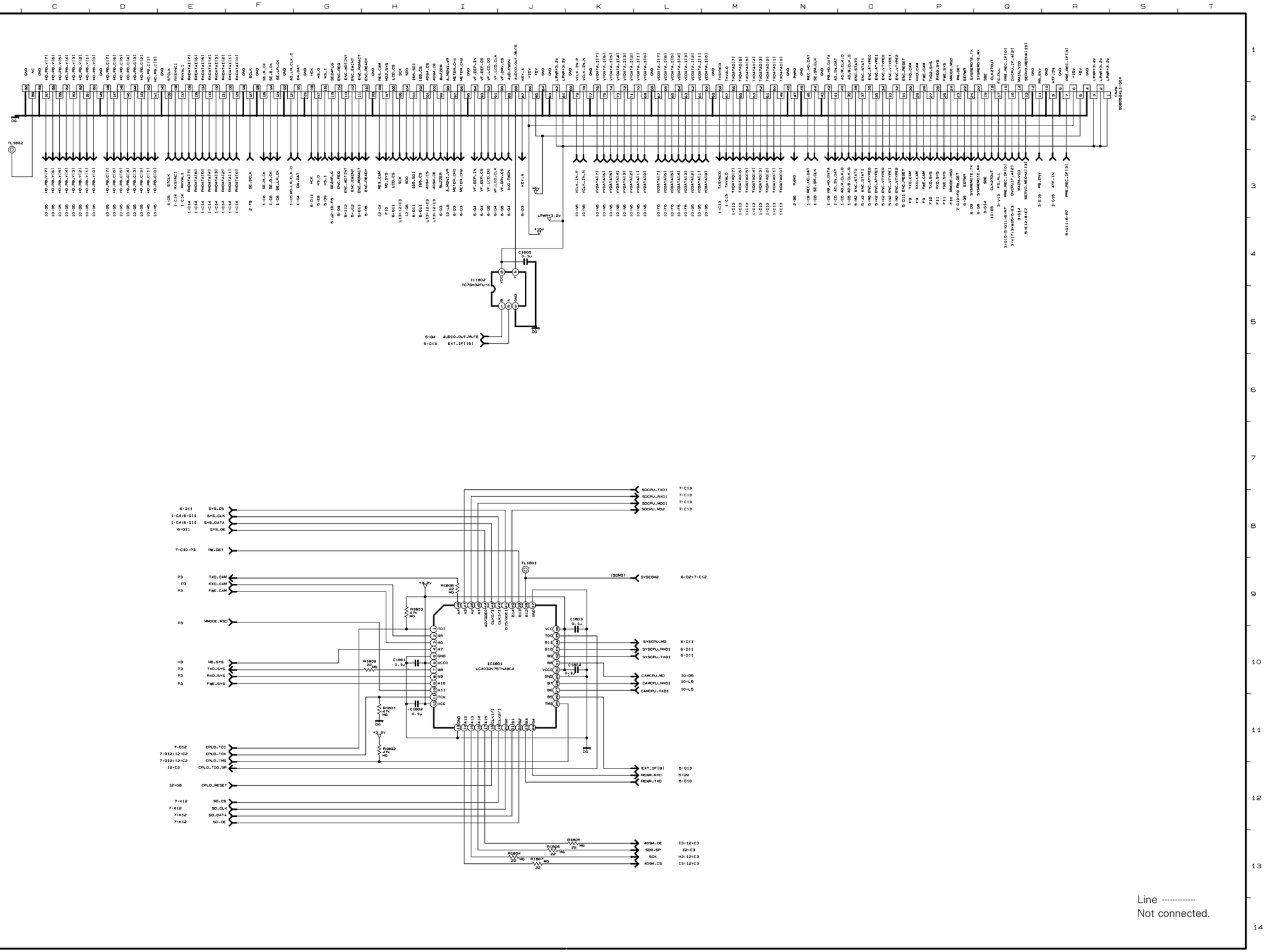
— DV SCHEMATIC DIAGRAM 1 2 (7/12) (SD CPU) —



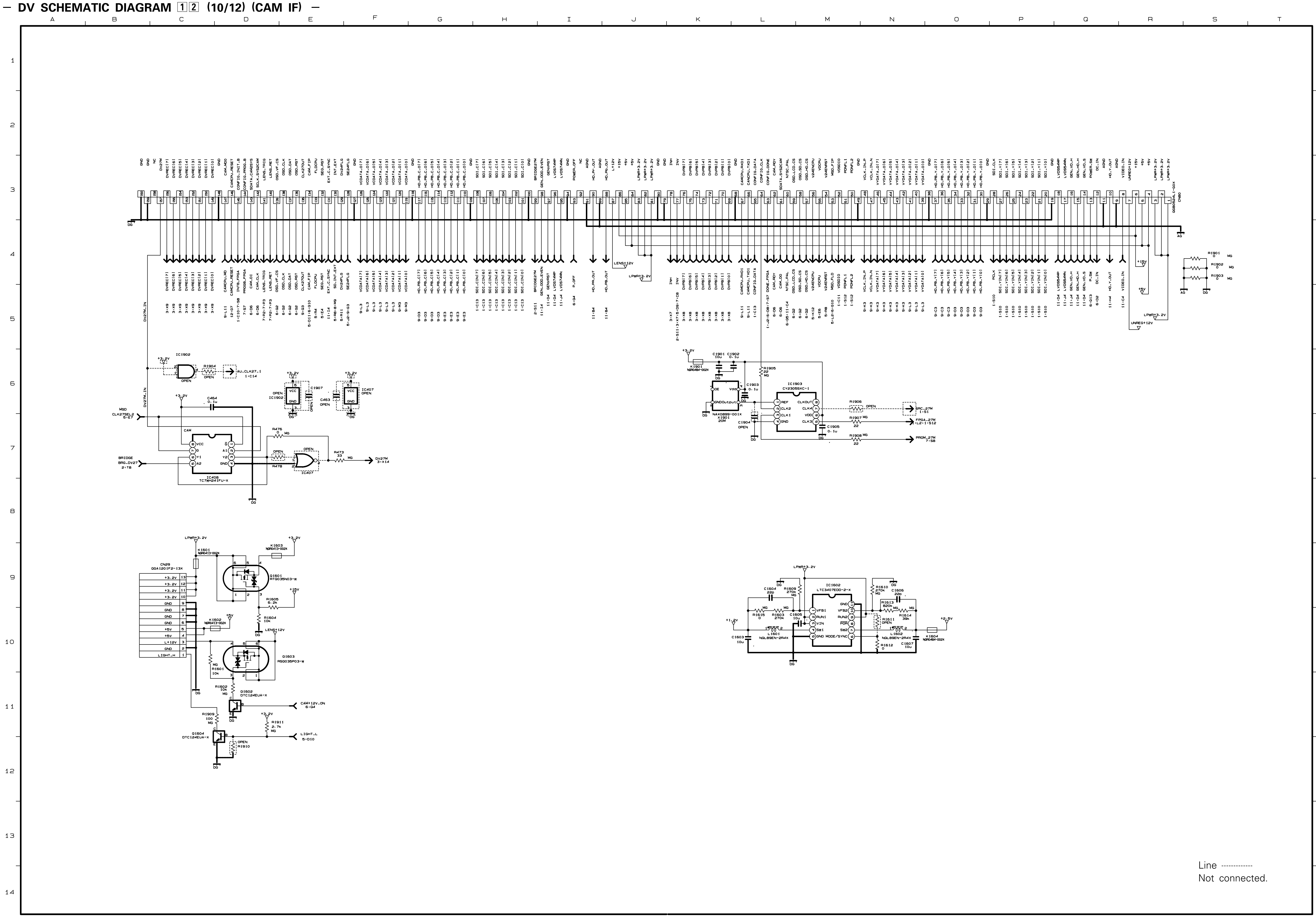
— DV SCHEMATIC DIAGRAM 1 2 (8/12) (MDA IF) —



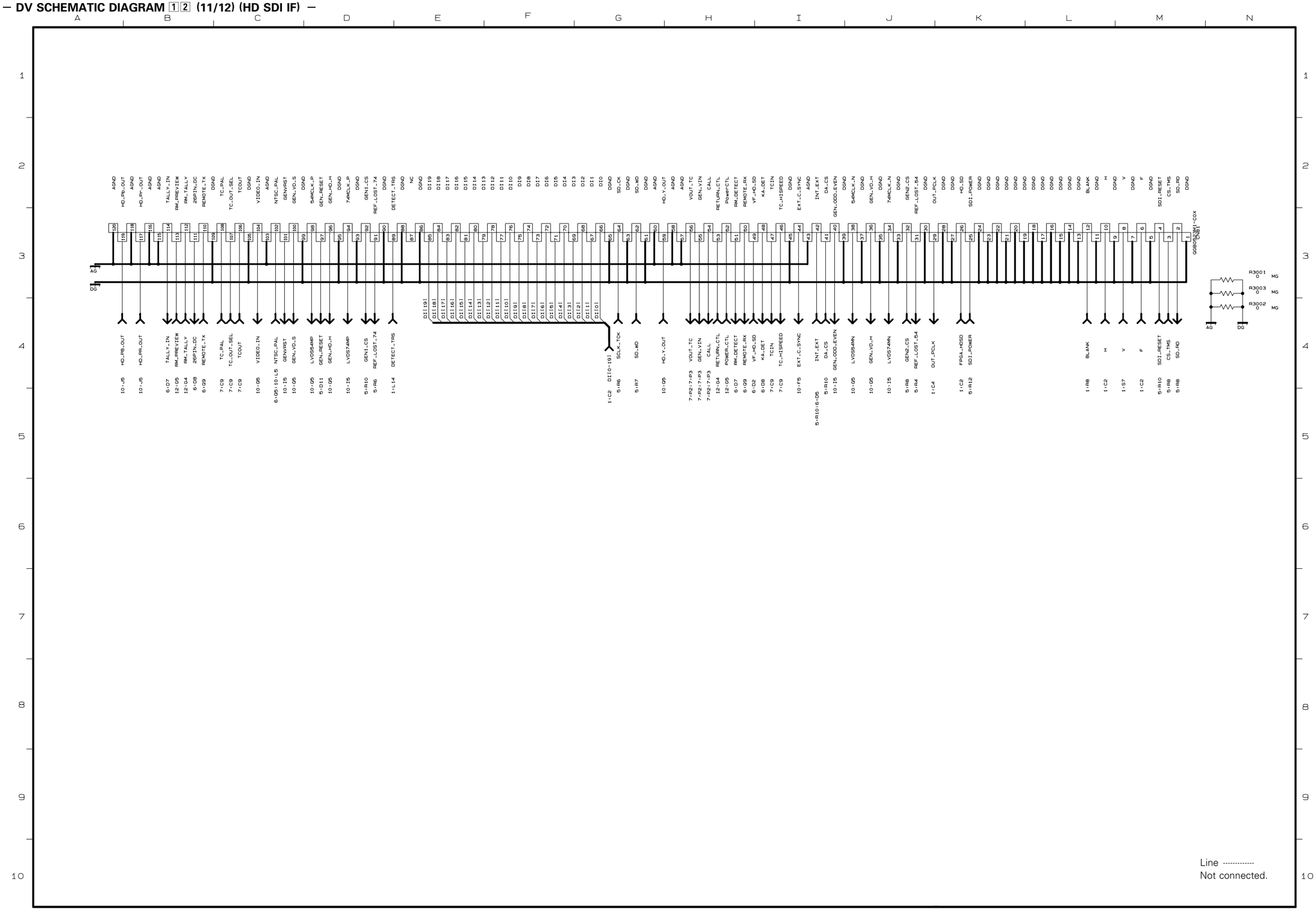
— DV SCHEMATIC DIAGRAM 12 (9/12) (CODEC IF) —

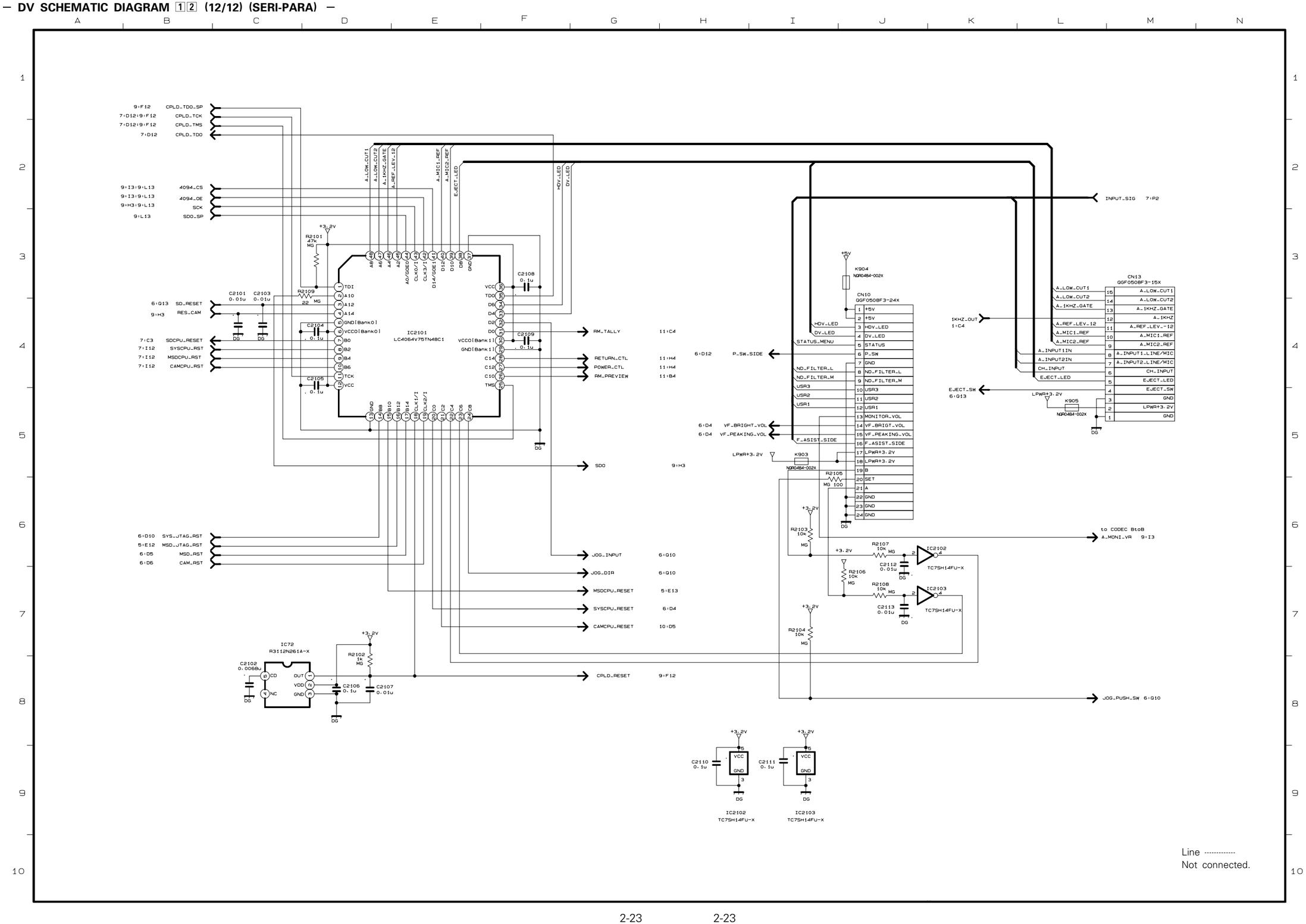


Line ----------
Not connected.

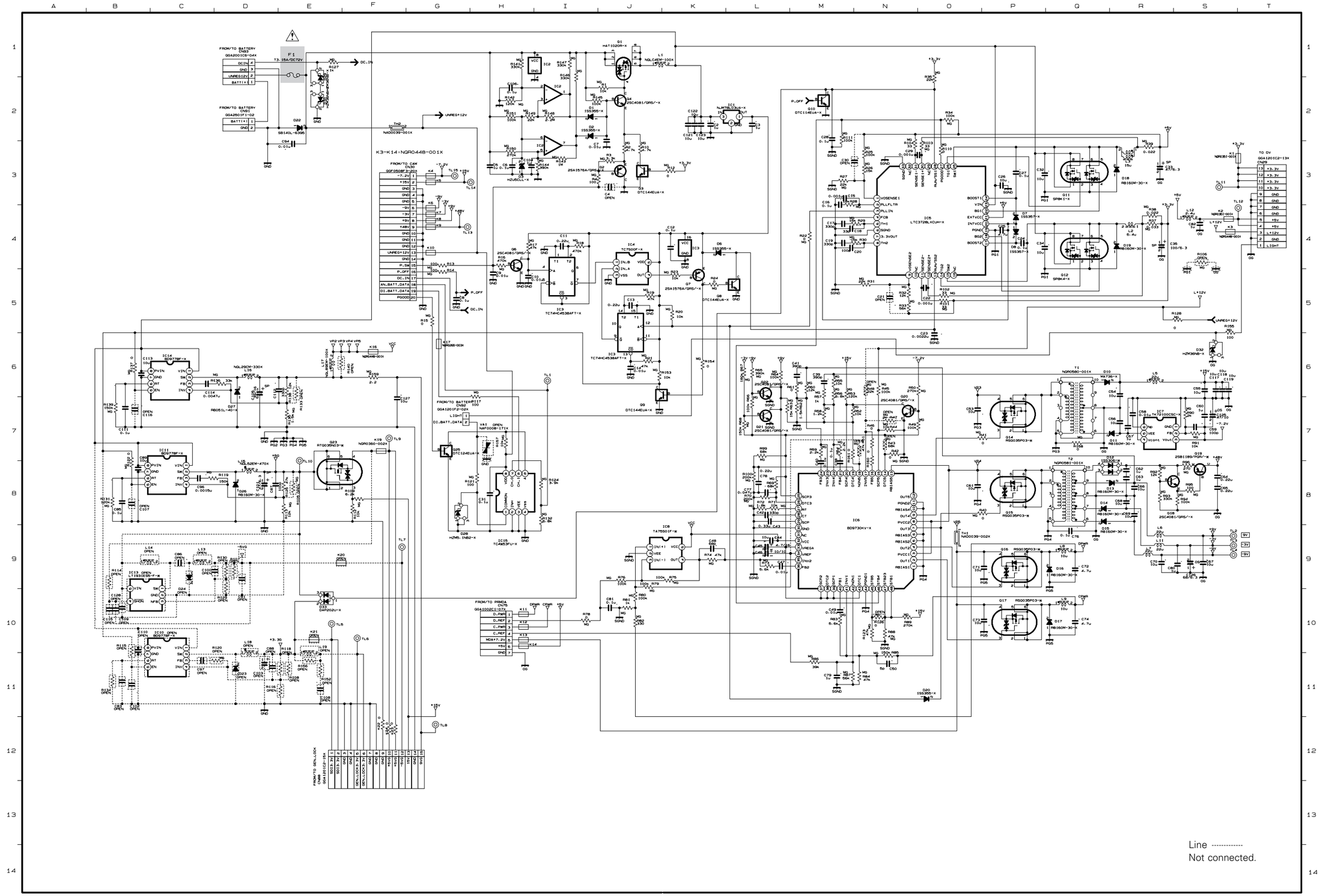

Line ------------
Not connected.

— DV SCHEMATIC DIAGRAM 12 (11/12) (HD SDI IF) —



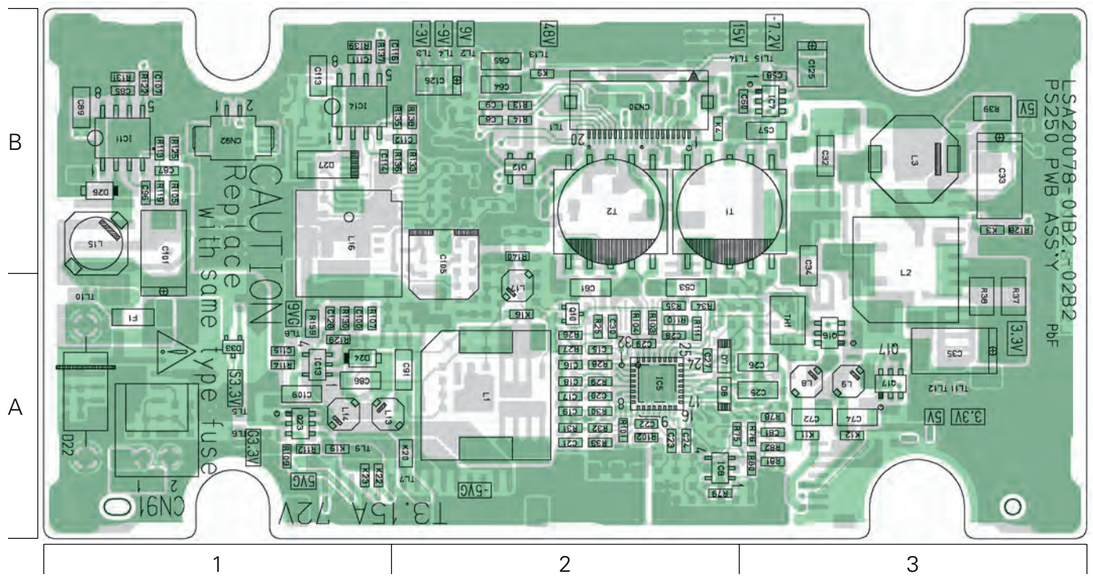

CPLD_TDO_SP
CPLD_TCK
CPLD_TMS
CPLD_TDO
4094_CS
4094_OE
SCK
SDO_SP
A_LOW_CUT1
A_LOW_CUT2
A_1KHZ_GATE
A_REF_LEV_12
A_MIC1_REF
A_MIC2_REF
EJECT_LED
HDV_LED
DV_LED
INPUT_SIG 7:P2
IC2101
LC4064V75TN48C1
SD_RESET
RES_CAM
SDCPU_RESET
SYSCPU_RST
MSDCPU_RST
CAMCPU_RST
RM_TALLY
RETURN_CTL
POWER_CTL
RM_PREVIEW
SDO
JOG_INPUT
JOG_DIR
MSDCPU_RESET
SYSCPU_RESET
CAMCPU_RESET
CPLD_RESET
SYS_JTAG_RST
MSD_JTAG_RST
MSD_RST
CAM_RST
K904
NQR0484-002X
CN10
QGF0508F3-24X
P_SW_SIDE
STATUS_MENU
ND_FILTER_L
ND_FILTER_M
USR3
USR2
USR1
MONITOR_VOL
VF_BRIGHT_VOL
VF_PEAKING_VOL
F_ASIST_SIDE
K903
K905
R2105
MG 100
CN13
QGF0508F3-15X
1KHZ_OUT
EJECT_SW
LPWR+3.2V
to CODEC BtoB
A_MONI_VR 9:I3
JOG_PUSH_SW 6:Q10
IC72
R3112N261A-X
IC2102
TC7SH14FU-X
IC2103
TC7SH14FU-X
Line ----------
Not connected.

2.5 PS250 SCHEMATIC DIAGRAM 2 1

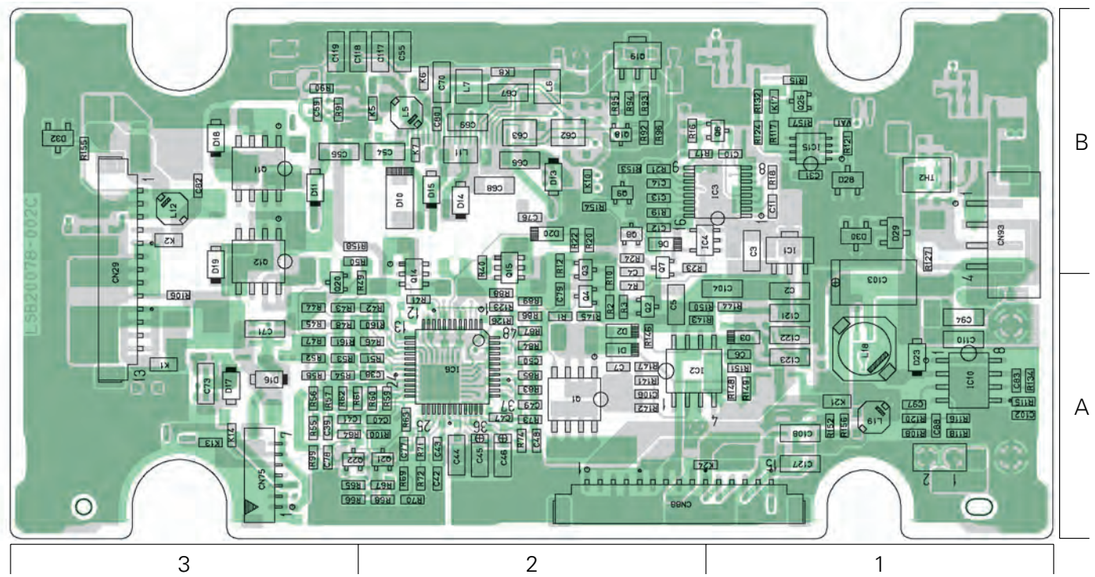


2.6 PS250 CIRCUIT BOARD

— SIDE A —



— SIDE B —



● **ADDRESS TABLE OF BOARD PARTS**

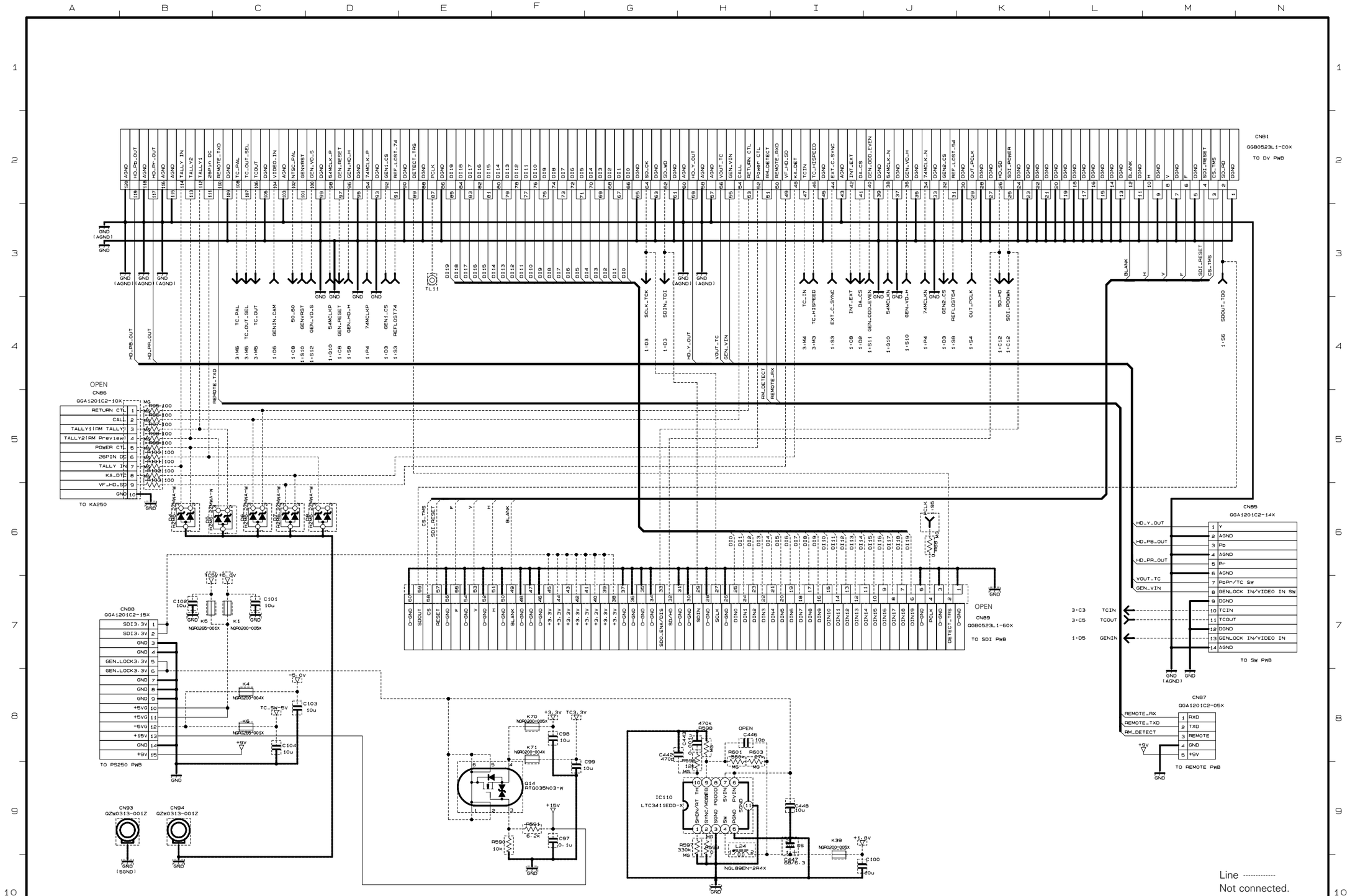
Each address may have an address error by one interval.

Side — A-1C (Y axis, X axis)

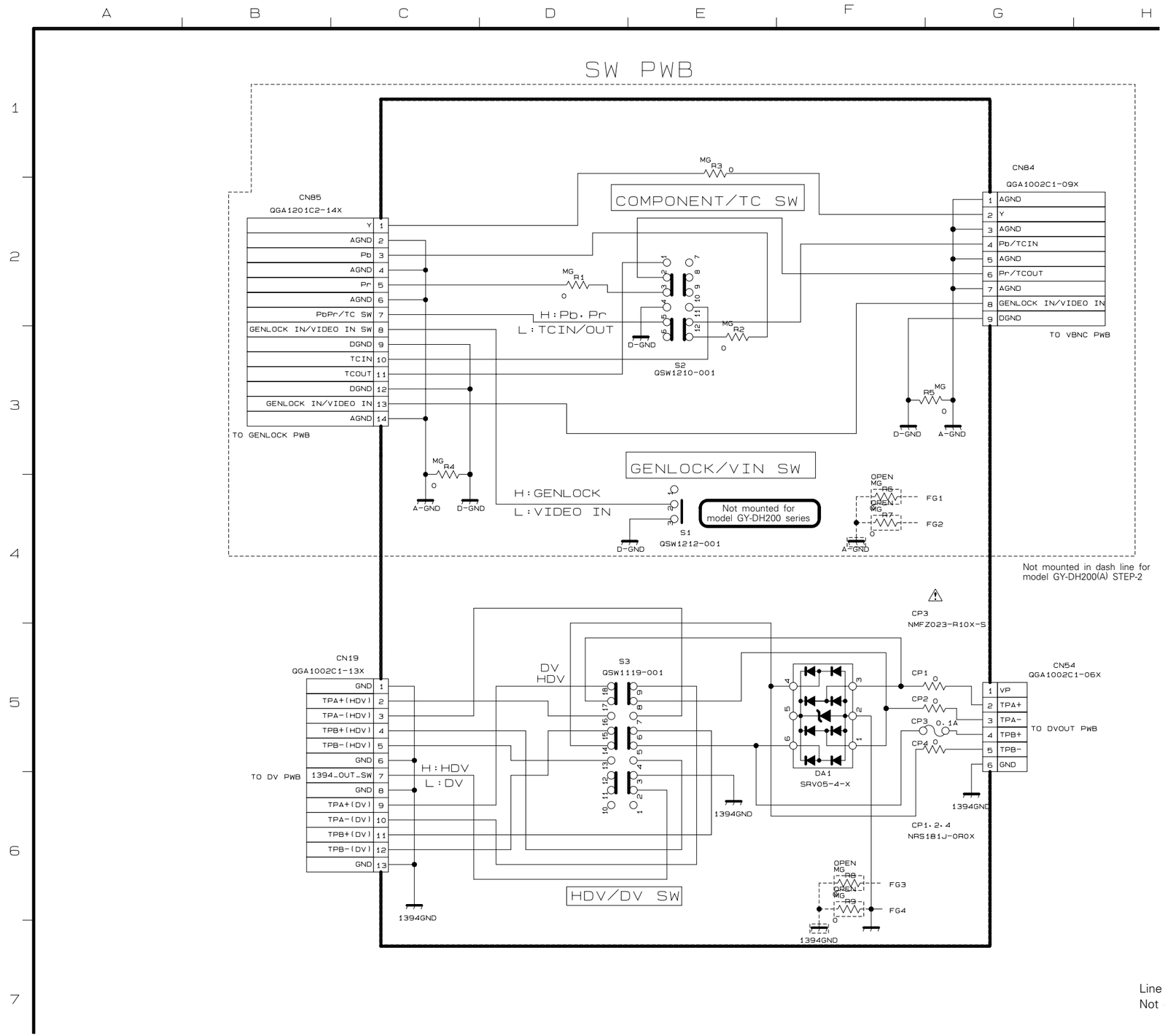
IC1	B-1B	Q12	B-3B	D15	B-2B	R14	A-2B	R39	A-3B	R63	B-2A	R88	B-2A	R114	A-1A	R138	A-2B	C2	B-1A	C26	A-3A	C54	B-2B	C79	B-2A	C110	B-1A	CN29	B-3B	K19	A-1A	F1	A-1A
IC2	B-2A	Q14	B-2B	D16	B-3A	R15	B-1B	R40	B-2B	R64	B-3A	R89	B-2A	R115	B-1A	R139	A-1B	C3	B-1B	C27	A-2A	C55	B-2B	C80	B-2B	C111	A-1B	CN30	A-2B	K20	A-2A		
IC3	B-1B	Q15	B-2B	D17	B-3A	R16	B-2B	R41	B-2A	R65	B-3A	R90	B-3B	R116	B-1A	R140	A-2B	C4	B-2B	C28	A-2A	C56	B-3B	C81	A-3A	C112	A-2B	CN75	B-3A	K21	B-1A	L1	A-2A
IC4	B-2B	Q16	A-3A	D18	B-3B	R17	B-2B	R42	B-2A	R66	B-3A	R91	B-3B	R117	B-1B	R141	B-2A	C5	B-2A	C29	A-2A	C57	A-3B	C82	B-3B	C113	A-1B	CN88	B-2A	K22	A-1A	L2	A-3B
IC5	A-2A	Q17	A-3A	D19	B-3B	R18	B-1B	R43	B-3A	R67	B-2A	R92	B-2B	R118	B-1A	R142	B-2A	C6	B-1A	C30	A-2A	C58	A-3B	C83	B-1A	C114	A-1B	CN91	A-1A	K23	A-1A	L3	A-3B
IC6	B-2A	Q18	B-2B	D20	B-2B	R19	B-2B	R44	B-3A	R68	B-2A	R93	B-2B	R119	A-1B	R143	B-2A	C7	B-2A	C31	B-1B	C59	B-3B	C85	A-1B	C115	A-1A	CN92	A-1B	K24	B-1A	L5	B-2B
IC7	A-3B	Q19	B-2B	D22	A-1A	R20	B-2B	R45	B-3A	R69	B-2A	R94	B-2B	R120	B-1A	R144	B-1A	C8	A-2B	C32	A-3B	C60	A-3B	C86	A-1A	C116	A-2B	CN93	B-1B			L6	B-2B
IC8	A-2A	Q20	B-3A	D23	B-1A	R21	B-2B	R46	B-2A	R70	B-2A	R95	B-2B	R121	B-1B	R145	B-2A	C9	A-2B	C33	A-3B	C61	A-2A	C87	A-1B	C117	B-2B			VA1	B-1B	L7	B-2B
IC10	B-1A	Q21	B-2A	D24	A-1A	R22	B-2B	R47	B-3A	R71	B-2A	R96	B-2B	R122	A-1B	R146	B-2A	C10	B-1B	C34	A-3B	C62	B-2B	C88	B-1A	C118	B-2B	K1	B-3A			L8	A-3A
IC11	A-1B	Q22	B-3A	D26	A-1B	R23	B-2B	R48	B-3A	R72	B-2A	R99	B-3A	R123	B-2A	R147	B-2A	C11	B-1B	C35	A-3A	C63	B-2B	C89	A-1B	C119	B-3B	K2	B-3B	TL1	A-2B	L9	A-3A
IC13	A-1A	Q23	A-1A	D27	A-1B	R24	B-2B	R49	B-2A	R73	B-2A	R100	B-2A	R124	B-1B	R148	B-1A	C12	B-2B	C38	B-2A	C64	A-2B	C91	A-2A	C121	B-1A	K3	A-3B	TL2	A-2B	L11	B-2B
IC14	A-1B	Q25	B-1B	D28	B-1B	R25	A-2A	R50	B-3B	R74	B-2A	R101	A-2A	R125	A-1B	R149	B-1A	C13	B-2B	C39	B-3A	C65	A-2B	C94	B-1A	C122	B-1A	K4	A-2B	TL3	A-2B	L12	B-3B
IC15	B-1B			D29	B-1B	R26	A-2A	R51	B-2A	R75	A-2A	R102	A-2A	R126	B-2A	R150	B-2A	C14	B-2B	C40	B-2A	C66	B-2B	C96	A-1B	C123	B-1A	K5	B-2B	TL4	A-2B	L13	A-1A
		D1	B-2A	D30	B-1B	R27	A-2A	R52	B-3A	R76	A-3A	R103	A-2A	R127	B-1B	R151	B-1A	C15	A-2A	C41	B-3A	C67	B-2B	C97	B-1A	C125	A-3B	K6	B-2B	TL5	A-1A	L14	A-1A
Q1	B-2A	D2	B-2A	D32	B-3B	R28	A-2A	R53	B-3A	R78	A-3A	R104	A-2A	R128	A-3B	R152	B-1A	C16	A-2A	C42	B-2A	C68	B-2B	C100	A-1A	C126	A-2B	K7	B-2B	TL6	A-1A	L15	A-1B
Q2	B-2A	D3	B-1A	D33	A-1A	R29	A-2A	R54	B-3A	R79	A-2A	R105	A-1B	R129	A-1A	R153	B-2B	C17	A-2A	C43	B-2A	C69	B-2B	C101	A-1B	C127	B-1A	K8	B-2B	TL7	A-2A	L16	A-1B
Q3	B-2B	D6	B-2B			R30	A-2A	R55	B-3A	R80	A-3A	R106	B-3A	R130	A-1A	R154	B-2B	C18	A-2A	C44	B-2A	C70	B-2B	C102	B-1A	C128	A-1A	K9	A-2B	TL8	A-1A	L17	A-2A
Q4	B-2A	D7	A-2A	R1	B-2A	R31	A-2A	R56	B-3A	R81	A-3A	R107	A-1A	R131	A-1B	R155	B-3B	C19	A-2A	C45	B-2A	C71	B-3A	C103	B-1A			K10	B-2B	TL9	A-1A	L18	B-1A
Q6	B-1B	D8	A-2A	R2	B-2A	R32	A-2A	R57	B-3A	R82	A-3A	R108	B-1A	R132	B-1B	R156	B-1A	C20	A-2A	C46	B-2A	C72	A-3A	C104	B-1A	T1	A-2B	K11	A-3A	TL10	A-1A	L19	B-1A
Q7	B-2B	D10	B-2B	R3	B-2A	R33	A-2A	R58	B-3A	R83	B-2A	R109	A-1A	R133	A-2B	R157	B-1B	C21	A-2A	C47	B-2A	C73	B-3A	C105	A-2B	T2	A-2B	K12	A-3A	TL11	A-3A		
Q8	B-2B	D11	B-3B	R4	B-2A	R34	A-2A	R59	B-2A	R84	B-2A	R110	A-2A	R134	B-1A	R158	B-3B	C22	A-2A	C48	B-2A	C74	A-3A	C106	B-2A			K13	B-3A	TL12	A-3A		
Q9	B-2B	D12	A-2B	R10	B-2A	R35	A-2A	R60	B-2A	R85	B-2A	R111	A-2A	R135	A-2B	R159	A-1A	C23	A-2A	C49	B-2A	C76	B-2B	C107	A-1B	TH1	A-3A	K14	B-3A	TL13	A-2B		
Q10	A-2A	D13	B-2B	R12	B-2B	R37	A-3A	R61	B-3A	R86	B-2A	R112	A-1A	R136	A-2B	R160	B-2A	C24	A-2A	C50	B-2A	C77	B-2A	C108	B-1A	TH2	B-1B	K16	A-2A	TL14	A-2B		
Q11	B-3B	D14	B-2B	R13	A-2B	R38	A-3A	R62	B-3A	R87	B-2A	R113	A-1B	R137	A-1B	R161	B-3A	C25	A-3A	C53	A-2A	C78	B-3A	C109	A-1A			K17	B-1B	TL15	A-3B		

2-25

2.7 GENLOCK SCHEMATIC DAIGRAM 8 2

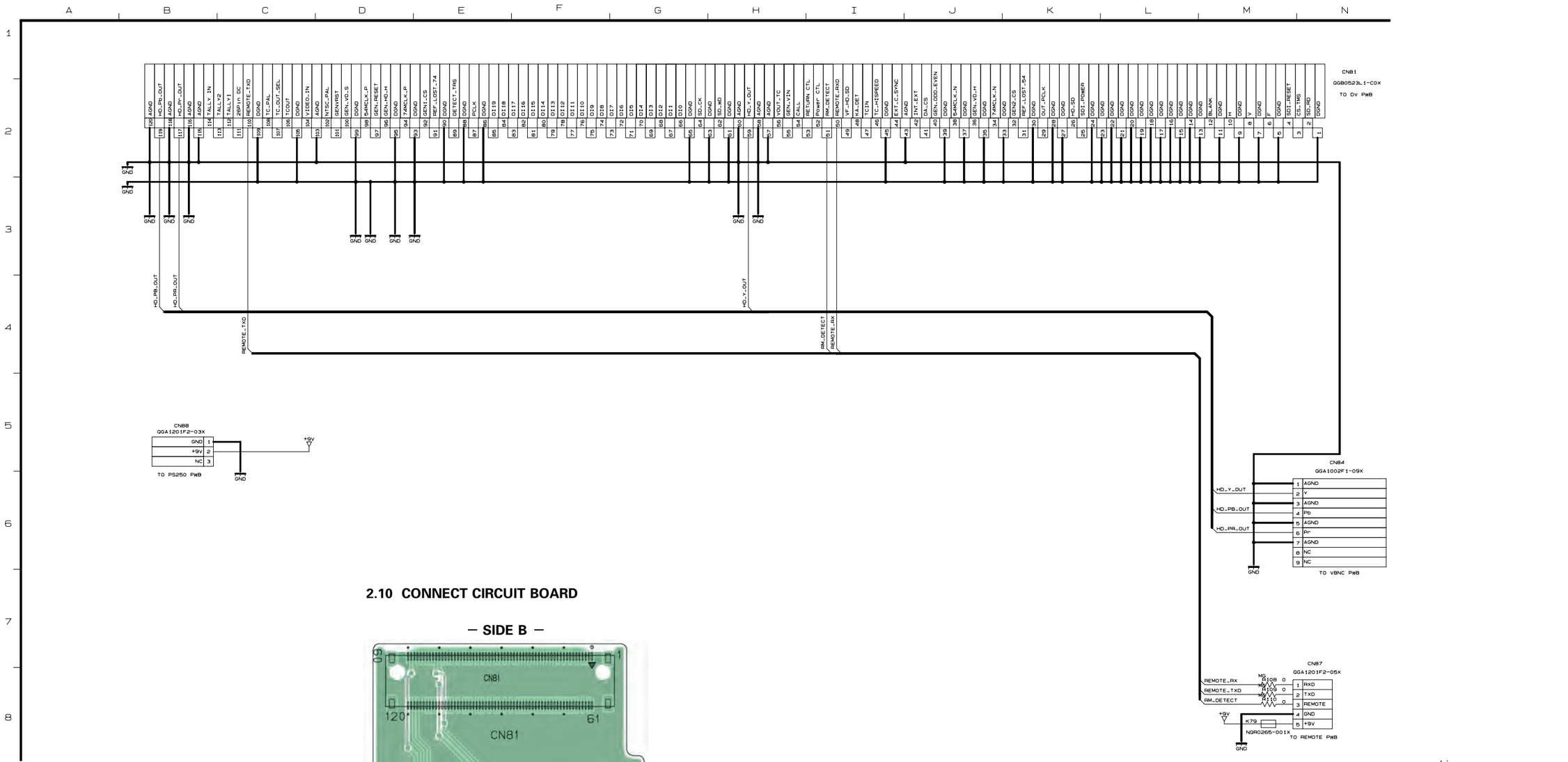


2.8 SW SCHEMATIC DIAGRAM 9 1



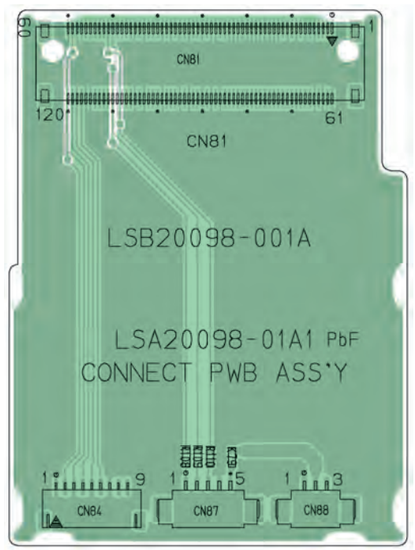
Line ----------
Not connected.

2.9 CONNECT SCHEMATIC DIAGRAM (GY-HD200(A) STEP-2 and after)



2.10 CONNECT CIRCUIT BOARD

— SIDE B —



Line ----------
Not connected.