

Technical Documentation

Signal addresses of the mxDSP card / Association with Mapping Tables

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1.1	17.04.2008	Addition of 4. Annex und 4.1 List of signal processing chains	Ro
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2.1	05.05.2009	Addition of HLSD example in chapter 2	US
2.2	27.05.2009	Dec/hex addresses for Chain IDs, completion of mxDSP variations (no PPM, mixing matrix, drawings), Example for HLSD of variation LineControl	US
2.3	28/07/2009	wrong file: "post_custom.tcl" was replaced by „post_config.tcl“, minor corrections	US

Contents

1	Introduction	3
2	HLSDs for mxDSP channels	3
3	Using the Mapping Tables	3
3.1	The Mapping Tables of the RMNOPL	3
3.2	The Mapping Tables in AdminHD	3
3.3	The configuration files custom.tcl and post_config.tcl	4
3.4	Creating entries in Mapping Tables for the signals of the KS DSP card	4
3.4.1	Note: multiple usage of a HLSD in a Mapping Table	4
3.4.2	Note: multiple use of a ClientSignalAddress in a Mapping Table	4
4	Annex	5
4.1	Definitions, acronyms and abbreviations	5
4.2	List of signal processing chains	5
4.3	Structure of mxDSP variations	6
4.3.1	mxDSP variation "Playout"	6
4.3.2	mxDSP variation "Universal"	6
4.3.3	mxDSP variation "Universal (no ppm)"	7
4.3.4	mxDSP variation "LineControl"	7
4.3.5	mxDSP unit "mixing matrix" (included in Universal, Universal no ppm and LineControl)	8
4.4	Examples	9
4.4.1	HLSDs for mxDSP variation "LineControl"	9
4.4.2	Mapping file for "Playout"	12

1 Introduction

This documentation describes the assembly of signal addresses (HLSDs) of the mxDSP card of the HD core. It also covers how these are meant to be used in combination with Mapping Tables.

2 HLSDs for mxDSP channels

HLSD are 32bit addresses (4 Byte) with a `class:subclass:component:subcomponent` layout. The following values need to be defined

Byte	Use
Class	XDR (0x73) for mxDSP Return, the output of a mxDSP channel. From the point of view of router this is an input signal XDS (0xF3) for mxDSP Send, the input of a mxDSP channel. From the point of view of the router this is a target resp. output signal.
Sub-Class	Chain-ID, the type of mxDSP channel or processing chain According to Annex 4.2 („List of signal processing chains“)
Component	A signal address depending on the type of mxDSP channel and its in- and outputs.
Sub-Component	In these two bytes the signals are numbered sequentially in relation to their chain ID.

Example:

XDR:001:000:000 would be the first output of the signal chain „Short Delay – Compressor – Fader – Limiter“. See also chapter 4.3.

3 Using the Mapping Tables

3.1 The Mapping Tables of the RMNOPL

When a RMNOPL client is not able to administer the Lawo HLSD as a signal address, the HLSDs can be translated to other address spaces and formats using *Mapping Tables*:

ClientSignalAddress ⇔ HLSD

The signal addresses sent by a RMNOPL client, for example in a *connect* command, are modified to HLSDs through the Mapping Table and then routed to the system.

In parallel there exists an automatically generated reversion table for notices from the system to the logged-in RMNOPL clients.

The Mapping Table to be used is specified by the client in the login message as a Mapping Mode.

Mapping Mode	Mapping Table
0	None. Addressing takes place via HLSDs
1 – 16	Mapping Table 1 – 16

3.2 The Mapping Tables in AdminHD

In AdminHD an individual ClientSignalAddress can be assigned per Mapping Table for every signal recognized there. These allocations are saved in the configuration file *config.td*.

Due to the complexity of the HLSD structure for mxDSP channels, the DSP signals are, however, not available in AdminHD. These are created directly within the system through the application and configuration of the operational system of the mxDSP card.

When generating a *config.td* with AdminHD, these signals therefore are not available for the placement of Mapping Tables, but have to be completed manually. It has to be taken into account that an extension of the *config.td* file would be overwritten by an update via AdminHD.

3.3 The configuration files `custom.tcl` and `post_config.tcl`

The file `custom.tcl` is a configuration file that can be generated through AdminHD. It is imported at every cold starting.

The file `post_config.tcl` is an optional configuration file, which – if present – is also imported at every cold starting. The file is however not generated by AdminHD and therefore cannot be overwritten in this manner.

Both files are in the configuration directory of the application.

3.4 Creating entries in Mapping Tables for the signals of the KS DSP card

Mapping addresses (ClientSignalAddress) for mxDSP signals are to be stored in the file `post_config.tcl`. The entries in the Mapping Tables are generated with the following command:

```
mcx_event command ID HLSD ClientSignalAddress
```

Example:

```
mcx_event SIGNALMAPPING_0_HLSD_TO_CLIENT_SIGNAL_VALUE [mcx_hlsc XDR:000:000:000] 0x1
```

Syntax	Meaning
Command <code>mcx_event</code>	A command is arriving (= event, therefore not a query.)
Command ID <code>SIGNALMAPPING</code>	Command for the creation of an entry in a Mapping Table <code>SIGNALMAPPING_0_HLSD_TO_CLIENT_SIGNAL_VALUE</code> = Mapping Table 1 <code>SIGNALMAPPING_1_HLSD_TO_CLIENT_SIGNAL_VALUE</code> = Mapping Table 2 ... <code>SIGNALMAPPING_f_HLSD_TO_CLIENT_SIGNAL_VALUE</code> = Mapping Table 16
HLSD <code>[mcx_hlsc XDR:000:000:000]</code>	The class is accordingly designated as a string (XDR/XDS), the three further bytes as a decimal number (0... 255)
ClientSignalAddress <code>0x0001</code>	The addressing of in- and output signals used by the RMNOPL client. The value must be hexadecimal; there is no need for distinguishing between source/drain, since this follows from the HLSD.

3.4.1 Note: multiple usage of a HLSD in a Mapping Table

In the case of multiple usage of a HLSD in a Mapping Table the following error message will be recorded in the log file:

```
SignalmappingHandler: mapping entry already exists for hlsc
```

Processing of `post_config.tcl` is aborted, the system fails to boot.

3.4.2 Note: multiple use of a ClientSignalAddress in a Mapping Table

This is not intercepted. The last ClientSignalAddress set is always the one valid.

One therefore has to take care that the ClientSignalAddresses set up via the AdminHD are not overwritten by the ClientSignalAddresses set up in the `post_config.tcl`.

4 Annex

4.1 Definitions, acronyms and abbreviations

HLSD

High Level Signal Definition – Address used by Lawo for audio in- and output signals

ClientSignalAddress

The addresses for audio in- and output signals used by external RMNOPL clients, as long as these don't use HLSDs.

mxDSP card

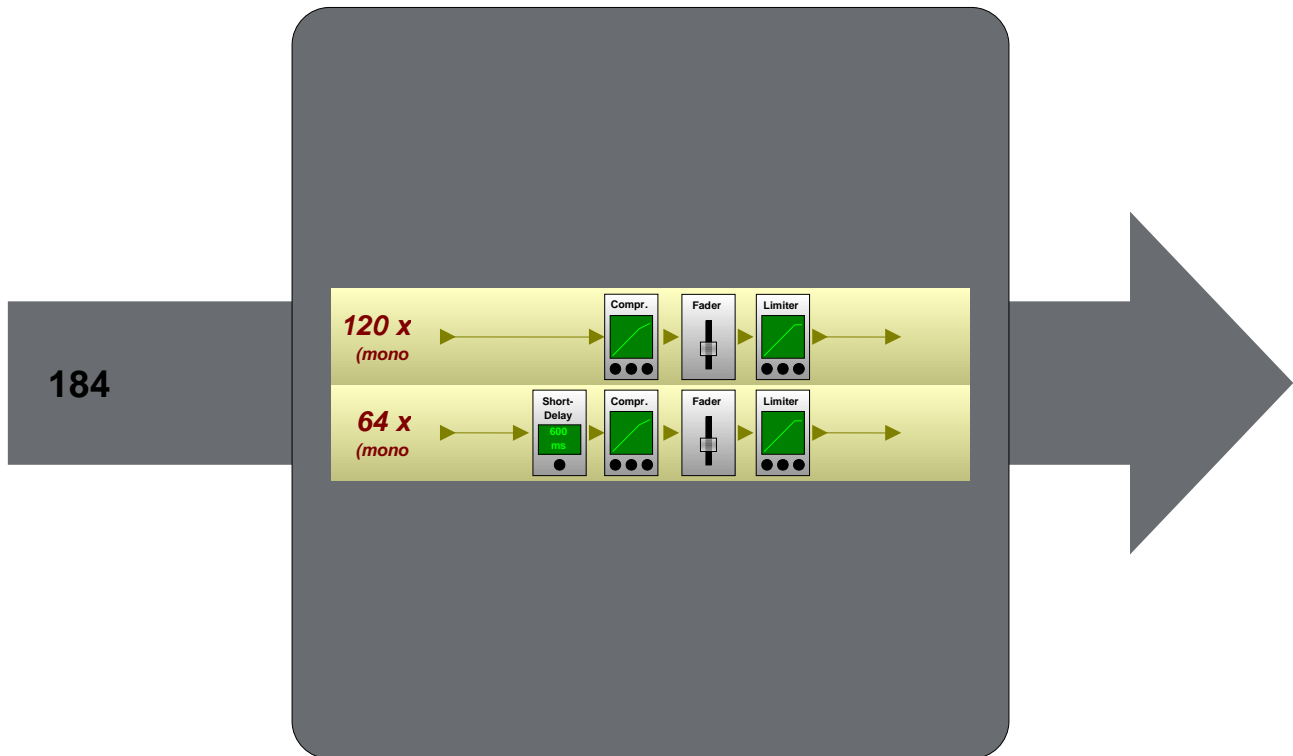
A plug-in card for the HD core created for crossbar applications for extended signal processing (Type 983/03-007).

4.2 List of signal processing chains

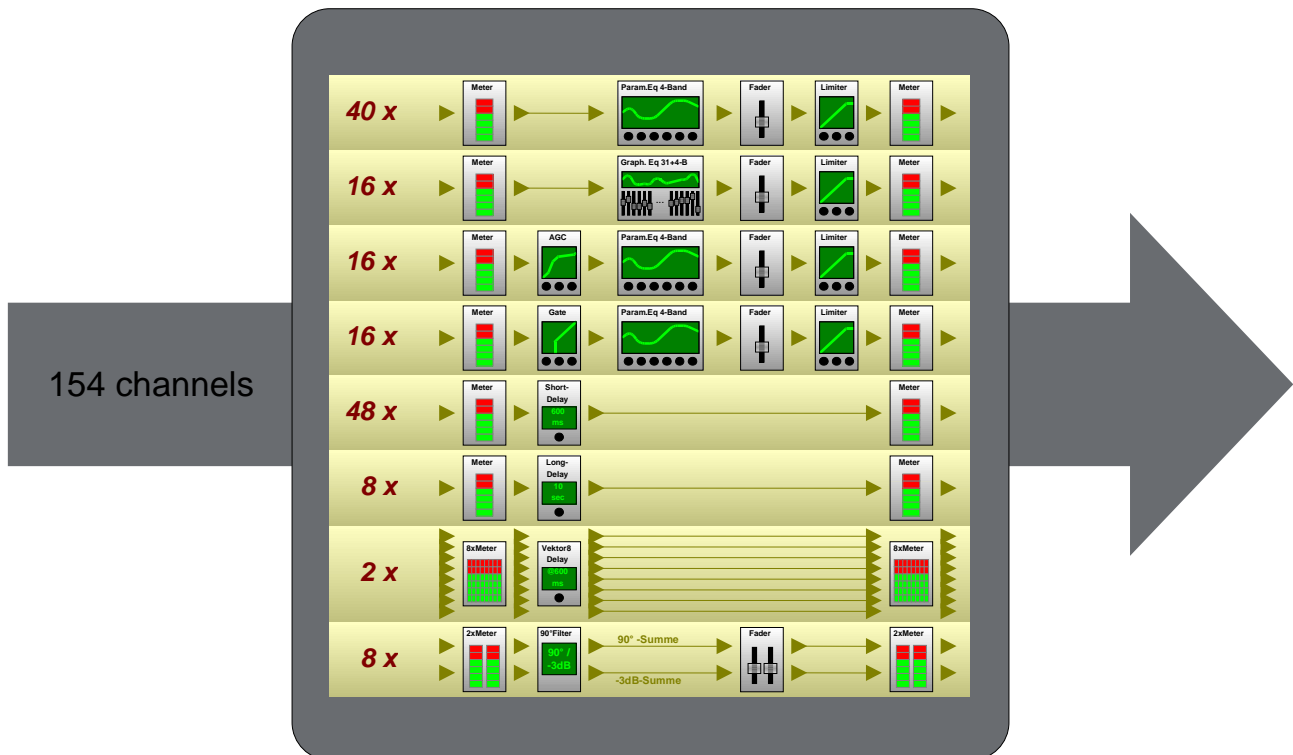
Signal processing chain	Chain-ID		Variation Playout	Number in		
	Dec.	Hex.		Variation Universal	Variation Universal (no ppm)	Variation LineControl
COMP_FDR_LIM	0	0x00	120			
DLY_COMP_FDR_LIM	1	0x01	64			
EQ_FDR_LIM	2	0x02		40	40	
GRAPHIC_EQ_FDR_LIM	3	0x03		16	16	
AGC_EQ_FDR_LIM	4	0x04		16	16	
GATE_EQ_FDR_LIM	5	0x05		16	16	
SHORT_DELAY	6	0x06		48	48	
LONG_DELAY	7	0x07		8	8	
LIM	8	0x08				
90DEG_FDR	9	0x09		8	8	
DLY_INP_AGC_TFDR_COMP_LIM_SCM	10	0x0a				72
INP_TFDR_SCM	11	0x0b				112
CHAIN_VECTOR_8_DELAY	193	0xc1		2	2	
CHAIN_SUM_MATRIX_64	241	0xf1		✓ (64 signals)	✓ (64 signals)	✓ (64 signals)

4.3 Structure of mxDSP variations

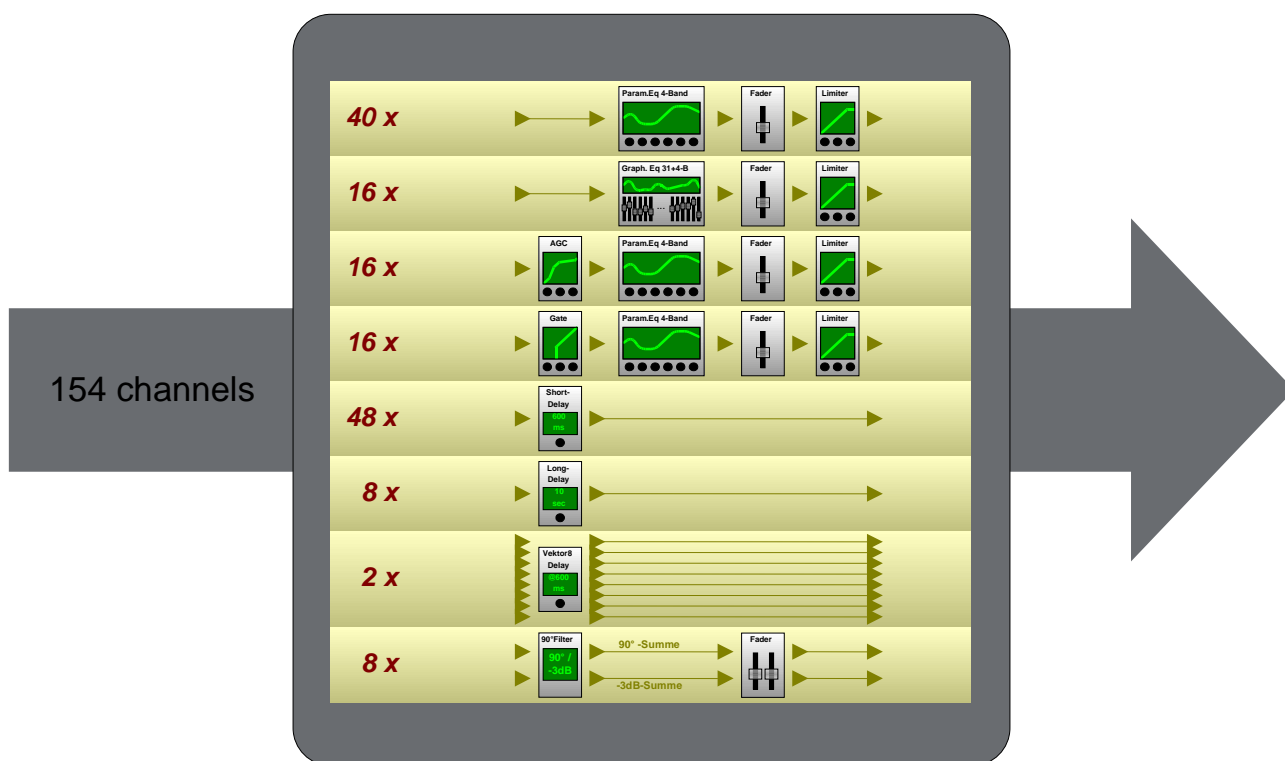
4.3.1 mxDSP variation "Playout"



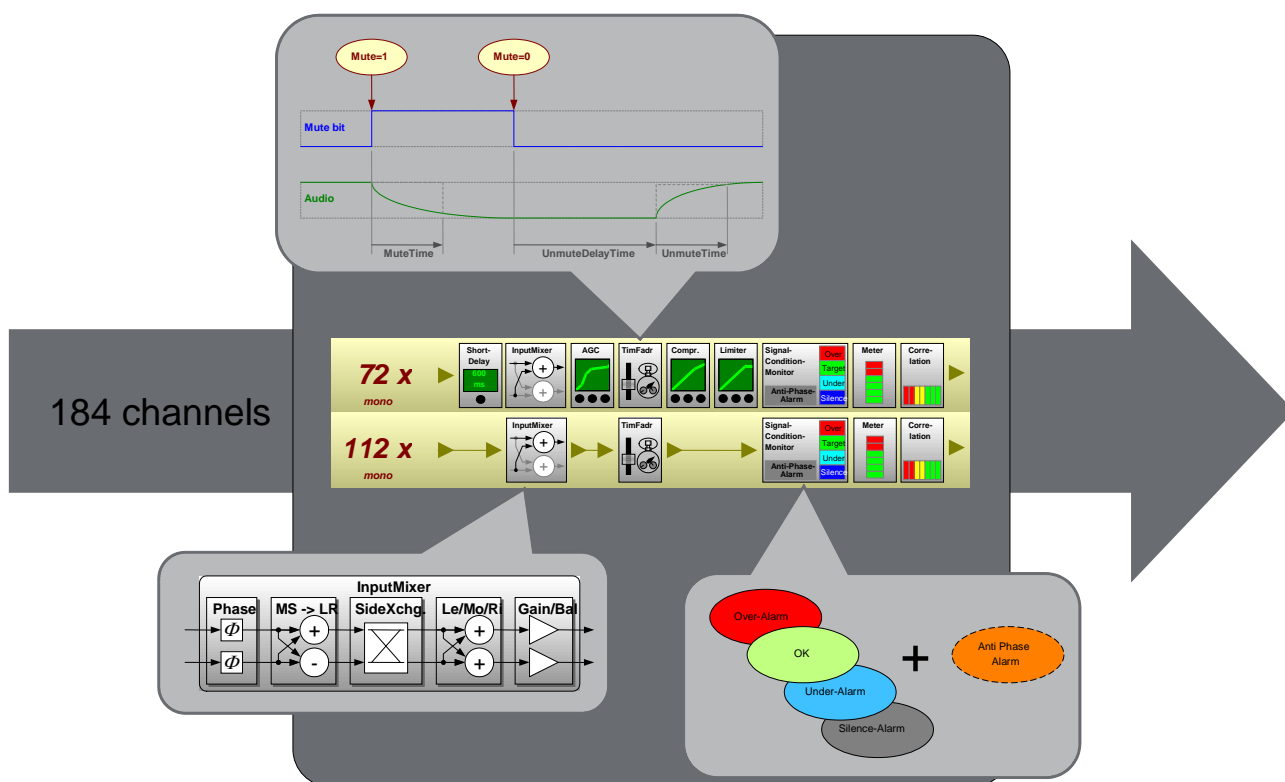
4.3.2 mxDSP variation "Universal"



4.3.3 mxDSP variation “Universal (no ppm)”

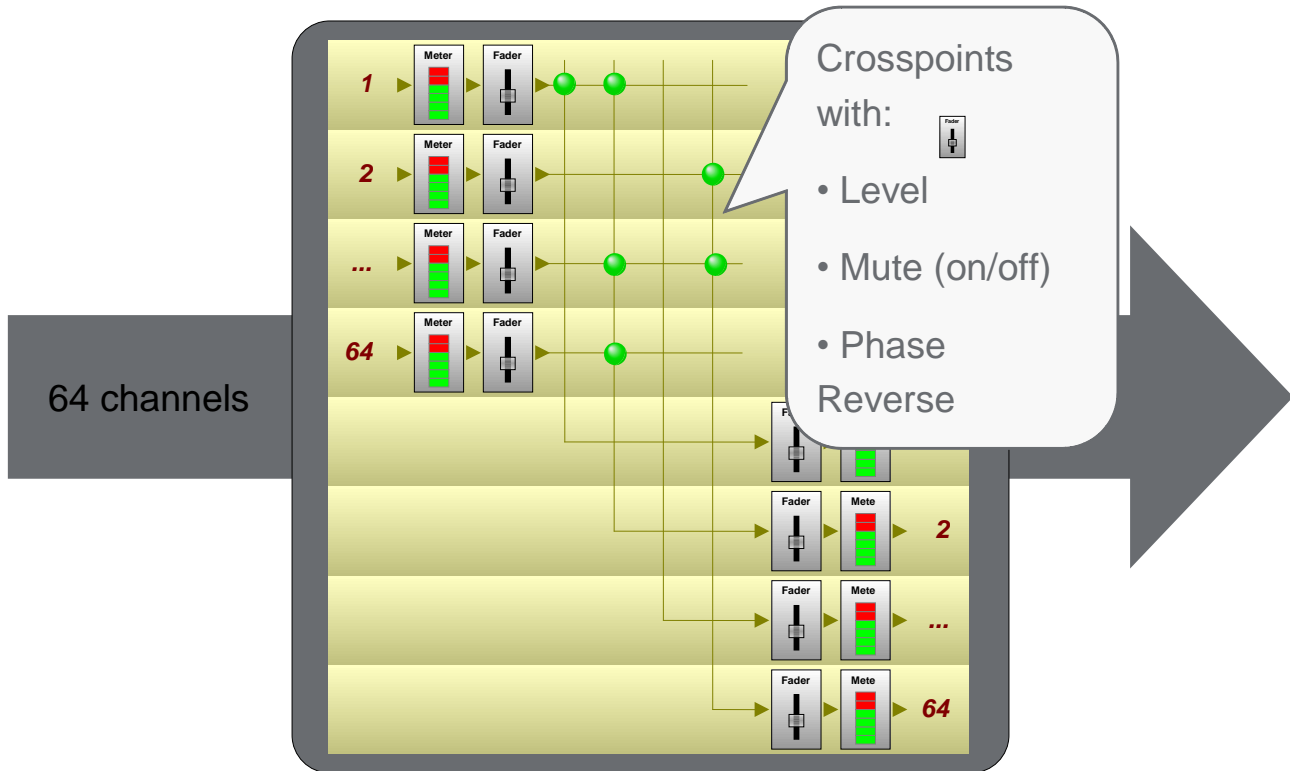


4.3.4 mxDSP variation “LineControl”



4.3.5 mxDSP unit “mixing matrix”

(included in Universal, Universal no ppm and LineControl)



4.4 Examples

4.4.1 HLSDs for mxDSP variation "LineControl"

* mxDSP board 983/03-007
* Variation 'Line Control'

* Signal processing chain DLY_INP_AGC_TFDR_COMP_LIM_SCM (72 times per mxDSP board)
* Outputs of the router (DSP channel inputs)
* -XDS- oder 0xF3
* HLSD

```
*mxDSP board No. 1
XDS:010:000:000      --HEX--> 0xF3 0A 00 00
XDS:010:000:001
XDS:010:000:002
XDS:010:000:003
XDS:010:000:004
XDS:010:000:005
XDS:010:000:006
XDS:010:000:007
XDS:010:000:008
XDS:010:000:009
XDS:010:000:010
XDS:010:000:011
XDS:010:000:012      --HEX--> 0xF3 0A 00 0C
.
.
XDS:010:000:070      --HEX--> 0xF3 0A 00 46
XDS:010:000:071      --HEX--> 0xF3 0A 00 47
```

```
*mxDSP board No. 2
XDS:010:000:072
XDS:010:000:073
XDS:010:000:074
.
.
.
XDS:010:000:142
XDS:010:000:143
```

* Signal processing chain DLY_INP_AGC_TFDR_COMP_LIM_SCM (72 times per mxDSP board)
* Inputs of the router (DSP channel outputs)
* -XDR- oder 0x73
* HLSD

```
*mxDSP board No. 1
XDR:010:000:000      --HEX--> 0x73 0A 00 00
XDR:010:000:001
XDR:010:000:002
.
.
.
XDR:010:000:070
XDR:010:000:071
```

*mxDSP board No. 2

XDR:010:000:072

XDR:010:000:073

XDR:010:000:074

.

.

.

XDR:010:000:142

XDR:010:000:143

* Signal processing chain INP_TFDR_SCM (112 times per mxDSP board)

* Outputs of the router (DSP channel inputs)

* -XDS- oder 0xF3

* HLSD

*mxDSP board No. 1

XDS:011:000:000 --HEX--> 0xF3 0B 00 00

XDS:011:000:001

XDS:011:000:002

.

.

.

XDS:011:000:110

XDS:011:000:111

*mxDSP board No. 2

XDS:011:000:112

XDS:011:000:113

XDS:011:000:114

.

.

.

XDS:011:000:222

XDS:011:000:223

* Signal processing chain INP_TFDR_SCM (112 times per mxDSP board)

* Inputs of the router (DSP channel outputs)

* -XDR- oder 0x73

* HLSD

*mxDSP board No. 1

XDR:011:000:000

XDR:011:000:001

XDR:011:000:002

.

.

.

XDR:011:000:110

XDR:011:000:111

*mxDSP board No. 2

XDR:011:000:112

XDR:011:000:113

XDR:011:000:114

.

.

```
.
XDR:011:000:222
XDR:011:000:223

* Signal processing chain CHAIN_SUM_MATRIX_64 (64 times per mxDSP board)*
* Outputs of the router (DSP channel inputs)
* -XDS- oder 0xF3
* HLSD

*mxDSP board No. 1
XDS:241:000:000      --HEX--> 0xF3 F1 00 00
XDS:241:000:001
XDS:241:000:002
.
.
.
XDS:241:000:062
XDS:241:000:063

*mxDSP board No. 2
XDS:241:000:064
XDS:241:000:065
XDS:241:000:066
.
.
.
XDS:241:000:126
XDS:241:000:127

* Signal processing chain CHAIN_SUM_MATRIX_64 (64 times per mxDSP board)*
* Inputs of the router (DSP channel outputs)
* -XDR- oder 0x73
* HLSD

*mxDSP board No. 1
XDR:241:000:000      --HEX--> 0x73 F1 00 00
XDR:241:000:001
XDR:241:000:002
.
.
.
XDR:241:000:062
XDR:241:000:063

*mxDSP board No. 2
XDR:241:000:064
XDR:241:000:065
XDR:241:000:066
.
.
.
XDR:241:000:126
XDR:241:000:127
```

4.4.2 Mapping file for "Payout"

In following an example is provided, in which all signals of three mxDSP cards of the "Payout" variation are entered into Mapping Table 1.

The connected HLSDs are given 2 Byte ClientSignalAddresses, which are sequentially numbered with a base 1.

Outputs of the matrix DSP channels (KS: input)	
mcx_event SIGNALMAPPING_0_HLSD_TO_CLIENT_SIGNAL_VALUE [mcx_hlsc XDR:000:000:000] 0x1	First output [Compressor – Fader – Limiter] mapped to ClientSignalAddress 0x1
mcx_event SIGNALMAPPING_0_HLSD_TO_CLIENT_SIGNAL_VALUE [mcx_hlsc XDR:000: 001:103] 0x168	Last output [Compressor – Fader – Limiter] mapped to ClientSignalAddress 0x168
mcx_event SIGNALMAPPING_0_HLSD_TO_CLIENT_SIGNAL_VALUE [mcx_hlsc XDR:001:000:000] 0x169	First output [Short Delay – Compressor – Fader – Limiter] mapped to ClientSignalAddress 0x169
mcx_event SIGNALMAPPING_0_HLSD_TO_CLIENT_SIGNAL_VALUE [mcx_hlsc XDR:001:000:191] 0x228	Last output [Short Delay – Compressor – Fader – Limiter] mapped to ClientSignalAddress 0x228
Inputs of the matrix DSP channels (KS: output)	
mcx_event SIGNALMAPPING_0_HLSD_TO_CLIENT_SIGNAL_VALUE [mcx_hlsc XDS:000:000:000] 0x1	First input [Compressor – Fader – Limiter] mapped to ClientSignalAddress 0x1
mcx_event SIGNALMAPPING_0_HLSD_TO_CLIENT_SIGNAL_VALUE [mcx_hlsc XDS:000:001:103] 0x168	Last Input [Compressor – Fader – Limiter] mapped to ClientSignalAddress 0x168
mcx_event SIGNALMAPPING_0_HLSD_TO_CLIENT_SIGNAL_VALUE [mcx_hlsc XDS:001:000:000] 0x169	First input [Short Delay – Compressor – Fader – Limiter] mapped to ClientSignalAddress 0x169
mcx_event SIGNALMAPPING_0_HLSD_TO_CLIENT_SIGNAL_VALUE [mcx_hlsc XDS:001:000:191] 0x228	Last input [Short Delay – Compressor – Fader – Limiter] Mapped to ClientSignalAddress 0x228

Example of a configuration file:

```
puts "post_config.tcl"
puts "Generated manually"
puts "Date: Mar 14, 2006"
puts "Time: 11:58:50 AM"

#####
#      MAPPING TABLES      #
#####

puts "mapping tables extensions for mx dsp signals"
puts "mapping address starts at 1"

#Mapping table 01: Out [Compressor - Fader - Limiter]
mcx_event SIGNALMAPPING_0_HLSD_TO_CLIENT_SIGNAL_VALUE [mcx_hlsd XDR:000:000:000] 0x001
mcx_event SIGNALMAPPING_0_HLSD_TO_CLIENT_SIGNAL_VALUE [mcx_hlsd XDR:000:000:001] 0x002
mcx_event SIGNALMAPPING_0_HLSD_TO_CLIENT_SIGNAL_VALUE [mcx_hlsd XDR:000:000:002] 0x003
mcx_event SIGNALMAPPING_0_HLSD_TO_CLIENT_SIGNAL_VALUE [mcx_hlsd XDR:000:000:003] 0x004
mcx_event SIGNALMAPPING_0_HLSD_TO_CLIENT_SIGNAL_VALUE [mcx_hlsd XDR:000:000:004] 0x005
mcx_event SIGNALMAPPING_0_HLSD_TO_CLIENT_SIGNAL_VALUE [mcx_hlsd XDR:000:000:005] 0x006
mcx_event SIGNALMAPPING_0_HLSD_TO_CLIENT_SIGNAL_VALUE [mcx_hlsd XDR:000:000:006] 0x007
mcx_event SIGNALMAPPING_0_HLSD_TO_CLIENT_SIGNAL_VALUE [mcx_hlsd XDR:000:000:007] 0x008
mcx_event SIGNALMAPPING_0_HLSD_TO_CLIENT_SIGNAL_VALUE [mcx_hlsd XDR:000:000:008] 0x009
mcx_event SIGNALMAPPING_0_HLSD_TO_CLIENT_SIGNAL_VALUE [mcx_hlsd XDR:000:000:009] 0x00A
mcx_event SIGNALMAPPING_0_HLSD_TO_CLIENT_SIGNAL_VALUE [mcx_hlsd XDR:000:000:010] 0x00B
mcx_event SIGNALMAPPING_0_HLSD_TO_CLIENT_SIGNAL_VALUE [mcx_hlsd XDR:000:000:011] 0x00C
mcx_event SIGNALMAPPING_0_HLSD_TO_CLIENT_SIGNAL_VALUE [mcx_hlsd XDR:000:000:012] 0x00D
mcx_event SIGNALMAPPING_0_HLSD_TO_CLIENT_SIGNAL_VALUE [mcx_hlsd XDR:000:000:013] 0x00E
mcx_event SIGNALMAPPING_0_HLSD_TO_CLIENT_SIGNAL_VALUE [mcx_hlsd XDR:000:000:014] 0x00F
mcx_event SIGNALMAPPING_0_HLSD_TO_CLIENT_SIGNAL_VALUE [mcx_hlsd XDR:000:000:015] 0x010
mcx_event SIGNALMAPPING_0_HLSD_TO_CLIENT_SIGNAL_VALUE [mcx_hlsd XDR:000:000:016] 0x011
mcx_event SIGNALMAPPING_0_HLSD_TO_CLIENT_SIGNAL_VALUE [mcx_hlsd XDR:000:000:017] 0x012
mcx_event SIGNALMAPPING_0_HLSD_TO_CLIENT_SIGNAL_VALUE [mcx_hlsd XDR:000:000:018] 0x013
mcx_event SIGNALMAPPING_0_HLSD_TO_CLIENT_SIGNAL_VALUE [mcx_hlsd XDR:000:000:019] 0x014
mcx_event SIGNALMAPPING_0_HLSD_TO_CLIENT_SIGNAL_VALUE [mcx_hlsd XDR:000:000:020] 0x015
mcx_event SIGNALMAPPING_0_HLSD_TO_CLIENT_SIGNAL_VALUE [mcx_hlsd XDR:000:000:021] 0x016
mcx_event SIGNALMAPPING_0_HLSD_TO_CLIENT_SIGNAL_VALUE [mcx_hlsd XDR:000:000:022] 0x017
mcx_event SIGNALMAPPING_0_HLSD_TO_CLIENT_SIGNAL_VALUE [mcx_hlsd XDR:000:000:023] 0x018
mcx_event SIGNALMAPPING_0_HLSD_TO_CLIENT_SIGNAL_VALUE [mcx_hlsd XDR:000:000:024] 0x019
mcx_event SIGNALMAPPING_0_HLSD_TO_CLIENT_SIGNAL_VALUE [mcx_hlsd XDR:000:000:025] 0x01A
mcx_event SIGNALMAPPING_0_HLSD_TO_CLIENT_SIGNAL_VALUE [mcx_hlsd XDR:000:000:026] 0x01B
mcx_event SIGNALMAPPING_0_HLSD_TO_CLIENT_SIGNAL_VALUE [mcx_hlsd XDR:000:000:027] 0x01C
mcx_event SIGNALMAPPING_0_HLSD_TO_CLIENT_SIGNAL_VALUE [mcx_hlsd XDR:000:000:028] 0x01D
mcx_event SIGNALMAPPING_0_HLSD_TO_CLIENT_SIGNAL_VALUE [mcx_hlsd XDR:000:000:029] 0x01E
mcx_event SIGNALMAPPING_0_HLSD_TO_CLIENT_SIGNAL_VALUE [mcx_hlsd XDR:000:000:030] 0x01F
mcx_event SIGNALMAPPING_0_HLSD_TO_CLIENT_SIGNAL_VALUE [mcx_hlsd XDR:000:000:031] 0x020
mcx_event SIGNALMAPPING_0_HLSD_TO_CLIENT_SIGNAL_VALUE [mcx_hlsd XDR:000:000:032] 0x021
mcx_event SIGNALMAPPING_0_HLSD_TO_CLIENT_SIGNAL_VALUE [mcx_hlsd XDR:000:000:033] 0x022
mcx_event SIGNALMAPPING_0_HLSD_TO_CLIENT_SIGNAL_VALUE [mcx_hlsd XDR:000:000:034] 0x023
mcx_event SIGNALMAPPING_0_HLSD_TO_CLIENT_SIGNAL_VALUE [mcx_hlsd XDR:000:000:035] 0x024
mcx_event SIGNALMAPPING_0_HLSD_TO_CLIENT_SIGNAL_VALUE [mcx_hlsd XDR:000:000:036] 0x025
mcx_event SIGNALMAPPING_0_HLSD_TO_CLIENT_SIGNAL_VALUE [mcx_hlsd XDR:000:000:037] 0x026
mcx_event SIGNALMAPPING_0_HLSD_TO_CLIENT_SIGNAL_VALUE [mcx_hlsd XDR:000:000:038] 0x027
```

Seite 14 / 33

Seite 15 / 33

Seite 16 / 33

Seite 17 / 33

Seite 18 / 33

mcx_event	SIGNALMAPPING_0_HLSD_TO_CLIENT_SIGNAL_VALUE	[mcx_hlsd XDR:000:001:063]	0x140
mcx_event	SIGNALMAPPING_0_HLSD_TO_CLIENT_SIGNAL_VALUE	[mcx_hlsd XDR:000:001:064]	0x141
mcx_event	SIGNALMAPPING_0_HLSD_TO_CLIENT_SIGNAL_VALUE	[mcx_hlsd XDR:000:001:065]	0x142
mcx_event	SIGNALMAPPING_0_HLSD_TO_CLIENT_SIGNAL_VALUE	[mcx_hlsd XDR:000:001:066]	0x143
mcx_event	SIGNALMAPPING_0_HLSD_TO_CLIENT_SIGNAL_VALUE	[mcx_hlsd XDR:000:001:067]	0x144
mcx_event	SIGNALMAPPING_0_HLSD_TO_CLIENT_SIGNAL_VALUE	[mcx_hlsd XDR:000:001:068]	0x145
mcx_event	SIGNALMAPPING_0_HLSD_TO_CLIENT_SIGNAL_VALUE	[mcx_hlsd XDR:000:001:069]	0x146
mcx_event	SIGNALMAPPING_0_HLSD_TO_CLIENT_SIGNAL_VALUE	[mcx_hlsd XDR:000:001:070]	0x147
mcx_event	SIGNALMAPPING_0_HLSD_TO_CLIENT_SIGNAL_VALUE	[mcx_hlsd XDR:000:001:071]	0x148
mcx_event	SIGNALMAPPING_0_HLSD_TO_CLIENT_SIGNAL_VALUE	[mcx_hlsd XDR:000:001:072]	0x149
mcx_event	SIGNALMAPPING_0_HLSD_TO_CLIENT_SIGNAL_VALUE	[mcx_hlsd XDR:000:001:073]	0x14A
mcx_event	SIGNALMAPPING_0_HLSD_TO_CLIENT_SIGNAL_VALUE	[mcx_hlsd XDR:000:001:074]	0x14B
mcx_event	SIGNALMAPPING_0_HLSD_TO_CLIENT_SIGNAL_VALUE	[mcx_hlsd XDR:000:001:075]	0x14C
mcx_event	SIGNALMAPPING_0_HLSD_TO_CLIENT_SIGNAL_VALUE	[mcx_hlsd XDR:000:001:076]	0x14D
mcx_event	SIGNALMAPPING_0_HLSD_TO_CLIENT_SIGNAL_VALUE	[mcx_hlsd XDR:000:001:077]	0x14E
mcx_event	SIGNALMAPPING_0_HLSD_TO_CLIENT_SIGNAL_VALUE	[mcx_hlsd XDR:000:001:078]	0x14F
mcx_event	SIGNALMAPPING_0_HLSD_TO_CLIENT_SIGNAL_VALUE	[mcx_hlsd XDR:000:001:079]	0x150
mcx_event	SIGNALMAPPING_0_HLSD_TO_CLIENT_SIGNAL_VALUE	[mcx_hlsd XDR:000:001:080]	0x151
mcx_event	SIGNALMAPPING_0_HLSD_TO_CLIENT_SIGNAL_VALUE	[mcx_hlsd XDR:000:001:081]	0x152
mcx_event	SIGNALMAPPING_0_HLSD_TO_CLIENT_SIGNAL_VALUE	[mcx_hlsd XDR:000:001:082]	0x153
mcx_event	SIGNALMAPPING_0_HLSD_TO_CLIENT_SIGNAL_VALUE	[mcx_hlsd XDR:000:001:083]	0x154
mcx_event	SIGNALMAPPING_0_HLSD_TO_CLIENT_SIGNAL_VALUE	[mcx_hlsd XDR:000:001:084]	0x155
mcx_event	SIGNALMAPPING_0_HLSD_TO_CLIENT_SIGNAL_VALUE	[mcx_hlsd XDR:000:001:085]	0x156
mcx_event	SIGNALMAPPING_0_HLSD_TO_CLIENT_SIGNAL_VALUE	[mcx_hlsd XDR:000:001:086]	0x157
mcx_event	SIGNALMAPPING_0_HLSD_TO_CLIENT_SIGNAL_VALUE	[mcx_hlsd XDR:000:001:087]	0x158
mcx_event	SIGNALMAPPING_0_HLSD_TO_CLIENT_SIGNAL_VALUE	[mcx_hlsd XDR:000:001:088]	0x159
mcx_event	SIGNALMAPPING_0_HLSD_TO_CLIENT_SIGNAL_VALUE	[mcx_hlsd XDR:000:001:089]	0x15A
mcx_event	SIGNALMAPPING_0_HLSD_TO_CLIENT_SIGNAL_VALUE	[mcx_hlsd XDR:000:001:090]	0x15B
mcx_event	SIGNALMAPPING_0_HLSD_TO_CLIENT_SIGNAL_VALUE	[mcx_hlsd XDR:000:001:091]	0x15C
mcx_event	SIGNALMAPPING_0_HLSD_TO_CLIENT_SIGNAL_VALUE	[mcx_hlsd XDR:000:001:092]	0x15D
mcx_event	SIGNALMAPPING_0_HLSD_TO_CLIENT_SIGNAL_VALUE	[mcx_hlsd XDR:000:001:093]	0x15E
mcx_event	SIGNALMAPPING_0_HLSD_TO_CLIENT_SIGNAL_VALUE	[mcx_hlsd XDR:000:001:094]	0x15F
mcx_event	SIGNALMAPPING_0_HLSD_TO_CLIENT_SIGNAL_VALUE	[mcx_hlsd XDR:000:001:095]	0x160
mcx_event	SIGNALMAPPING_0_HLSD_TO_CLIENT_SIGNAL_VALUE	[mcx_hlsd XDR:000:001:096]	0x161
mcx_event	SIGNALMAPPING_0_HLSD_TO_CLIENT_SIGNAL_VALUE	[mcx_hlsd XDR:000:001:097]	0x162
mcx_event	SIGNALMAPPING_0_HLSD_TO_CLIENT_SIGNAL_VALUE	[mcx_hlsd XDR:000:001:098]	0x163
mcx_event	SIGNALMAPPING_0_HLSD_TO_CLIENT_SIGNAL_VALUE	[mcx_hlsd XDR:000:001:099]	0x164
mcx_event	SIGNALMAPPING_0_HLSD_TO_CLIENT_SIGNAL_VALUE	[mcx_hlsd XDR:000:001:100]	0x165
mcx_event	SIGNALMAPPING_0_HLSD_TO_CLIENT_SIGNAL_VALUE	[mcx_hlsd XDR:000:001:101]	0x166
mcx_event	SIGNALMAPPING_0_HLSD_TO_CLIENT_SIGNAL_VALUE	[mcx_hlsd XDR:000:001:102]	0x167
mcx_event	SIGNALMAPPING_0_HLSD_TO_CLIENT_SIGNAL_VALUE	[mcx_hlsd XDR:000:001:103]	0x168

#Mapping table 01: Out [shortDelay - Compressor - Fader - Limiter]

mcx_event	SIGNALMAPPING_0_HLSD_TO_CLIENT_SIGNAL_VALUE	[mcx_hlsd XDR:001:000:000]	0x0169
mcx_event	SIGNALMAPPING_0_HLSD_TO_CLIENT_SIGNAL_VALUE	[mcx_hlsd XDR:001:000:001]	0x016A
mcx_event	SIGNALMAPPING_0_HLSD_TO_CLIENT_SIGNAL_VALUE	[mcx_hlsd XDR:001:000:002]	0x016B
mcx_event	SIGNALMAPPING_0_HLSD_TO_CLIENT_SIGNAL_VALUE	[mcx_hlsd XDR:001:000:003]	0x016C
mcx_event	SIGNALMAPPING_0_HLSD_TO_CLIENT_SIGNAL_VALUE	[mcx_hlsd XDR:001:000:004]	0x016D
mcx_event	SIGNALMAPPING_0_HLSD_TO_CLIENT_SIGNAL_VALUE	[mcx_hlsd XDR:001:000:005]	0x016E
mcx_event	SIGNALMAPPING_0_HLSD_TO_CLIENT_SIGNAL_VALUE	[mcx_hlsd XDR:001:000:006]	0x016F
mcx_event	SIGNALMAPPING_0_HLSD_TO_CLIENT_SIGNAL_VALUE	[mcx_hlsd XDR:001:000:007]	0x0170
mcx_event	SIGNALMAPPING_0_HLSD_TO_CLIENT_SIGNAL_VALUE	[mcx_hlsd XDR:001:000:008]	0x0171
mcx_event	SIGNALMAPPING_0_HLSD_TO_CLIENT_SIGNAL_VALUE	[mcx_hlsd XDR:001:000:009]	0x0172
mcx_event	SIGNALMAPPING_0_HLSD_TO_CLIENT_SIGNAL_VALUE	[mcx_hlsd XDR:001:000:010]	0x0173
mcx_event	SIGNALMAPPING_0_HLSD_TO_CLIENT_SIGNAL_VALUE	[mcx_hlsd XDR:001:000:011]	0x0174
mcx_event	SIGNALMAPPING_0_HLSD_TO_CLIENT_SIGNAL_VALUE	[mcx_hlsd XDR:001:000:012]	0x0175

Seite 20 / 33

Seite 21 / 33

Seite 22 / 33

mcx_event	SIGNALMAPPING_0_HLSD_TO_CLIENT_SIGNAL_VALUE	[mcx_hlsl	XDR:001:000:181]	0x021E
mcx_event	SIGNALMAPPING_0_HLSD_TO_CLIENT_SIGNAL_VALUE	[mcx_hlsl	XDR:001:000:182]	0x021F
mcx_event	SIGNALMAPPING_0_HLSD_TO_CLIENT_SIGNAL_VALUE	[mcx_hlsl	XDR:001:000:183]	0x0220
mcx_event	SIGNALMAPPING_0_HLSD_TO_CLIENT_SIGNAL_VALUE	[mcx_hlsl	XDR:001:000:184]	0x0221
mcx_event	SIGNALMAPPING_0_HLSD_TO_CLIENT_SIGNAL_VALUE	[mcx_hlsl	XDR:001:000:185]	0x0222
mcx_event	SIGNALMAPPING_0_HLSD_TO_CLIENT_SIGNAL_VALUE	[mcx_hlsl	XDR:001:000:186]	0x0223
mcx_event	SIGNALMAPPING_0_HLSD_TO_CLIENT_SIGNAL_VALUE	[mcx_hlsl	XDR:001:000:187]	0x0224
mcx_event	SIGNALMAPPING_0_HLSD_TO_CLIENT_SIGNAL_VALUE	[mcx_hlsl	XDR:001:000:188]	0x0225
mcx_event	SIGNALMAPPING_0_HLSD_TO_CLIENT_SIGNAL_VALUE	[mcx_hlsl	XDR:001:000:189]	0x0226
mcx_event	SIGNALMAPPING_0_HLSD_TO_CLIENT_SIGNAL_VALUE	[mcx_hlsl	XDR:001:000:190]	0x0227
mcx_event	SIGNALMAPPING_0_HLSD_TO_CLIENT_SIGNAL_VALUE	[mcx_hlsl	XDR:001:000:191]	0x0228

#Mapping table 01: In [Compressor - Fader - Limiter]

mcx_event	SIGNALMAPPING_0_HLSD_TO_CLIENT_SIGNAL_VALUE	[mcx_hlsl	XDS:000:000:000]	0x001
mcx_event	SIGNALMAPPING_0_HLSD_TO_CLIENT_SIGNAL_VALUE	[mcx_hlsl	XDS:000:000:001]	0x002
mcx_event	SIGNALMAPPING_0_HLSD_TO_CLIENT_SIGNAL_VALUE	[mcx_hlsl	XDS:000:000:002]	0x003
mcx_event	SIGNALMAPPING_0_HLSD_TO_CLIENT_SIGNAL_VALUE	[mcx_hlsl	XDS:000:000:003]	0x004
mcx_event	SIGNALMAPPING_0_HLSD_TO_CLIENT_SIGNAL_VALUE	[mcx_hlsl	XDS:000:000:004]	0x005
mcx_event	SIGNALMAPPING_0_HLSD_TO_CLIENT_SIGNAL_VALUE	[mcx_hlsl	XDS:000:000:005]	0x006
mcx_event	SIGNALMAPPING_0_HLSD_TO_CLIENT_SIGNAL_VALUE	[mcx_hlsl	XDS:000:000:006]	0x007
mcx_event	SIGNALMAPPING_0_HLSD_TO_CLIENT_SIGNAL_VALUE	[mcx_hlsl	XDS:000:000:007]	0x008
mcx_event	SIGNALMAPPING_0_HLSD_TO_CLIENT_SIGNAL_VALUE	[mcx_hlsl	XDS:000:000:008]	0x009
mcx_event	SIGNALMAPPING_0_HLSD_TO_CLIENT_SIGNAL_VALUE	[mcx_hlsl	XDS:000:000:009]	0x00A
mcx_event	SIGNALMAPPING_0_HLSD_TO_CLIENT_SIGNAL_VALUE	[mcx_hlsl	XDS:000:000:010]	0x00B
mcx_event	SIGNALMAPPING_0_HLSD_TO_CLIENT_SIGNAL_VALUE	[mcx_hlsl	XDS:000:000:011]	0x00C
mcx_event	SIGNALMAPPING_0_HLSD_TO_CLIENT_SIGNAL_VALUE	[mcx_hlsl	XDS:000:000:012]	0x00D
mcx_event	SIGNALMAPPING_0_HLSD_TO_CLIENT_SIGNAL_VALUE	[mcx_hlsl	XDS:000:000:013]	0x00E
mcx_event	SIGNALMAPPING_0_HLSD_TO_CLIENT_SIGNAL_VALUE	[mcx_hlsl	XDS:000:000:014]	0x00F
mcx_event	SIGNALMAPPING_0_HLSD_TO_CLIENT_SIGNAL_VALUE	[mcx_hlsl	XDS:000:000:015]	0x010
mcx_event	SIGNALMAPPING_0_HLSD_TO_CLIENT_SIGNAL_VALUE	[mcx_hlsl	XDS:000:000:016]	0x011
mcx_event	SIGNALMAPPING_0_HLSD_TO_CLIENT_SIGNAL_VALUE	[mcx_hlsl	XDS:000:000:017]	0x012
mcx_event	SIGNALMAPPING_0_HLSD_TO_CLIENT_SIGNAL_VALUE	[mcx_hlsl	XDS:000:000:018]	0x013
mcx_event	SIGNALMAPPING_0_HLSD_TO_CLIENT_SIGNAL_VALUE	[mcx_hlsl	XDS:000:000:019]	0x014
mcx_event	SIGNALMAPPING_0_HLSD_TO_CLIENT_SIGNAL_VALUE	[mcx_hlsl	XDS:000:000:020]	0x015
mcx_event	SIGNALMAPPING_0_HLSD_TO_CLIENT_SIGNAL_VALUE	[mcx_hlsl	XDS:000:000:021]	0x016
mcx_event	SIGNALMAPPING_0_HLSD_TO_CLIENT_SIGNAL_VALUE	[mcx_hlsl	XDS:000:000:022]	0x017
mcx_event	SIGNALMAPPING_0_HLSD_TO_CLIENT_SIGNAL_VALUE	[mcx_hlsl	XDS:000:000:023]	0x018
mcx_event	SIGNALMAPPING_0_HLSD_TO_CLIENT_SIGNAL_VALUE	[mcx_hlsl	XDS:000:000:024]	0x019
mcx_event	SIGNALMAPPING_0_HLSD_TO_CLIENT_SIGNAL_VALUE	[mcx_hlsl	XDS:000:000:025]	0x01A
mcx_event	SIGNALMAPPING_0_HLSD_TO_CLIENT_SIGNAL_VALUE	[mcx_hlsl	XDS:000:000:026]	0x01B
mcx_event	SIGNALMAPPING_0_HLSD_TO_CLIENT_SIGNAL_VALUE	[mcx_hlsl	XDS:000:000:027]	0x01C
mcx_event	SIGNALMAPPING_0_HLSD_TO_CLIENT_SIGNAL_VALUE	[mcx_hlsl	XDS:000:000:028]	0x01D
mcx_event	SIGNALMAPPING_0_HLSD_TO_CLIENT_SIGNAL_VALUE	[mcx_hlsl	XDS:000:000:029]	0x01E
mcx_event	SIGNALMAPPING_0_HLSD_TO_CLIENT_SIGNAL_VALUE	[mcx_hlsl	XDS:000:000:030]	0x01F
mcx_event	SIGNALMAPPING_0_HLSD_TO_CLIENT_SIGNAL_VALUE	[mcx_hlsl	XDS:000:000:031]	0x020
mcx_event	SIGNALMAPPING_0_HLSD_TO_CLIENT_SIGNAL_VALUE	[mcx_hlsl	XDS:000:000:032]	0x021
mcx_event	SIGNALMAPPING_0_HLSD_TO_CLIENT_SIGNAL_VALUE	[mcx_hlsl	XDS:000:000:033]	0x022
mcx_event	SIGNALMAPPING_0_HLSD_TO_CLIENT_SIGNAL_VALUE	[mcx_hlsl	XDS:000:000:034]	0x023
mcx_event	SIGNALMAPPING_0_HLSD_TO_CLIENT_SIGNAL_VALUE	[mcx_hlsl	XDS:000:000:035]	0x024
mcx_event	SIGNALMAPPING_0_HLSD_TO_CLIENT_SIGNAL_VALUE	[mcx_hlsl	XDS:000:000:036]	0x025
mcx_event	SIGNALMAPPING_0_HLSD_TO_CLIENT_SIGNAL_VALUE	[mcx_hlsl	XDS:000:000:037]	0x026
mcx_event	SIGNALMAPPING_0_HLSD_TO_CLIENT_SIGNAL_VALUE	[mcx_hlsl	XDS:000:000:038]	0x027
mcx_event	SIGNALMAPPING_0_HLSD_TO_CLIENT_SIGNAL_VALUE	[mcx_hlsl	XDS:000:000:039]	0x028
mcx_event	SIGNALMAPPING_0_HLSD_TO_CLIENT_SIGNAL_VALUE	[mcx_hlsl	XDS:000:000:040]	0x029

Seite 26 / 33

Seite 27 / 33

Seite 28 / 33

mcx_event	SIGNALMAPPING_0_HLSD_TO_CLIENT_SIGNAL_VALUE	[mcx_hlsc XDS:000:001:065]	0x142
mcx_event	SIGNALMAPPING_0_HLSD_TO_CLIENT_SIGNAL_VALUE	[mcx_hlsc XDS:000:001:066]	0x143
mcx_event	SIGNALMAPPING_0_HLSD_TO_CLIENT_SIGNAL_VALUE	[mcx_hlsc XDS:000:001:067]	0x144
mcx_event	SIGNALMAPPING_0_HLSD_TO_CLIENT_SIGNAL_VALUE	[mcx_hlsc XDS:000:001:068]	0x145
mcx_event	SIGNALMAPPING_0_HLSD_TO_CLIENT_SIGNAL_VALUE	[mcx_hlsc XDS:000:001:069]	0x146
mcx_event	SIGNALMAPPING_0_HLSD_TO_CLIENT_SIGNAL_VALUE	[mcx_hlsc XDS:000:001:070]	0x147
mcx_event	SIGNALMAPPING_0_HLSD_TO_CLIENT_SIGNAL_VALUE	[mcx_hlsc XDS:000:001:071]	0x148
mcx_event	SIGNALMAPPING_0_HLSD_TO_CLIENT_SIGNAL_VALUE	[mcx_hlsc XDS:000:001:072]	0x149
mcx_event	SIGNALMAPPING_0_HLSD_TO_CLIENT_SIGNAL_VALUE	[mcx_hlsc XDS:000:001:073]	0x14A
mcx_event	SIGNALMAPPING_0_HLSD_TO_CLIENT_SIGNAL_VALUE	[mcx_hlsc XDS:000:001:074]	0x14B
mcx_event	SIGNALMAPPING_0_HLSD_TO_CLIENT_SIGNAL_VALUE	[mcx_hlsc XDS:000:001:075]	0x14C
mcx_event	SIGNALMAPPING_0_HLSD_TO_CLIENT_SIGNAL_VALUE	[mcx_hlsc XDS:000:001:076]	0x14D
mcx_event	SIGNALMAPPING_0_HLSD_TO_CLIENT_SIGNAL_VALUE	[mcx_hlsc XDS:000:001:077]	0x14E
mcx_event	SIGNALMAPPING_0_HLSD_TO_CLIENT_SIGNAL_VALUE	[mcx_hlsc XDS:000:001:078]	0x14F
mcx_event	SIGNALMAPPING_0_HLSD_TO_CLIENT_SIGNAL_VALUE	[mcx_hlsc XDS:000:001:079]	0x150
mcx_event	SIGNALMAPPING_0_HLSD_TO_CLIENT_SIGNAL_VALUE	[mcx_hlsc XDS:000:001:080]	0x151
mcx_event	SIGNALMAPPING_0_HLSD_TO_CLIENT_SIGNAL_VALUE	[mcx_hlsc XDS:000:001:081]	0x152
mcx_event	SIGNALMAPPING_0_HLSD_TO_CLIENT_SIGNAL_VALUE	[mcx_hlsc XDS:000:001:082]	0x153
mcx_event	SIGNALMAPPING_0_HLSD_TO_CLIENT_SIGNAL_VALUE	[mcx_hlsc XDS:000:001:083]	0x154
mcx_event	SIGNALMAPPING_0_HLSD_TO_CLIENT_SIGNAL_VALUE	[mcx_hlsc XDS:000:001:084]	0x155
mcx_event	SIGNALMAPPING_0_HLSD_TO_CLIENT_SIGNAL_VALUE	[mcx_hlsc XDS:000:001:085]	0x156
mcx_event	SIGNALMAPPING_0_HLSD_TO_CLIENT_SIGNAL_VALUE	[mcx_hlsc XDS:000:001:086]	0x157
mcx_event	SIGNALMAPPING_0_HLSD_TO_CLIENT_SIGNAL_VALUE	[mcx_hlsc XDS:000:001:087]	0x158
mcx_event	SIGNALMAPPING_0_HLSD_TO_CLIENT_SIGNAL_VALUE	[mcx_hlsc XDS:000:001:088]	0x159
mcx_event	SIGNALMAPPING_0_HLSD_TO_CLIENT_SIGNAL_VALUE	[mcx_hlsc XDS:000:001:089]	0x15A
mcx_event	SIGNALMAPPING_0_HLSD_TO_CLIENT_SIGNAL_VALUE	[mcx_hlsc XDS:000:001:090]	0x15B
mcx_event	SIGNALMAPPING_0_HLSD_TO_CLIENT_SIGNAL_VALUE	[mcx_hlsc XDS:000:001:091]	0x15C
mcx_event	SIGNALMAPPING_0_HLSD_TO_CLIENT_SIGNAL_VALUE	[mcx_hlsc XDS:000:001:092]	0x15D
mcx_event	SIGNALMAPPING_0_HLSD_TO_CLIENT_SIGNAL_VALUE	[mcx_hlsc XDS:000:001:093]	0x15E
mcx_event	SIGNALMAPPING_0_HLSD_TO_CLIENT_SIGNAL_VALUE	[mcx_hlsc XDS:000:001:094]	0x15F
mcx_event	SIGNALMAPPING_0_HLSD_TO_CLIENT_SIGNAL_VALUE	[mcx_hlsc XDS:000:001:095]	0x160
mcx_event	SIGNALMAPPING_0_HLSD_TO_CLIENT_SIGNAL_VALUE	[mcx_hlsc XDS:000:001:096]	0x161
mcx_event	SIGNALMAPPING_0_HLSD_TO_CLIENT_SIGNAL_VALUE	[mcx_hlsc XDS:000:001:097]	0x162
mcx_event	SIGNALMAPPING_0_HLSD_TO_CLIENT_SIGNAL_VALUE	[mcx_hlsc XDS:000:001:098]	0x163
mcx_event	SIGNALMAPPING_0_HLSD_TO_CLIENT_SIGNAL_VALUE	[mcx_hlsc XDS:000:001:099]	0x164
mcx_event	SIGNALMAPPING_0_HLSD_TO_CLIENT_SIGNAL_VALUE	[mcx_hlsc XDS:000:001:100]	0x165
mcx_event	SIGNALMAPPING_0_HLSD_TO_CLIENT_SIGNAL_VALUE	[mcx_hlsc XDS:000:001:101]	0x166
mcx_event	SIGNALMAPPING_0_HLSD_TO_CLIENT_SIGNAL_VALUE	[mcx_hlsc XDS:000:001:102]	0x167
mcx_event	SIGNALMAPPING_0_HLSD_TO_CLIENT_SIGNAL_VALUE	[mcx_hlsc XDS:000:001:103]	0x168

#Mapping table 01: In [shortDelay - Compressor - Fader - Limiter]

mcx_event	SIGNALMAPPING_0_HLSD_TO_CLIENT_SIGNAL_VALUE	[mcx_hlsc XDS:001:000:000]	0x0169
mcx_event	SIGNALMAPPING_0_HLSD_TO_CLIENT_SIGNAL_VALUE	[mcx_hlsc XDS:001:000:001]	0x016A
mcx_event	SIGNALMAPPING_0_HLSD_TO_CLIENT_SIGNAL_VALUE	[mcx_hlsc XDS:001:000:002]	0x016B
mcx_event	SIGNALMAPPING_0_HLSD_TO_CLIENT_SIGNAL_VALUE	[mcx_hlsc XDS:001:000:003]	0x016C
mcx_event	SIGNALMAPPING_0_HLSD_TO_CLIENT_SIGNAL_VALUE	[mcx_hlsc XDS:001:000:004]	0x016D
mcx_event	SIGNALMAPPING_0_HLSD_TO_CLIENT_SIGNAL_VALUE	[mcx_hlsc XDS:001:000:005]	0x016E
mcx_event	SIGNALMAPPING_0_HLSD_TO_CLIENT_SIGNAL_VALUE	[mcx_hlsc XDS:001:000:006]	0x016F
mcx_event	SIGNALMAPPING_0_HLSD_TO_CLIENT_SIGNAL_VALUE	[mcx_hlsc XDS:001:000:007]	0x0170
mcx_event	SIGNALMAPPING_0_HLSD_TO_CLIENT_SIGNAL_VALUE	[mcx_hlsc XDS:001:000:008]	0x0171
mcx_event	SIGNALMAPPING_0_HLSD_TO_CLIENT_SIGNAL_VALUE	[mcx_hlsc XDS:001:000:009]	0x0172
mcx_event	SIGNALMAPPING_0_HLSD_TO_CLIENT_SIGNAL_VALUE	[mcx_hlsc XDS:001:000:010]	0x0173
mcx_event	SIGNALMAPPING_0_HLSD_TO_CLIENT_SIGNAL_VALUE	[mcx_hlsc XDS:001:000:011]	0x0174
mcx_event	SIGNALMAPPING_0_HLSD_TO_CLIENT_SIGNAL_VALUE	[mcx_hlsc XDS:001:000:012]	0x0175
mcx_event	SIGNALMAPPING_0_HLSD_TO_CLIENT_SIGNAL_VALUE	[mcx_hlsc XDS:001:000:013]	0x0176
mcx_event	SIGNALMAPPING_0_HLSD_TO_CLIENT_SIGNAL_VALUE	[mcx_hlsc XDS:001:000:014]	0x0177

Seite 30 / 33

Seite 31 / 33

mcx_event	SIGNALMAPPING_0_HLSD_TO_CLIENT_SIGNAL_VALUE	[mcx_hlsd XDS:001:000:183]	0x0220
mcx_event	SIGNALMAPPING_0_HLSD_TO_CLIENT_SIGNAL_VALUE	[mcx_hlsd XDS:001:000:184]	0x0221
mcx_event	SIGNALMAPPING_0_HLSD_TO_CLIENT_SIGNAL_VALUE	[mcx_hlsd XDS:001:000:185]	0x0222
mcx_event	SIGNALMAPPING_0_HLSD_TO_CLIENT_SIGNAL_VALUE	[mcx_hlsd XDS:001:000:186]	0x0223
mcx_event	SIGNALMAPPING_0_HLSD_TO_CLIENT_SIGNAL_VALUE	[mcx_hlsd XDS:001:000:187]	0x0224
mcx_event	SIGNALMAPPING_0_HLSD_TO_CLIENT_SIGNAL_VALUE	[mcx_hlsd XDS:001:000:188]	0x0225
mcx_event	SIGNALMAPPING_0_HLSD_TO_CLIENT_SIGNAL_VALUE	[mcx_hlsd XDS:001:000:189]	0x0226
mcx_event	SIGNALMAPPING_0_HLSD_TO_CLIENT_SIGNAL_VALUE	[mcx_hlsd XDS:001:000:190]	0x0227
mcx_event	SIGNALMAPPING_0_HLSD_TO_CLIENT_SIGNAL_VALUE	[mcx_hlsd XDS:001:000:191]	0x0228