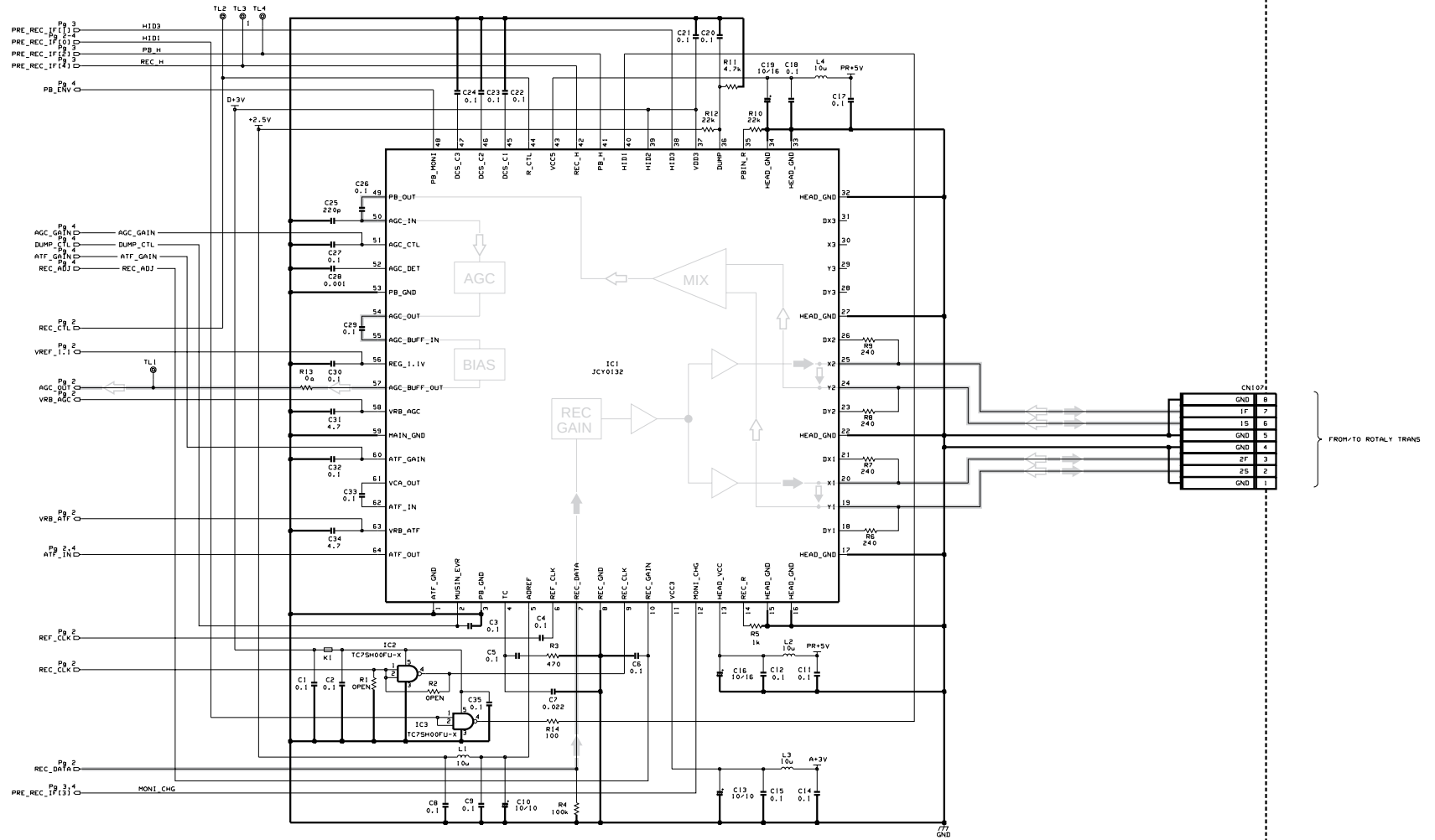


DV/CPU



DV/CPU SCHEMATIC DIAGRAM 2/4

