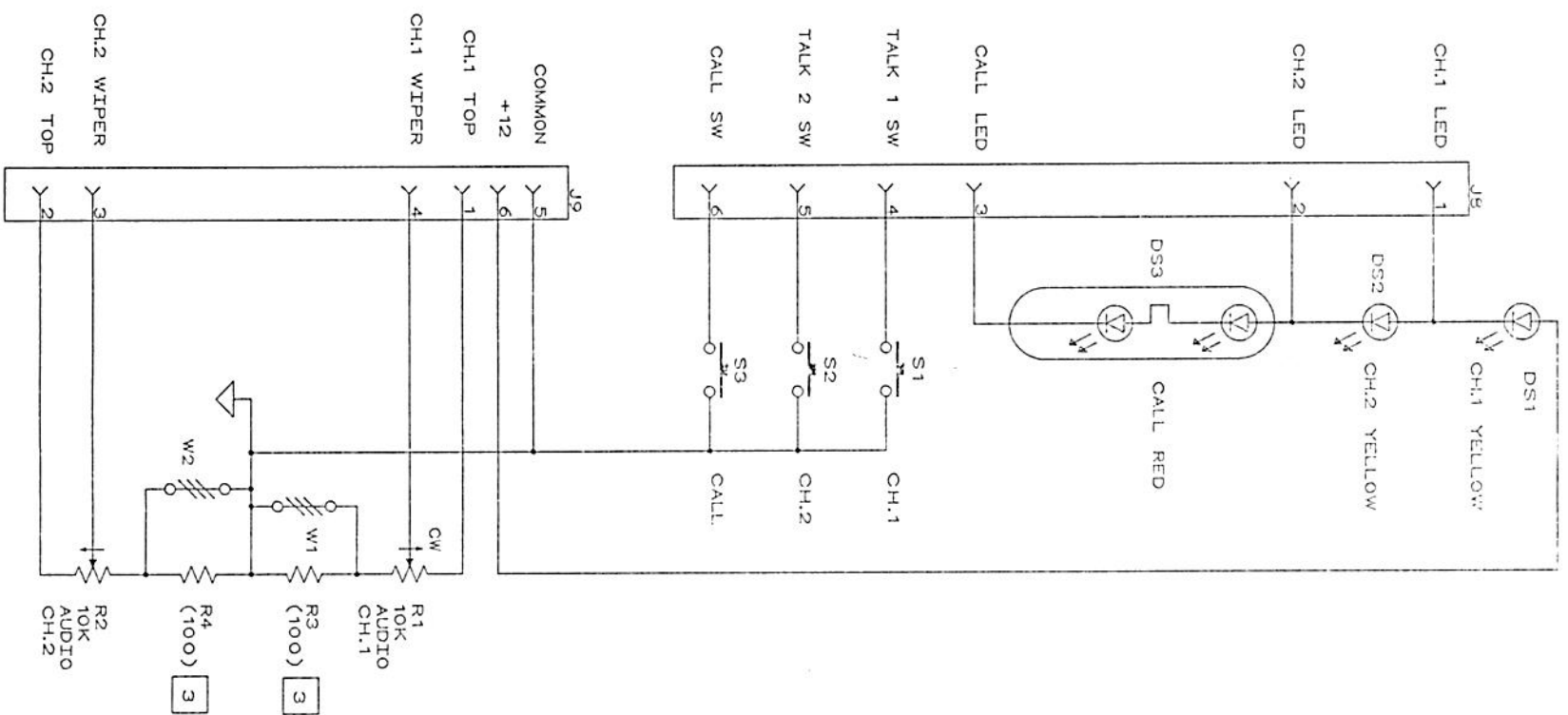
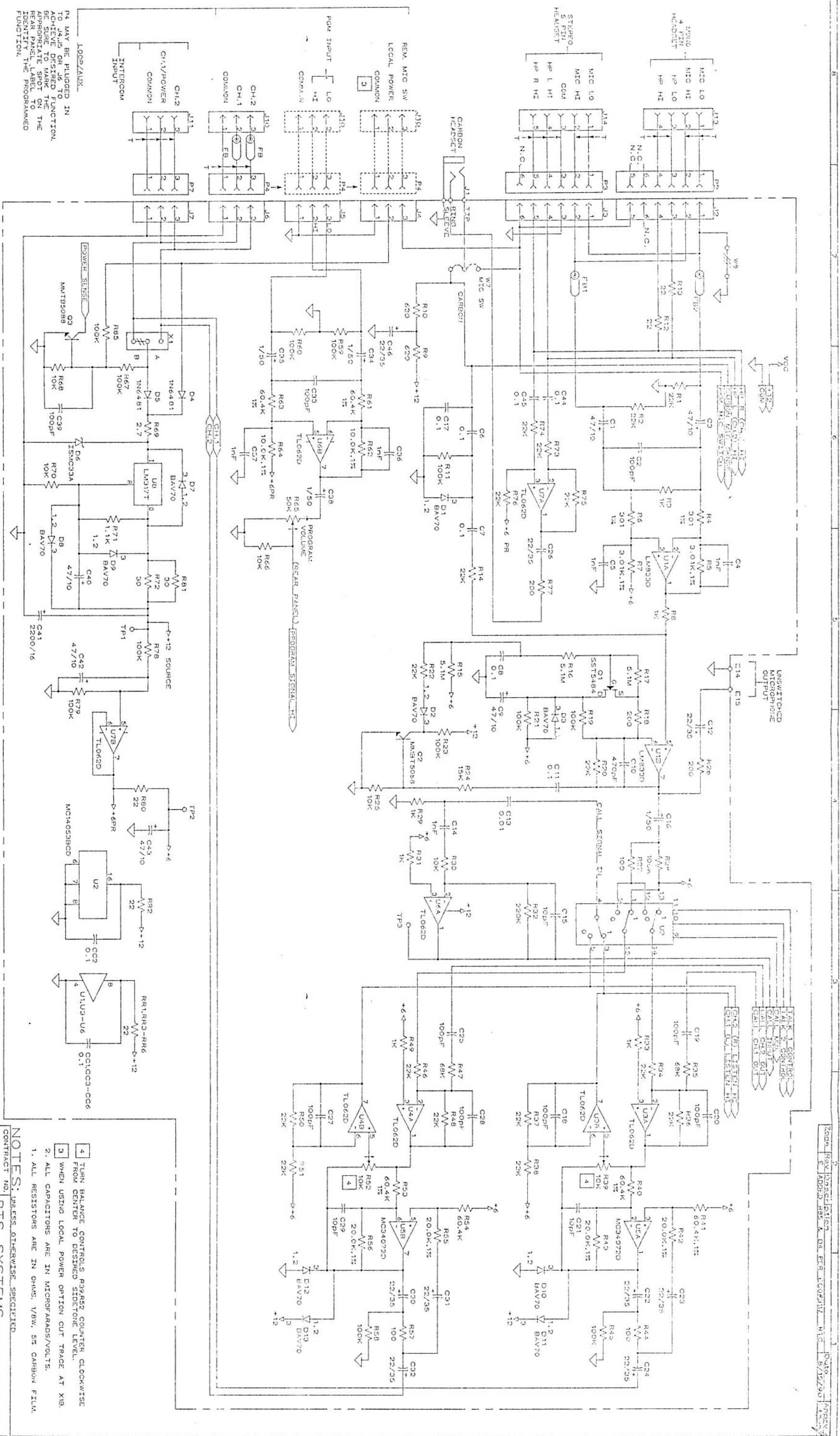




- NOTES: UNLESS OTHERWISE SPECIFIED
1. ALL RESISTORS ARE IN OHMS, 1/8W, 5% CARBON FILM.
 2. ALL CAPACITORS ARE IN MICROFARADS/VOLTS.
 3. R3/R4 NORMALLY NOT INSTALLED. W1/W2 ARE CUTTABLE TRACES.

CONTRACT NO.		RTS SYSTEMS		Burbank, California	
Drawn		R. NETTLESON		SCHEMATIC DIAGRAM, FRONT PANEL BOARD.	
Checked		MODEL BP325SM			
Issued		SIZE FSCM		DWG NO	
		C 60572		SD6634	
DATE 2-13-90		2		S6634A01	
				SHEET 1 of 1	



P4 MAY BE PLUGGED IN TO J4,J5 OR J6 TO ACHIEVE DESIRED FUNCTION. BE SURE TO MARK THE APPROPRIATE SPOT ON THE REAR PANEL LABEL TO IDENTIFY THE PROGRAMMED FUNCTION.

LAST USED: (SHEET 1) C46.D13.FB2.J7.Q3.R84.CC6.UE
NOT USED: (SHEET 1) C47-C99.Q4-Q9.R86-R99.U9

NOTES: UNLESS OTHERWISE SPECIFIED

CONTRACT NO. **RTS SYSTEMS** Burbank, California

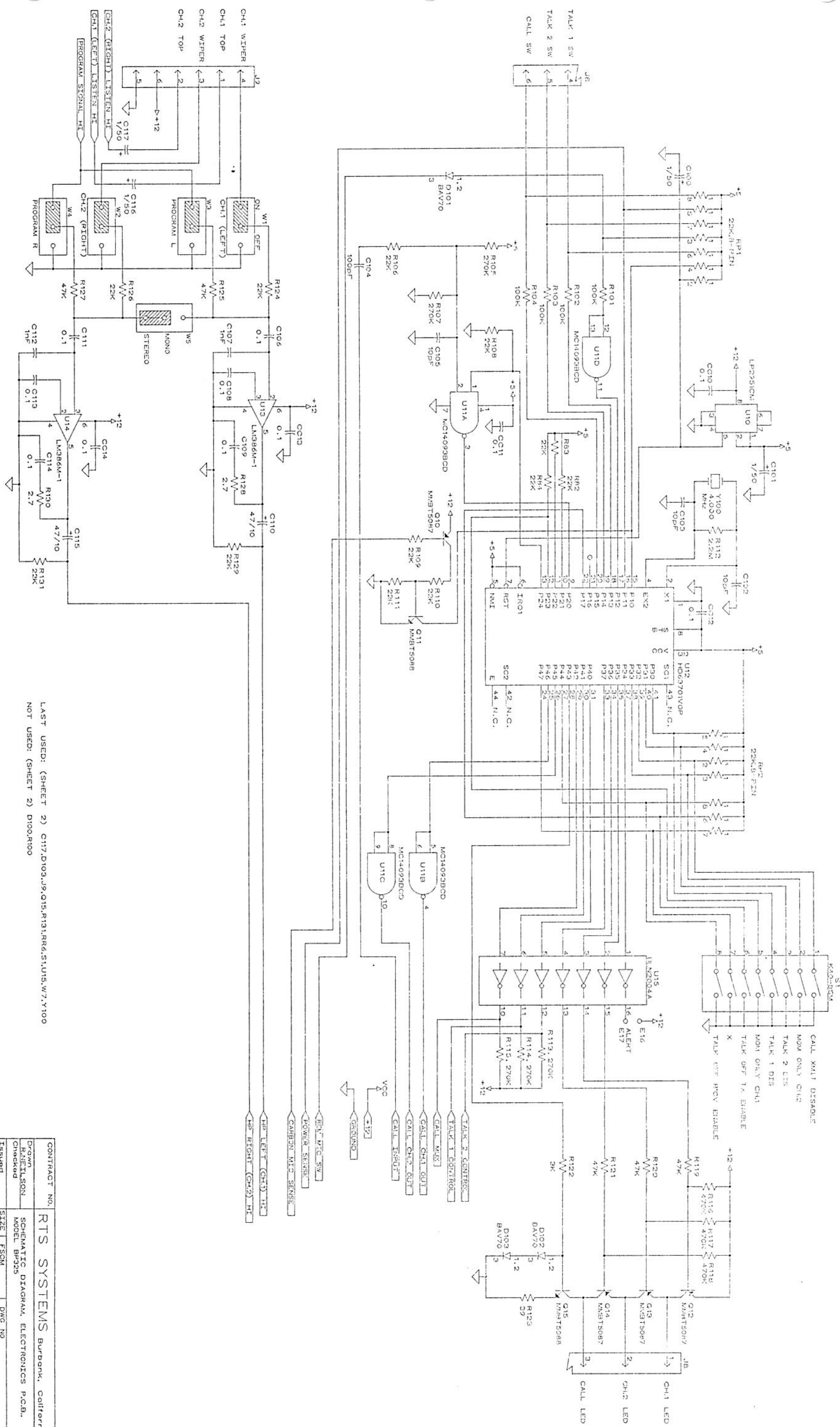
DRAWN R. NEILSON	SCHEMATIC DIAGRAM, ELECTRONICS PCB, MODEL 8000
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R. NEALSON
Checked

Issued

D	60572	SD6635	E
DATE 2-15-90		SHEET 1 of 2	

2006	REV	DESCRIPTION	DATE	APPROVED
E		ADD'D H85 & D4 PER EGO#3117	8/15/90	4/2/97



LAST USED: (SHEET 2) C11:7.D103.J9.Q15.R131.RR6.S1.U15.W7.Y100
NOT USED: (SHEET 2) D100.R100

CONTRACT NO.		RTS SYSTEMS		Burbank, California	
DRAWN R. ELLISON CHECKED		SCHEMATIC DIAGRAM, ELECTRONICS P.C.B., MODEL BP225			
Issued		SIZE		REV	
		D 60572		SD6635 E	
		FSCM		DWG NO	
		DATE 2-13-90		66035-02 1 SHEET 2 of 2	