# KP98-7 Pushbutton Latch Disable Patching

See also working calculator.  Load file KP98 Latch Disable Calc.XLS.

At the start of the EPROM (offset 0) are 32 bytes of patch table, all (initially) hex "0F". Each byte represents 4 keys (beginning with the least significant bit (0) of the word); a set bit indicates latch enable, a clear bit indicates latch disable. So offset 0 bit 0 is talk key 1 (main panel); bit 1 is talk key 2; etc. ( 1= enabled, 0 Default is"enabled" for all buttons except Expansion panel talk keys.

The first 4 bytes represent main panel talk keys. Next 4 bytes represent expansion panel talk keys. Next 8 bytes are reserved. Next 4 bytes beginning at (offset 10) represent main panel listen keys. The rest of the table is reserved (the only expansion panel supported on the KP98 has no listen keys).

## EMP-20  BUFFER  EDITOR
### - Modified Screen View -

KP98-7 Firmware ver 8.3, default base checksum 8043

Chipset 27C128, file offset 0000c000

Start Location = 00000000

Expansion TALK keys are latch disabled by dfault

| BYTE > | (1) | (2) | (3) | (4) | (5) | (6) | (7) | (8) | (9) | (10) | (11) | (12) | (13) | (14) | (15) | (16) |
|---|---|---|---|---|---|---|---|---|---|---|---|---|---|---|---|---|
| ^QWERTYU | 0 | 1 | 2 | 3 | 4 | 5 | 6 | 7 | 8 | 9 | A | B | C | D | E | F |
| | BYTES 1-4 → | | | | BYTES 5-8 → | | | | BYTES 9-16 | | | | | | | |
| ( binary ) > | 1111 | 1111 | 1111 | 1111 | 0000 | 0000 | 0000 | 0000 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 |
| 00000000 | 0F | 0F | 0F | 0F | 00 | 00 | 00 | 00 | 0F | 0F | 0F | 0F | 0F | 0F | 0F | 0F |
| | 1st 4 TK keys main panel | 2nd 4 TK keys main panel | 3rd 4 TK keys main panel | 4th 4 TK keys main panel | 1st 4 TK keys exp panel | 2nd 4 TK keys exp panel | 3rd 4 TK keys exp panel | 4th 4 TK keys exp panel | RESERVED | | | | | | | |

| | (1) | (2) | (3) | (4) | BYTES 21-32 | | | | | | | | | | | |
| | BYTES 17-20 → | | | | | | | | | | | | | | | |
| ( binary ) > | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 | 1111 |
| 00000010 | 0F | 0F | 0F | 0F | 0F | 0F | 0F | 0F | 0F | 0F | 0F | 0F | 0F | 0F | 0F | 0F |
| | 1st 4 LSN keys main panel | 2nd 4 LSN keys main panel | 3rd 4 LSN keys main panel | 4th 4 LSN keys main panel | RESERVED | | | | | | | | | | | |

## E X A M P L E

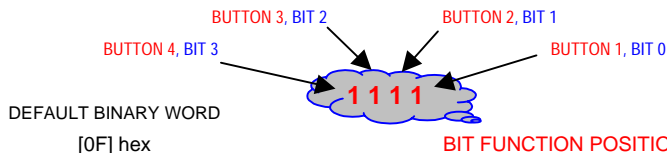| BYTE > | (1) | (2) | (3) | (4) |
|---|---|---|---|---|
| ^QWERTYU | 0 | 1 | 2 | 3 |
| | BYTES 1-4 → | | | |
| ( binary ) > | 0011 | 1100 | 1111 | 1111 |
| 00000000 | 03 | 0C | 0F | 0F |
| | 1st 4 TK keys main panel | 2nd 4 TK keys main panel | 3rd 4 TK keys main panel | 4th 4 TK keys main panel |

Buttons 3,4 LATCH DISABLED

Buttons 5,6 LATCH DISABLED

The example to the left shows the **talk keys** for positions 3-6 are **latch diabled**. ( 1=enabled, 0 = disabled )

Note that in byte 1, bit 2 and 3 are set to "0" (disabled) as well as byte 2, bit 0 and 1 are also set to "0".

Convert binary word to hex and enter the hex value in the appropriate location.

Notice binary 0011 = 03 hex and binary 1100 = 0C hex and binary 1111 remains "0F".

BUTTON 3, BIT 2    BUTTON 2, BIT 1

BUTTON 4, BIT 3    BUTTON 1, BIT 0

1 1 1 1

DEFAULT BINARY WORD

[0F] hex    BIT FUNCTION POSITION

*[0F] = latch enabled for entire group of 4 buttons.*

Revised 6-30-99

KP98 latch disable.XLS

Set KP98-7 DIP switches 1,2,3 & 8 per changes noted on Keypanel Programming Guide ADAMDIP.XLS