XCP—ADAM—MC   VIEW FROM CONNECTOR SIDE

1.75"

J1 TO J10 ARE DE-9-S, FEMALE 9 PIN " D " SUB.
J11 IS DB-25-S, FEMALE 25 PIN " D " SUB

19.00"

XCP—ADAM—MC

J 1   CSEDIT

PIN	ASSIGNMENT 2-W	ASSIGNMENT 4-W
1	RS485 TX/RX −	RS422 RX −
2	RS232C RX	
3	RS232C TX	
4	RS422 TX −	RS422 TX −
5	GROUND	
6	GROUND	
7	RS422 TX +	RS422 TX +
8	RS485 TX/RX +	RS422 RX +
9	NOT USED	

J 2   TRUNKING SYSTEM

PIN	ASSIGNMENT 2-W	ASSIGNMENT 4-W
1	RS485 TX/RX −	RS422 RX −
2	GROUND	
3	RS232C RX	
4	NOT USED	
5	RS422 TX +	RS422 TX +
6	RS485 TX/RX +	RS422 RX +
7	GROUND	
8	RS232C TX	
9	RS422 TX −	RS422 TX −

J 3   UIO-256 & PAP

PIN	ASSIGNMENT 2-W	ASSIGNMENT 4-W
1	RS485 TX/RX −	RS422 RX −
2	GROUND	
3	NOT USED	
4	NOT USED	
5	RS422 TX +	RS422 TX +
6	RS485 TX/RX +	RS422 RX +
7	GROUND	
8	NOT USED	
9	RS422 TX −	RS422 TX −

J 4   GENERAL PURPOSE

PIN	ASSIGNMENT 2-W	ASSIGNMENT 4-W
1	RS485 TX/RX −	RS422 RX −
2	GROUND	
3	NOT USED	
4	NOT USED	
5	RS422 TX +	RS422 TX +
6	RS485 TX/RX +	RS422 RX +
7	GROUND	
8	NOT USED	
9	RS422 TX −	RS422 TX −

J 5   GENERAL PURPOSE

PIN	ASSIGNMENT 2-W
1	RS485 TX/RX −
2	GROUND
3	NOT USED
4	NOT USED
5	NOT USED
6	RS485 TX/RX +
7	GROUND
8	NOT USED
9	NOT USED

J 6   GENERAL PURPOSE

PIN	ASSIGNMENT 2-W
1	RS485 TX/RX −
2	GROUND
3	NOT USED
4	NOT USED
5	NOT USED
6	RS485 TX/RX +
7	GROUND
8	NOT USED
9	NOT USED

J 7   GENERAL PURPOSE

PIN	ASSIGNMENT 2-W
1	RS485 TX/RX −
2	GROUND
3	NOT USED
4	NOT USED
5	NOT USED
6	RS485 TX/RX +
7	GROUND
8	NOT USED
9	NOT USED

J 8   GENERAL PURPOSE

PIN	ASSIGNMENT 2-W
1	RS485 TX/RX −
2	GROUND
3	NOT USED
4	NOT USED
5	NOT USED
6	RS485 TX/RX +
7	GROUND
8	NOT USED
9	NOT USED

J 9   DEBUG CONNECTOR "A"

PIN	ASSIGNMENT 2-W
1	NOT USED
2	GROUND
3	RS232C RX
4	NOT USED
5	NOT USED
6	NOT USED
7	GROUND
8	RS232C TX
9	NOT USED

J 10   DEBUG CONNECTOR "B"

PIN	ASSIGNMENT 2-W
1	NOT USED
2	GROUND
3	RS232C RX
4	NOT USED
5	NOT USED
6	NOT USED
7	GROUND
8	RS232C TX
9	NOT USED

J 11   MISCELLANEOUS I/O CONNECTOR

PIN	ASSIGNMENT	SIGNAL
1	MI−0	LOGIC INPUT 0
2	MI−1	LOGIC INPUT 1
3	MI−2	LOGIC INPUT 2
4	MI−3	LOGIC INPUT 3
5	MI−4	LOGIC INPUT 4
6	MI−5	LOGIC INPUT 5
7	MI−6	LOGIC INPUT 6
8	MI−7	LOGIC INPUT 7
9	GROUND	GROUND
10	GROUND	GROUND
11	GROUND	GROUND
12	GROUND	GROUND
13	GROUND	GROUND
14	MO−0	LOGIC OUTPUT 0
15	MO−1	LOGIC OUTPUT 1
16	MO−2	LOGIC OUTPUT 2
17	MO−3	LOGIC OUTPUT 3
18	MO−4	LOGIC OUTPUT 4
19	MO−5	LOGIC OUTPUT 5
20	MO−6	LOGIC OUTPUT 6
21	MO−7	LOGIC OUTPUT 7
22	GROUND	GROUND
23	GROUND	GROUND
24	GROUND	GROUND
25	GROUND	GROUND