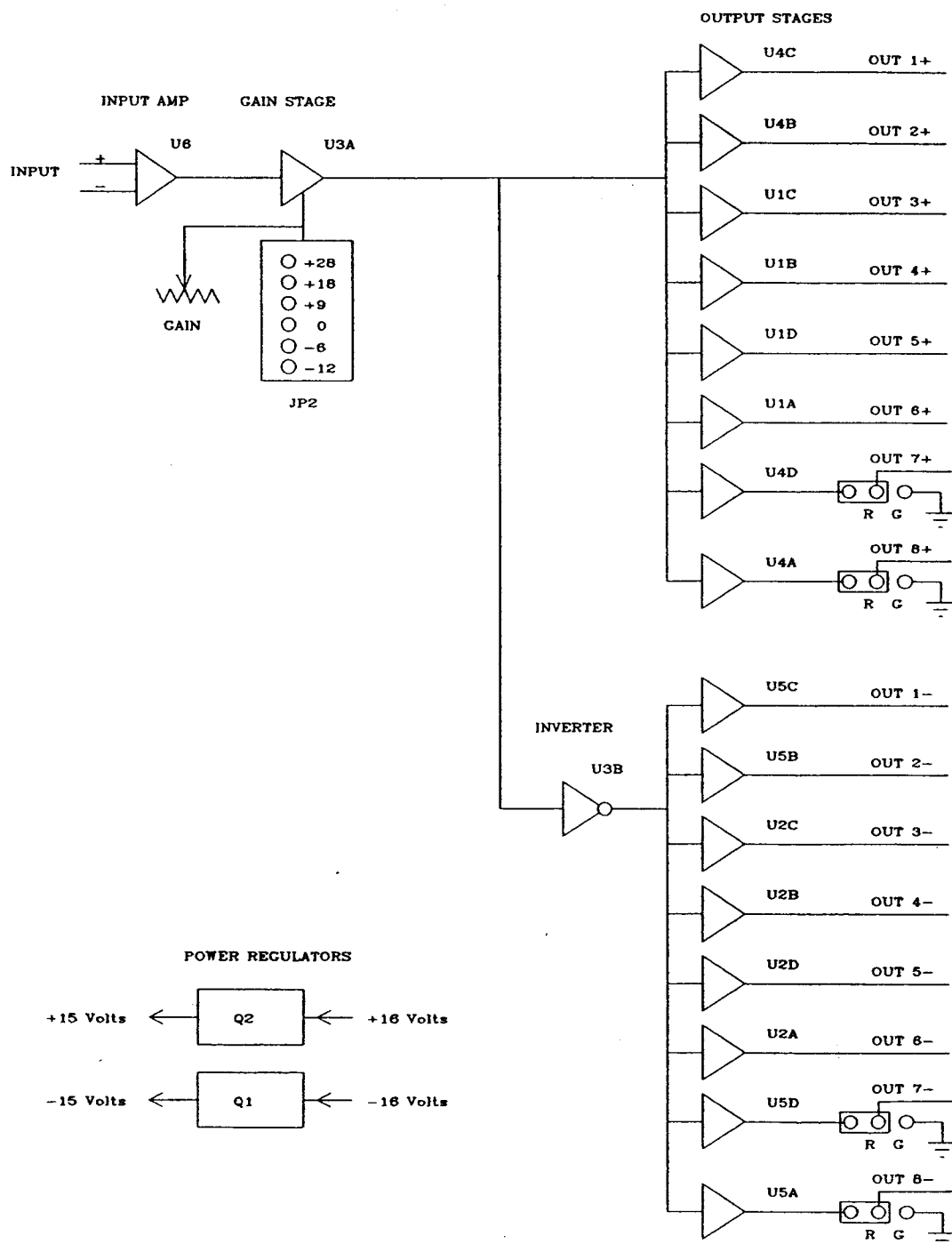


**ADA-7551**  
**Audio Distribution Amplifier**

AM-7551-03

# ADA 7551 BLOCK DIAGRAM



## Introduction

The Ross ADA-7551 Audio Distribution Amplifier provides a means of amplifying and distributing program-level audio with virtually no loss of quality. Use of the latest types of integrated circuits which are developed specifically suited to professional audio applications assures a very low level of distortion and noise.

## Installation

The card TRAY TYPE jumper (JP1) settings must be set before use. All jumper plugs must be set in a vertical row corresponding with the tray type designation as follows:



Ross Trays



GVG+ Trays

### CAUTION:

If JP1 is not set correctly for a GVG tray, damage to the integrated circuits may occur.

## Operation

Controls are provided for setting the gain of the amplifier.

The GAIN jumper plug is to be set for the desired gain range. In most installations it will be set to the 0 dB position to obtain unity gain.

The GAIN potentiometer provides a fine control of the gain and has a range of +/- 6dB.

There is no need to set the common mode BAL adjustment as it has been precisely factory pre-set and sealed. This amplifier incorporates an advanced type of input stage that does not have to be balanced for each installation.

## Circuit Description

The input stage, U6, performs the function of converting the balanced input signal into an unbalanced (single ended) signal for further processing.

This stage is unique because it functions exactly as if the amplifier had a floating transformer input. This means that it responds only to the voltage difference between the two input lines and ignores any imbalance to ground. This gives the distribution amplifier the very desirable ability to accept any out-of-balance input signal and produce a perfectly balanced output. As a consequence, it also has an exceptional ability to reject common mode hum and noise over the whole audio band.

The input circuit includes diodes to protect the amplifier against damaging input noise spikes.

The amplifier gain is provided by circuit U3A. JP2 selects the desired gain range and RV1 provides vernier gain adjustment.

\*GVG is a trademark of The Green Valley Group Inc.

The positive line outputs are driven by unity-gain stages U1 and U4. These circuits are designed to be very stable and not oscillate at any normal line load.

The negative output stages U2 and U5 are driven by unity-gain inverter U3B.

The power input voltage of approximately +/- 16 volts is reduced to +/- 15 volts by transistors Q2 and Q1. This lower voltage powers the input and gain stages while the output stages are fed directly from the power supply.

### **Alignment**

The only alignment controls provided are for balancing the common-mode rejection of the input amplifiers. To adjust the BAL control, make the indicated connections to the terminal block associated with the particular amplifier.

#### **NOTE:**

Because this control has been precisely calibrated at the factory, it must not be adjusted unless U6 has been replaced.

1. Place the amplifier on the extender board.
2. Use a short jumper wire to connect the tray "input +" and "input -" terminals together.
3. Connect an audio generator between "input +" and ground. Set the generator frequency to 1 KHz and output level to +20 dBu.
4. Connect a balanced-input sensitive audio level meter or distortion analyser to a balanced pair of amplifier outputs.
5. With the output level meter set to the most sensitive scale, adjust the INPUT BAL potentiometer to obtain the lowest possible output level. It should be possible to obtain an output level of at least -80 dBu. (100dB below +20 dBu). Seal the pot.
6. Disconnect the test set up.

## Specifications

Input	Input Impedance	>35K ohms, balanced
	Max Input Level	+34 dBu [ +30 dBm ]
	Common Mode Rejection	>100 dB @ 60 Hz >80 dB @ 20 KHz
Output	Number of Outputs	8
	Max Output Level	+28 dBu [ +24 dBm ]
	Output Impedance	48 ohms, [ 600 ohms]
	Output Isolation	>70 dB
Performance	S/N Ratio	>100 dB (unity gain) relative to +8 dBu
	Gain Range	-18 to +34 dB ( $\pm 6$ dB fine adjustment)
	Frequency Response	+/- 0.02 dB 20 Hz to 20 KHz
	Total Harmonic Distortion	<0.002% [ 0.007% ]
	Intermodulation	<0.002% [ 0.006% ](SMPTE)
	Crosstalk between Amplifiers	>100 dB
	Power Consumption @ +8 dBu output	1.6 W [ 2.6 W ]
All tests performed at +18 dBu and cover 20 Hz to 20 KHz unless otherwise specified. All measurements made with an Audio Precision System One test set. Performance of the 600 ohm version is similar, except where indicated [ ].		

## ADA 7551

### Bill of Materials

## 7551A-001

Item	QTY	REF	Part	Description	Part Number
1.	1	R48	NVR	NO VALUE, RESISTOR 5%	—
2.	16	R1,R3,R5,R7,R15,R17,R19, R21,R35,R36,R38,R41,R51, R53,R55,R57	NVR 1%	NO VALUE, RESISTOR 1%	—
3.	1	C18	22p	CAPACITOR, CERAMIC 100V 2% 22p	201-220
4.	3	C2,C8,C16	47p	CAPACITOR, CERAMIC 100V 2% 47p	201-470
5.	1	C4	4n7	CAPACITOR, CERAMIC 4n7	203-470
6.	1	C3	10n	CAPACITOR, CERAMIC 10n	204-100
7.	4	C1,C7,C11,C17	100n	CAPACITOR, GLASS 100n	225-100
8.	2	C6,C5	100u 250-005	CAPACITOR, TANTALUM 6.3V 100u	250-005
9.	2	C14,C12	47u	CAPACITOR, TANTALUM 16V 47u	250-006
10.	4	C9,C10,C13,C15	6u8	CAPACITOR, TANTALUM 25V 6u8	250-008
11.	1	J1	311-035	CONNECTOR, 2X25P, PCB MNT, 90 DEG	311-035
12.	4	CR1,CR2,CR3,CR4	1N4148	DIODE, SIGNAL GP 1N4148	360-005
13.	1	MP1	365-003	PCB, EJECTOR	365-003
14.	1	JP2	403-004-12	HEADER, 12 PIN, 2 ROW MALE, PL.23 BL.1 LL.1	403-004-12
15.	1	JP1	403-004-16	HEADER, 16 PIN, 2 ROW MALE, PL.23 BL.1 LL.1	403-004-16
16.	1	U3	NE5532N	INT-COMPENSATED, DUAL LO-NOISE OP, AMP	504-129
17.	1	U6	SSM-2143P	-6dB DIFFERENTIAL, LINE RECEIVER	504-130
18.	4	U1,U2,U4,U5	TLE2064CN	JFET-INPUT, HIGH-OUT POWER, QUAD OP AMP	504-155
19.	5	JPPLUG1D,JPPLUG1C, JPPLUG1B,JPPLUG1A, JPPLUG2	603-005	JUMPER, 2-POSITION LOW PROFILE	603-005
20.	1	RV2	100R 1T	VARIABLE, RESISTOR, 1/4 DIA 1-TURN 100R	710-002
21.	1	RV1	10K 720-002	VARIABLE, RESISTOR, 20-TURN 10K	720-002
22.	1	PCB	7551-001-01	AUDIO, AMP	7551-001-01
23.	2	F2,F1	1R 1%	RESISTOR, 1/4W, 1% 1R	810-100
24.	1	R28	53R6 1%	RESISTOR, 1/4W, 1% 53R6	811-536
25.	1	R29	191R 1%	RESISTOR, 1/4W, 1% 191R	812-191
26.	1	R30	750R 1%	RESISTOR, 1/4W, 1% 750R	812-750
27.	1	R50	1K07 1%	RESISTOR, 1/4W, 1% 1K07	813-107
28.	1	R49	3K32 1%	RESISTOR, 1/4W, 1% 3K32	813-332
29.	16	R2,R4,R6,R8,R16,R18,R20, R22,R37,R39,R40,R42,R52, R54,R56,R58	4K75 1%	RESISTOR, 1/4W, 1% 4K75	813-475
30.	2	R14,R33	10K 1%	RESISTOR, 1/4W, 1% 10K	814-100
31.	1	R68	52K3 1%	RESISTOR, 1/4W, 1% 52K3	814-523
32.	16	R9,R10,R11,R12,R23,R24, R25,R26,R43,R44,R45,R46, R59,R60,R61,R62	24R	RESISTOR, 1/2W, 5% 24R	825-240
33.	1	R67	47R	RESISTOR, 1/2W, 5% 47R	825-470
34.	4	R13,R34,R63,R64	1K	RESISTOR, 1/2W, 5% 1K	827-100
35.	1	R31	1K5	RESISTOR, 1/2W, 5% 1K5	827-150
36.	1	R47	20K	RESISTOR, 1/2W, 5% 20K	827-200
37.	1	R27	2K7	RESISTOR, 1/2W, 5% 2K7	827-270
38.	1	R65	4K7	RESISTOR, 1/2W, 5% 4K7	827-470
39.	1	R32	12K	RESISTOR, 1/2W, 5% 12K	828-120
40.	2	R66,R69	10K 0.5%	RESISTOR, 1/4W, 0.5% 10K	840-059
41.	4	TP1,TP2,TP3,TP4	910-010	TEST POINT	910-010
42.	1	Q2	2N3904	TRANSISTOR, N-P-N	950-016
43.	1	Q1	2N3906	TRANSISTOR, P-N-P	950-018

## ADA 7551-600

## 7551A -001A (600 ohm version)

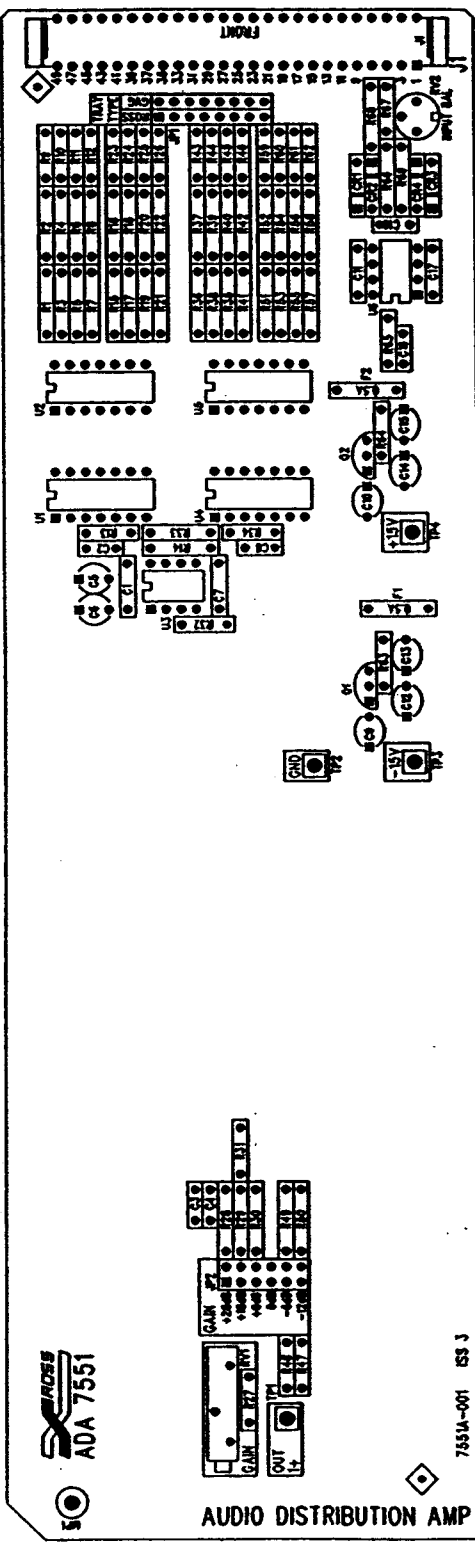
Bill of materials same as per 7551A above with the following exceptions.

Item 1:	20K 1/2 W 5%	RESISTOR, 1/2 5% 20K	827-200
Item 2:	4K75 1/4 W 1%	RESISTOR, 1/4 W 1%	813-475
Item 32:	300R	RESISTOR, 1/2 WF 5% 300R	826-300

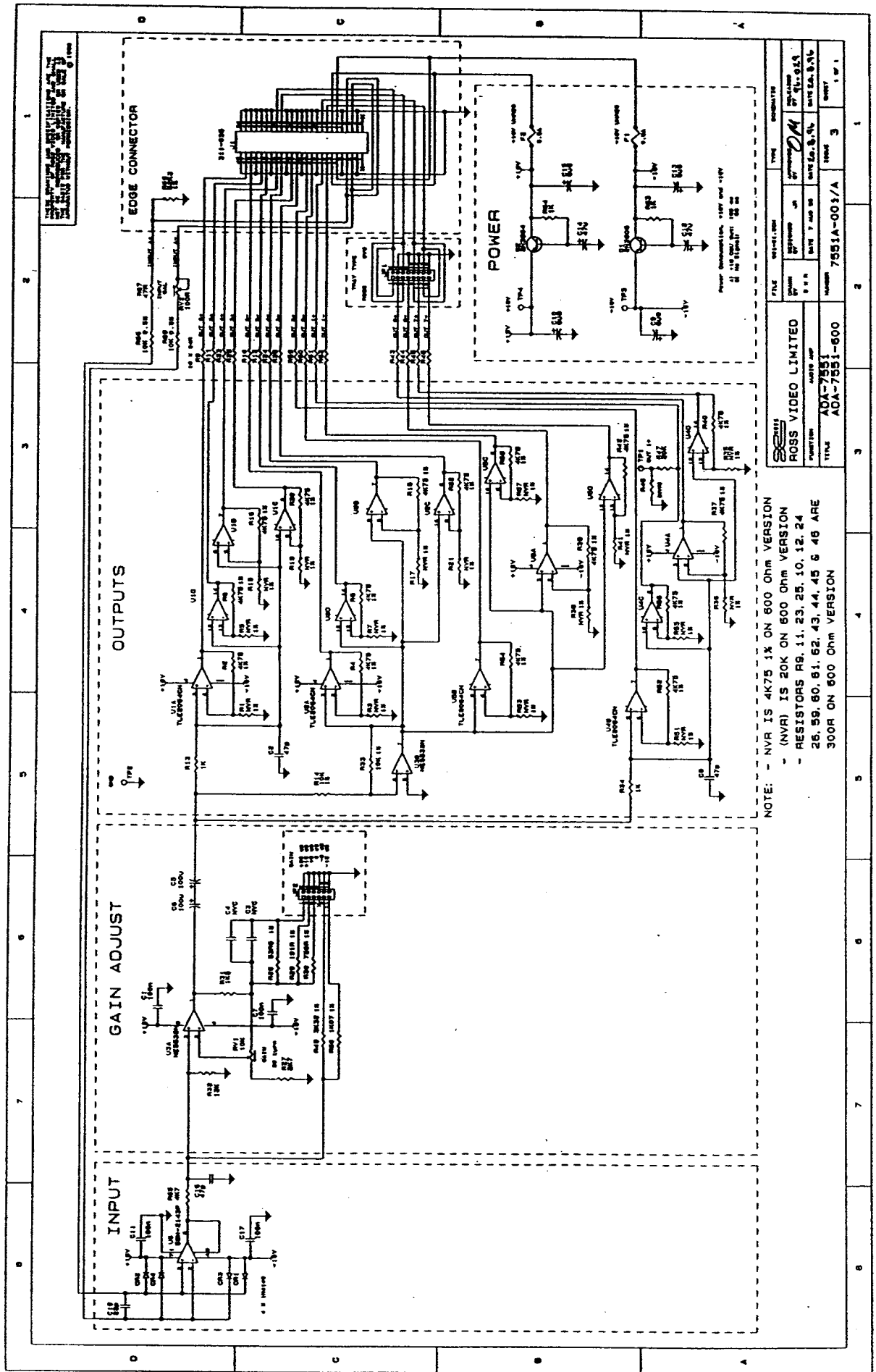
## **Notes**

[illegible][illegible]

F1 = 0.5A FUSE  
F2 = 0.5A FUSE  
J#1 = TRAY TYPE  
J#2 = GAIN -12dB TO +20dB  
RV1 = GAIN  
RV2 = INPUT BAL  
TP1 = OUT +1  
TP2 = GND  
TP3 = -15V  
TP4 = +15V

[illegible]







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