Sony Corporation

No. HDCM14-048-TN

Technical News

Issued : July 24, 2014 Subject: Software Release [SDP-PLD, V2.05]

[Applicable Model]

Model / Destination	Serial Number	Number of Unit
HDCU1700 CED		
HDCU1700 SY3		
HDCU1700 SYL		
HDCU2000 CED		
HDCU2000 E2		
HDCU2000 E3		
HDCU2000 J2		
HDCU2000 UC5		
HDCU2080 CNB		
HDCU2500 CED		
HDCU2500 SY3		
HDCU2500 SYL		

[Description]

The V2.05 of SDP-PLD is released for HDCU2000/2500/2080/1700.

* Sure to perform this upgrading if the upgrading described in HDCM14-039 is performed.

[Change Point]

<Error correction>

1. Correct the error that the image is disturbed rarely as if the H sync would be lost after the power is turned on.

Correct the CPU_IF circuit in FPGA to secure the phase margin between the control signals. The internal layout is also corrected so that the circuit for the sync signal would not malfunction.

[Preparation]

<Software Files>

File Name	Update target	Version
E-000-005-26-10	PLD (SDP-17)	V2.05

* Please contact to your local Sony's service office for obtaining the software file.

* Software tool for the PLD upgrade

DLT_v35.exeSoftware tool for PLD version updateDLT30_install.pdf DLT_v35Install Guide (Japanese)DLT30_install_E.pdf DLT_v35Install Guide (English)Refer to HDCM12-041 for obtain the files above.

<PLD upgrade tool>

- Personal computer

OS: Windows 2000/XP, with USB port

Above software tool for version update is already installed

- PLD download jig (cable) Part No. J-7120-220-A

[Installation Procedure]

Write data into PLD of SDP-17 board following the Service Manual Chapter 1-13. The PLD internal data on the SDP-17 board is written or rewritten via the CN3/AVP-15 board. Rewrite the data with the e-Production tool with switch S1-8 on the AVP-15 board turned on. After it is rewritten, return switch S1-8 on the AVP-15 board to off.

[Confirmation/Adjustment]

Check the SDP-PLD version name at the CHARACTER output or the MONITOR output.

Version in this Technical News PLD SDP-17 V2.05