


7A

**DEVICE CONTROL
UNIT PACK
DCU-8000 (MKS-8700/8701/8702)**

DCU Maintenance



**DEVICE CONTROL UNIT PACK
DCU-8000 (MKS-8700/8701/8702)**

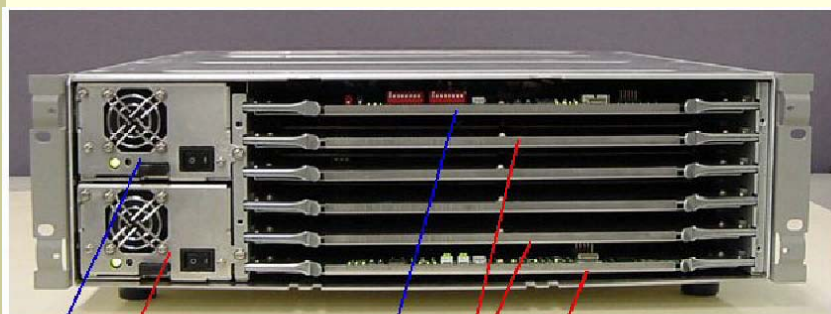
- MKS-8700 DEVICE CONTROL UNIT
 - CA-47 : SYSTEM CONTROL BOARD
 - MB-949 : MOTHER BOARD
 - CN-2193 : CONNECTOR BOARD (TALLY/GPI INPUT)
 - CN-2198 : CONNECTOR BOARD (LAN , SERIAL TALLY)
 - CN-2199 : CONNECTOR BOARD (REFERENCE VIDEO INPUT)
- MKS-8701 TALLY/GPI OUTPUT BOARD
 - RC-90 : TALLY/GPI OUTPUT BOARD
 - CN-2195 : CONNECTOR BOARD
- MKS-8702 SERIAL INTERFACE BOARD
 - IF-848 : SERIAL INTERFACE BOARD
 - CN-2194 : CONNECTOR BOARD

DCU-8000

DEVICE CONTROL UNIT PACK

DCU-8000 (MKS-8700/8701/8702)

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HK-PSU03 (optional)
Power Supply

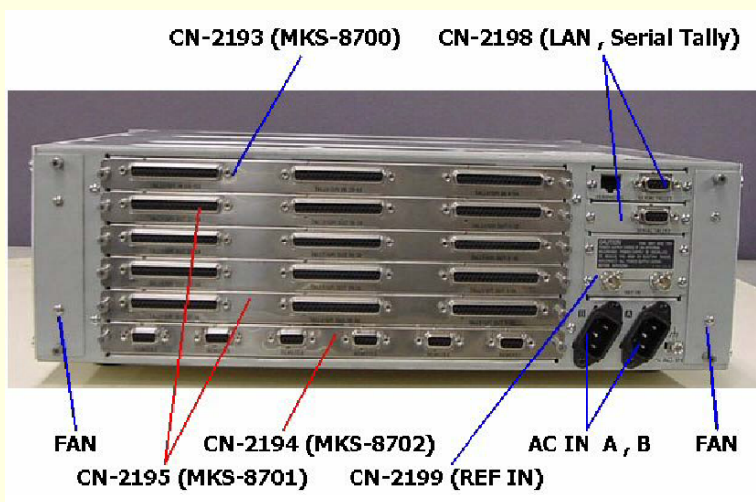
IF-848 (MKS-8702)
RC-90 (MKS-8701)
CA-47 (MKS-8700)

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DEVICE CONTROL UNIT PACK

DCU-8000 (MKS-8700/8701/8702)

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CN-2193 (MKS-8700)

CN-2198 (LAN, Serial Tally)

FAN

CN-2194 (MKS-8702)

AC IN A, B

FAN

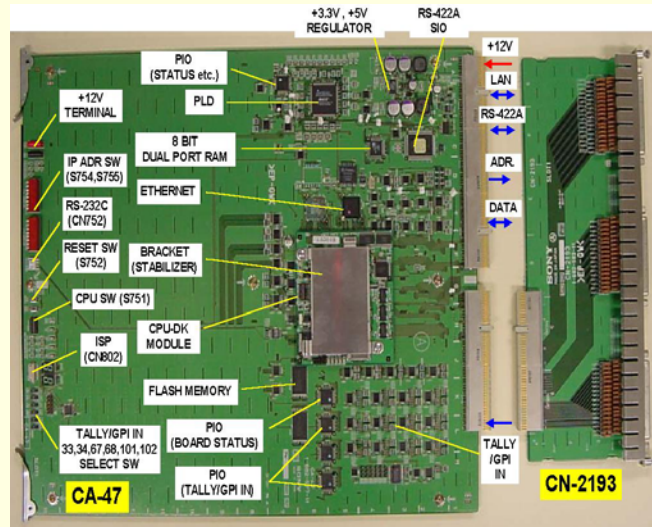
CN-2195 (MKS-8701)

CN-2199 (REF IN)

DCU-8000

SYSTEM CONTROL BOARD/ CONNECTOR BOARD - CA-47/CN-2193 (MKS-8700)

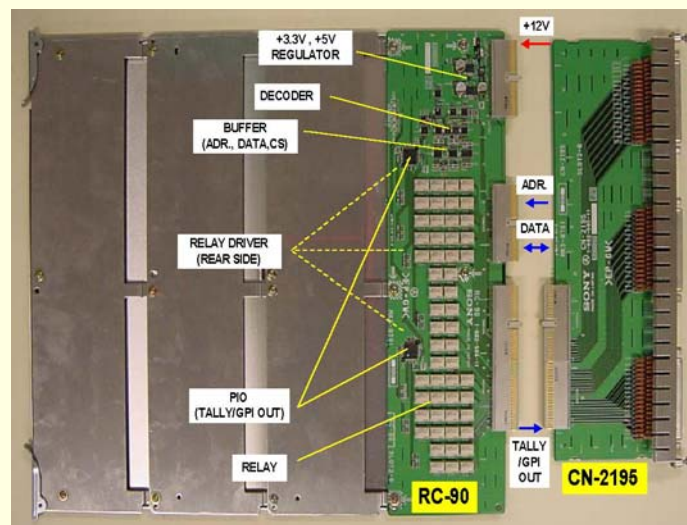
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TALLY/GPI OUTPUT BOARD/ CONNECTOR BOARD - RC-90/CN-2195 (MKS-8701)

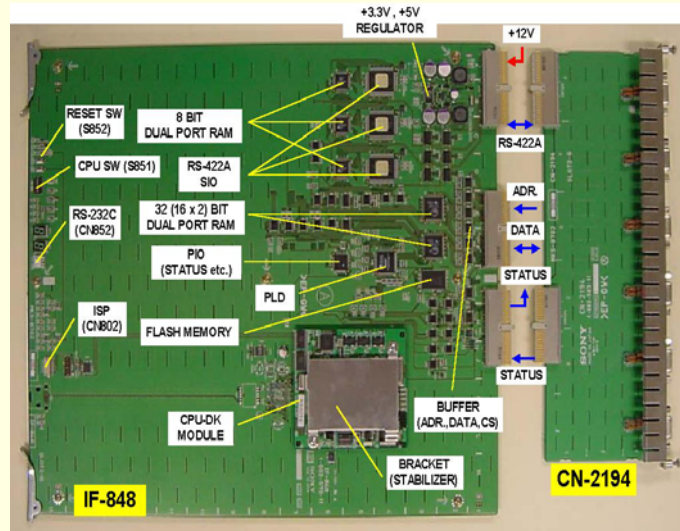
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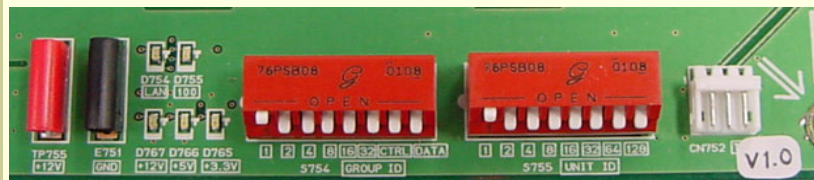
SERIAL INTERFACE BOARD/ CONNECTOR BOARD - IF-848/CN-2194 (MKS-8702)

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MKS-8700 MAINTENANCE (1)

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+12V
TERMINAL

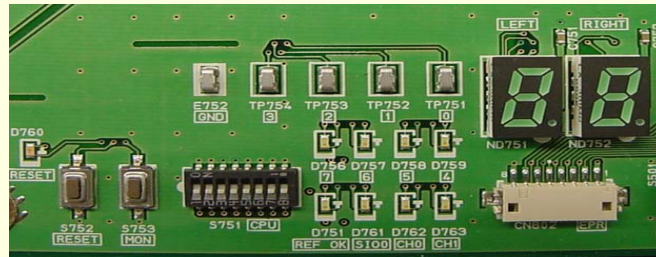
IP ADR SW (S754, 755)
S754-1 : OPEN, S754-2~8 : CLOSE
S755-1 : OPEN, S755-2~8 : CLOSE

RS-232C (CN752)
PC
(SERIAL)

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MKS-8700 MAINTENANCE (2)

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RESET SW
(S752)

CPU SW (S751)
S751-1~8 : OFF

ISP (CN802)

PLD
Programmer
(for ALTERA)

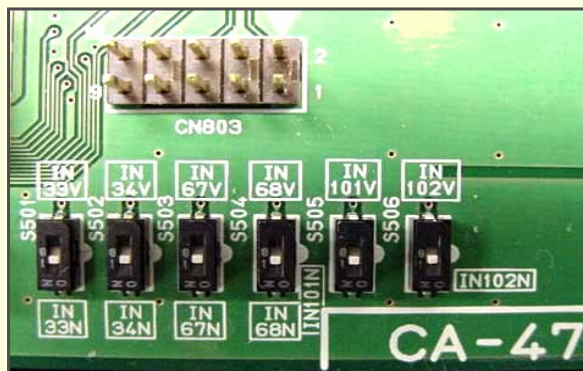
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MKS-8700 MAINTENANCE (3)

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TALLY/GPI IN SW Setting (S501~506)

- ON (IN xxN) : Normal (TTL) Mode
 - OFF (IN xxV) : +12V Mode
- Factory setting S501~506 : ON (IN xxN)



TALLY/GPI IN SW (S501~506)

TALLY/GPI IN
33,34,67,68,101,102

OFF (IN xxV)
+12V Mode



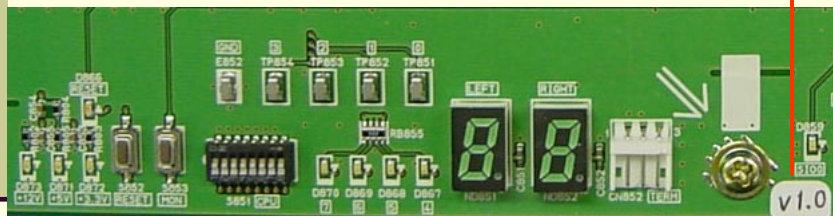
ON (IN xxN)
Normal (TTL) Mode

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MKS-8702 MAINTENANCE (1)

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Hardware
Version Label



RESET SW (S852) CPU SW (S851)
S851-1~8 : OFF

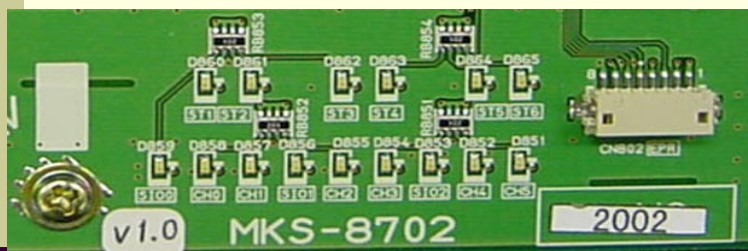
RS-232C
(CN752)

PC
(SERIAL)

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MKS-8702 MAINTENANCE (2)

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Hardware
Version Label

ISP (CN802)

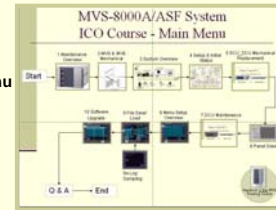
PLD
Programmer
(for ALTERA)

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Questions?

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