# SONY

XVS-8000-C
MULTI FORMAT SWITCHER
XVS-8000

OUTPUT PROCESSOR BOARD **XKS-8160** 

MIX EFFECT BOARD XKS-8210

FRAME MEMORY BOARD **XKS-8440** 

FORMAT CONVERTER BOARD **XKS-8460** 

HD DME BOARD XKS-8470

SDI INPUT CONNECTOR BOARD XKS-S8110

SDI OUTPUT CONNECTOR BOARD XKS-S8165

IP INPUT CONNECTOR BOARD **XKS-T8110** 

IP OUTPUT CONNECTOR BOARD XKS-T8165

INSTALLATION MANUAL 1st Edition



## △警告

このマニュアルは、サービス専用です。

お客様が、このマニュアルに記載された設置や保守、点検、修理などを行うと感電や火災、 人身事故につながることがあります。

危険をさけるため、サービストレーニングを受けた技術者のみご使用ください。

## **⚠ WARNING**

This manual is intended for qualified service personnel only.

To reduce the risk of electric shock, fire or injury, do not perform any servicing other than that contained in the operating instructions unless you are qualified to do so. Refer all servicing to qualified service personnel.

## **⚠ WARNUNG**

Die Anleitung ist nur für qualifiziertes Fachpersonal bestimmt.

Alle Wartungsarbeiten dürfen nur von qualifiziertem Fachpersonal ausgeführt werden. Um die Gefahr eines elektrischen Schlages, Feuergefahr und Verletzungen zu vermeiden, sind bei Wartungsarbeiten strikt die Angaben in der Anleitung zu befolgen. Andere als die angegeben Wartungsarbeiten dürfen nur von Personen ausgeführt werden, die eine spezielle Befähigung dazu besitzen.

## **AVERTISSEMENT**

Ce manual est destiné uniquement aux personnes compétentes en charge de l'entretien. Afin de réduire les risques de décharge électrique, d'incendie ou de blessure n'effectuer que les réparations indiquées dans le mode d'emploi à moins d'être qualifié pour en effectuer d'autres. Pour toute réparation faire appel à une personne compétente uniquement.

XVS-8000 (SY)	Serial No. 10001 and Higher
XVS-8000 (CN)	Serial No. 50001 and Higher
XKS-8160 (SY)	Serial No. 10001 and Higher
XKS-8210 (SY)	Serial No. 10001 and Higher
XKS-8440 (SY)	Serial No. 10001 and Higher
XKS-8460 (SY)	Serial No. 10001 and Higher
XKS-8470 (SY)	Serial No. 10001 and Higher
XKS-S8110 (SY)	Serial No. 10001 and Higher
XKS-S8165 (SY)	Serial No. 10001 and Higher
XKS-T8110 (SY)	Serial No. 10001 and Higher
XKS-T8165 (SY)	Serial No. 10001 and Higher

## For kundene i Norge

Dette utstyret kan kobles til et IT-strømfordelingssystem.

- ・安全のために、周辺機器を接続する際は、過大電圧を 持つ可能性があるコネクターを以下のポートに接続し ないでください。
- : MVS LAN コネクター
- : UTL LAN コネクター

上記のポートについては本書の指示に従ってください。

- For safety, do not connect the connector for peripheral device wiring that might have excessive voltage to the following ports.
- : MVS LAN connector
- : UTL LAN connector

Follow the instructions for the above ports.

#### 警告

万一,異常が起きた際に、お客様が電源を切ることができるように、設置の際には、機器近くの固定配線内に専用遮断装置を設けるか、機器使用中に、容易に抜き差しできるコンセントに電源プラグを接続してください。

#### **WARNING**

When installing the unit, incorporate a readily accessible disconnect device in the fixed wiring, or connect the power cord to a socket-outlet which must be provided near the unit and easily accessible, so that the user can turn off the power in case a fault should occur.

#### **WARNUNG**

Beim Einbau des Geräts ist daher im Festkabel ein leicht zugänglicher Unterbrecher einzufügen, oder das Netzkabel muß mit einer in der Nähe des Geräts befindlichen, leicht zugänglichen Wandsteckdose verbunden werden, damit sich bei einer Funktionsstörung die Stromversorgung zum Gerät jederzeit unterbrechen läßt.

#### Attention-when the product is installed in Rack:

Prevention against overloading of branch circuit
 When this product is installed in a rack and is
 supplied power from an outlet on the rack, please
 make sure that the rack does not overload the supply
 circuit.

#### 2. Providing protective earth

When this product is installed in a rack and is supplied power from an outlet on the rack, please confirm that the outlet is provided with a suitable protective earth connection.

- 3. Internal air ambient temperature of the rack When this product is installed in a rack, please make sure that the internal air ambient temperature of the rack is within the specified limit of this product.
- 4. Prevention against achieving hazardous condition due to uneven mechanical loading When this product is installed in a rack, please make sure that the rack does not achieve hazardous condition due to uneven mechanical loading.
- 5. Install the equipment while taking the operating temperature of the equipment into consideration For the operating temperature of the equipment, refer to the specifications of the Installation Manual.
- 6. When performing the installation, keep the following space away from walls in order to obtain proper exhaust and radiation of heat.

xvs-8000 1 (P)

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xvs-8000 1 (E)

## **Manual Structure**

## Purpose of this manual

This manual is the installation manual of Switcher Processor Pack XVS-8000-C and the optional boards.

This manual is intended for use by trained system and service engineers, and describes the information on installing the XVS-8000-C system.

## Related manuals

The following manuals are prepared for XVS-8000-C and the optional boards.

## Operation Manual (Supplied with XVS-8000-C)

This manual describes the overview, system connection example and specifications of options of XVS-8000-C.

## User's Guide (Supplied with XVS-8000-C)

This manual describes the application and operation of XVS-8000-C.

## · Service Manual (Available on request)

This manual describes service overview, error messages, periodic maintenance and inspection, replacement of main parts, and etc. of the unit to provide information required for block-level service.

If this manual is required, please contact your local Sony Sales Office/Service Center.

## · Factory Service Manual (Available on request)

Parts list, circuit diagram, and board layouts of the unit are included to provide information required for part-level service.

If this manual is required, please contact your local Sony Sales Office/Service Center.

## **Trademarks**

System names and product names written in this manual are usually registered trademarks or trademarks of respective development manufacturers.

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# Section 1 Installation

## 1-1. Operating Environment

Operating guaranteed temperature: +5 °C to +40 °C Performance guaranteed temperature: +10 °C to +35 °C Operating humidity: 10 % to 90 %

(relative humidity)

Storage temperature:  $-20 \,^{\circ}\text{C}$  to  $+60 \,^{\circ}\text{C}$ 

Mass (when all options are installed):

XVS-8000: Approx. 63 kg

#### Prohibited locations for installation

- Areas where the unit will be exposed do direct sunlight or any other strong lights.
- · Dusty areas
- Areas subject to vibration.
- Areas with strong electric or magnetic fields.
- · Areas near heat sources.
- Areas subject to electrical noise.
- Areas subject where is subjected to static electricity.

#### Ventilation

The inside of the XVS-8000 is cooled by a fan (right side). The power supply can be damaged if the exhaust vent (right side) and air intake (front panel) are blocked or the fan is stopped.

Therefore, leave a blank space of more than 10 cm in the front and both sides of the XVS-8000.

## 1-2. Power Supply

## 1-2-1. Power Specifications

A switching regulator is used for the power supply of this unit. The voltage within the range of 100 V to 240 V can be used without changing the supply voltage.

Power requirements: 100 to 240 V AC  $\pm 10 \%$ 

Power frequency: 50/60 Hz

Current consumption (when all options are installed):

XVS-8000: 22 to 9.2 A

## Note

As the inrush current at turn-on, the capacity of the AC power source must be commensurate with this load.
 If the capacity of the AC power is not adequately large, the AC power source breaker will operate or the unit will abnormally operate.

Inrush current: 173 A (at peak), 47 Ar.m.s (at AC 240V)

 The XVS-8000 contains the four power supply units as the standard configuration. When starting up the XVS-8000, be sure to turn on the power of four power supply units.

#### 1-2-2. Power Cord

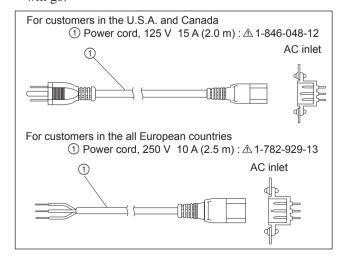
## WARNING

- Use the approved Power Cord (3-core mains lead)/Appliance Connector/Plug with earthing-contacts that conforms to the safety regulations of each country if applicable.
- Use the Power Cord (3-core mains lead)/Appliance Connector/Plug conforming to the proper ratings (Voltage, Ampere).

If you have questions on the use of the above Power Cord/ Appliance Connector/Plug, please contact your local Sony Sales Office/Service Center.

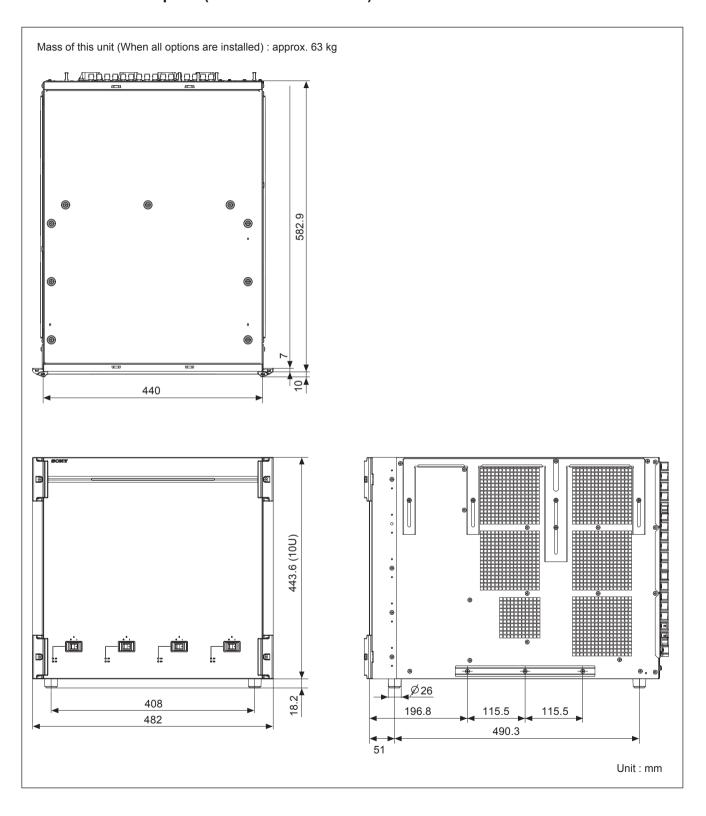
## WARNING

- Never use an injured power cord.
- Plugging the power cord in the AC inlet, push as far as it will go.



xvs-8000 1-1 (E)

# 1-3. Installation Space (External Dimensions)



1-2 (E) xvs-8000

## 1-4. Installing the Optional Board

The XVS-8000-C is shipped from the factory with the necessary option boards (refer to the following table) already installed in accordance with the specified system configuration.

The following options are available for the XVS-8000C.

## **Option board list**

Model name Board configuration		ation
	Plug-in board (Front)	Connector board (Rear)
XKS-8160 Output Processor Board	OUT-38A board	_
XKS-8210 Mix Effect Board	MIX-57 board	-
XKS-8440 Frame Memory Board	VPR-129 board	-
XKS-8460 Format Converter Board	VPR-129A board	_
XKS-8470 HD DME Board	DVP-53A board	_
XKS-S8110 SDI Input Connector Board	-	CNI-43 board
XKS-S8165 SDI Output Connector Board	-	CNO-43 board
XKS-T8110 IP Input Connector Board	-	NET-28 board
XKS-T8165 IP Output Connector Board	_	NET-28A board

## 1-4-1. Installing the Plug-in Boards

## CAUTION

Be sure to turn off the POWER switch before starting installation work.

If installation work is started with the POWER switch left on, it may cause electrical shock or damage to printed circuit boards.

In XVS-8000, the slot for installing each plug-in board is specified. Install each board in the specified slot correctly according to the indications as described in ① and ② below.

- 1 The board name is indicated close to the left-of-center of each plug-in board.
- ② The board name is indicated on the left surface of the inside of front panel of XVS-8000.

## Note

 Check to see that connectors of the plug-in boards are securely inserted into the mother board (MB-1223 board) without loose contact.

If any plug-in board is inserted into the incorrect slot, it causes a system error and the system will not work correctly.

 After installing the plug-in board, the software must be installed. Install the software same version as the XVS-8000.

For installing the software, refer to the user's guide of the XVS-8000 system.

#### Installation Procedure

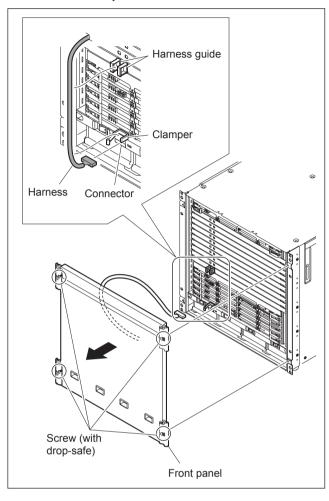
- Turn off the main power of this unit and disconnect the AC power cord from the wall outlet.
- 2. Loosen the four screws (with drop-safe) and remove the front panel to the arrow.

## Note

The front panel is connected to the unit by the harness. When removing the front panel, be careful not too pull the harness.

xvs-8000 1-3 (E)

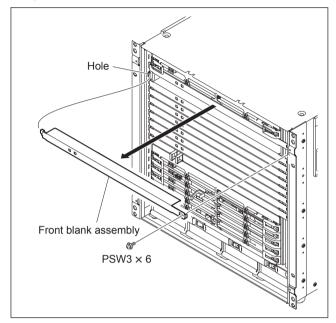
- 3. Open the clamper.
- 4. Remove the harness from the clamper, and then remove the front panel.



5. Loosen the screw and remove the front blank assembly to the arrow.

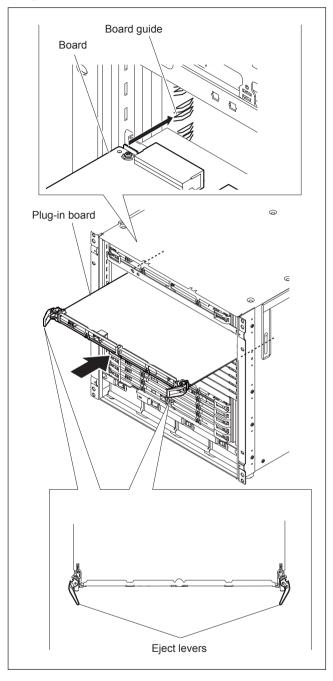
## Note

Store the removed blank panel and screws in a safe place.



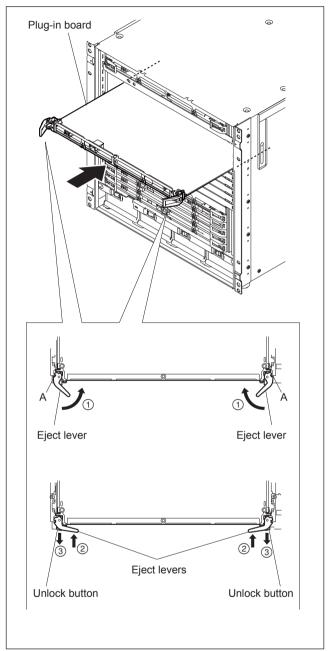
1-4 (E) xvs-8000

6. While the eject levers are opened as shown in the illustration, insert the plug-in board into the board guide.



7. Close the both eject levers at a time, when the eject lever claws reach the A position ①.

And push the eject levers ② until the unlock buttons pop out ③.

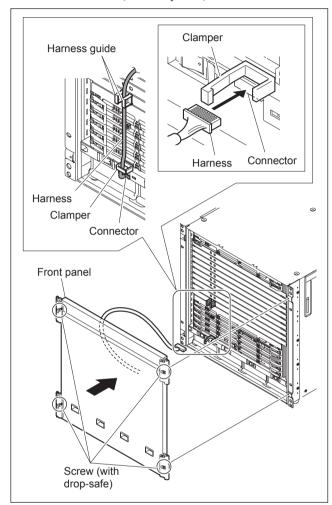


# XVS-8000 option

Name of option	Name of board	Slot on the front side
XKS-8160	OUT-38A board	17
XKS-8210	MIX-57 board	6, 7, 8, 9, 10
XKS-8440	VPR-129 board	11
XKS-8460	VPR-129A board	15
XKS-8470	DVP-53A board	4, 5

xvs-8000 1-5 (E)

- 8. Attach the front panel by the following procedure.
  - (1) Open the clamper, and then connect the harness of the front panel to the connector of the unit.
  - (2) Close the clamper.
  - (3) Pass through the harness to harness guide.
  - (4) Attach the front panel to the unit and tighten the four screws (with drop-safe).

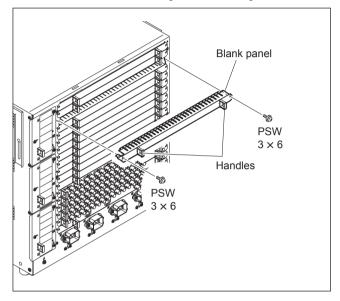


## 1-4-2. Installing the Connector Board

- 1. Remove two screws.
- 2. <u>Pull out the blank panel by pulling the handles.</u>

Note

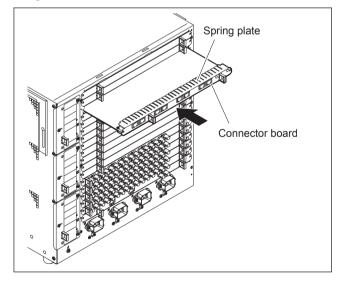
Store the removed blank panel in a safe place.



3. Insert the connector board as shown in the illustration.

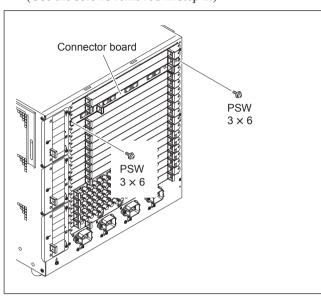
Note

Insert making sure the board's upper and lower spring plates do not catch on the board.



1-6 (E) xvs-8000

4. Tighten the two screws. (Use the screws removed in step 2.)



## XVS-8000 option

Name of option	Name of board	Slot on the front side
XKS-S8110	CNI-43 board	3, 4, 5, 6, 7, 8, 9, 10, 11, 12
XKS-S8165	CNO-43 board	13, 14, 15, 16, 17
XKS-T8110	NET-28 board	3, 4, 5, 6, 7, 8, 9, 10, 11, 12
XKS-T8165	NET-28A board	13, 14, 15, 16, 17

## 1-5. Rack Mounting

The XVS-8000 is mounted in the 19-inch standard rack. To mount the XVS-8000 in the rack, use the supplied rack mount kit and follow the procedure described below.

## Note

If other than the supplied rack mount kit is used, the unit may not be mounted in the 19-inch standard rack.

## Using parts list (Accessory of the XVS-8000)

•	Rack bracket	2 pcs
•	Support angle	2 pcs
•	Bracket	4 pcs
•	Support angle attaching screw	
	$(+PSW4 \times 10: 7-682-962-01)$	8 pcs
•	Bracket attaching screw	
	$(+B4 \times 10: 7-682-562-04)$	8 pcs

## Other required parts

• Screw for rack mounting (+B5 × 12: 7-682-576-09) 4 pcs

## 1. Precautions for rack mounting

#### WARNING

 To prevent the rack from falling or moving, fix the rack on a flat and steady floor and the like using bolts or others.

If the rack falls due to the weight of the equipment, it may cause death or serious injury.

- Be sure to use the specified rack mount kit.
   If not, injury may result and the equipment may fall due to insufficient strength.
- After rack mounting, be sure to tighten the screws on the rack angle and fix the unit in the rack.

If the screws on the rack angle are not tightened, the unit may slip from the rack and fall, causing injury.

## CAUTION

When mounting the unit in the rack, note the following:

- Be sure to mount in the rack with two persons or more.
- Be careful not to catch your fingers or hands in the rack mount rail or others.
- Mount in the rack in a stable position.

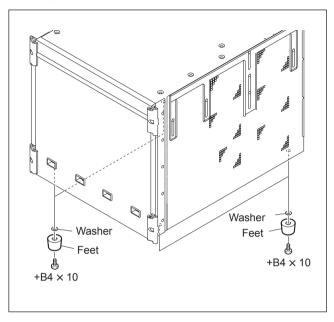
## Note

If several units are mounted in a rack, it is recommended to install a ventilation fan to prevent temperature rise inside the rack.

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## 2. Rack Mounting Procedure

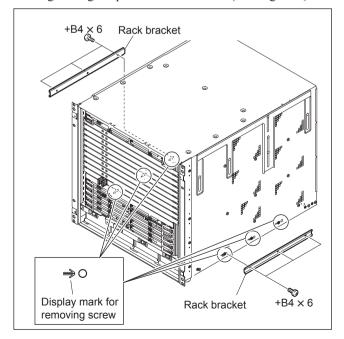
1. Loosen the four screws (+B4 × 8) and remove the four feet.



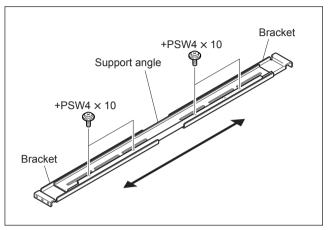
- 2. Remove the front panel of the unit. (Refer to Section 1-4-1.)
- 3. Remove the six screws (with display mark for removing screw) from the left and right side panels.
- 4. Attach the rack bracket to the side of the unit using the removed six screws in step 3.

## Note

Tighten the screws to the following torque. Tightening torque:  $120 \times 10^{-2} \text{ N} \cdot \text{m} \{12.2 \text{ kgf} \cdot \text{cm}\}\$ 



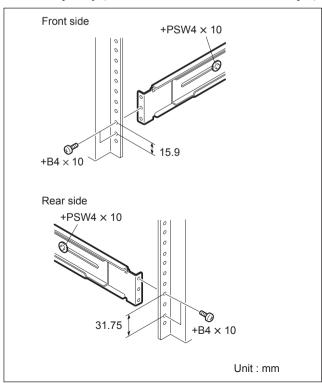
- 5. Attach the bracket to the support angle by inserting the specified four screws and loosely tightening.
- 6. Loosen the screws on the rear of the right and left brackets and adjust the length of the bracket according to the depth of the rack.



Tip

Maximum depth of bracket: 750 mm Minimum depth of bracket: 545 mm

- 7. Attach the right and left brackets to the rack completely using the specified eight screws. (The illustration below shows the left bracket.)
- 8. Tighten the screws (+PSW4 × 10: four screws each on the right and left) for adjusting the length of the bracket completely (the screws that were loosened in step 6).

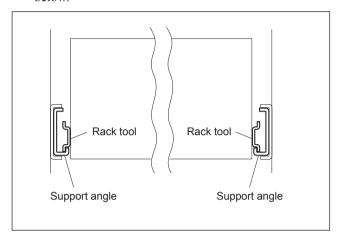


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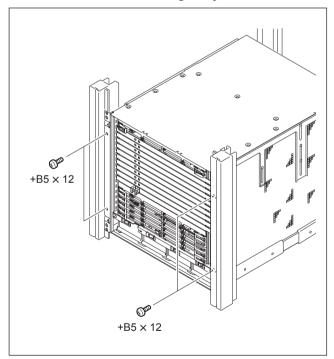
9. Align the groove of the rack bracket at the side of the unit with the rail, and slide the unit to the rear.

Tip

The rack brackets are hooked on the rails as shown below.



10. Fix the unit in the rack using the specified screws.



11. Attach the front panel to this unit. (Refer to Section 1-4-1.)

# 1-6. Matching Connectors and Cables

Use the following connectors, cables or equivalents when connecting cables to the unit.

Model name	Panel indication	Connector name	Matching connector and cable
XVS-8000	REF IN	BNC, 75 Ω	Belden 1694 coaxial cable
	MVS LAN UTL LAN	RJ-45 modular jack*1	
XKS-S8110	IN 1-16, 17-32, 33-48, 49-64, 65-80, 81-96, 97-112, 113-128, 129-144, 145-160	BNC, 75 Ω	Belden 1694 coaxial cable
XKS-T8110	IN 1-16, 17-32, 33-48, 49-64, 65-80, 81-96, 97-112, 113-128, 129-144, 145-160	RJ-45 modular jack*2	
XKS-S8165	OUT FC1-4, 1-12 FC5-8, 13-24 FC9-12, 25-36 FC13-18, 37-48 SPARE1-4, MV1-8, 49-52	BNC, 75 Ω	Belden 1694 coaxial cable
XKS-T8165	OUT FC1-4, 1-12 FC5-8, 13-24 FC9-12, 25-36 FC13-18, 37-48 SPARE1-4, MV1-8, 49-52	RJ-45 modular jack* <sup>2</sup>	

<sup>\*1:</sup> Conforms to the IEEE 802.3 Ethernet 1000BASE-T standards.

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<sup>\*2:</sup> Use the IEEE802.3 Ethernet 10GBASE-T cable (CAT7).

## 1-7. Input/Output Signals of Connectors

Input and output signals of the connectors on the rear panel are as follows.

## 1-7-1. XVS-8000

#### **REF IN:**

BNC  $\times$  2, loop through, 75  $\Omega$ NTSC, PAL black burst and sync signal or HD tri-level sync signal

## **MVS LAN/UTL LAN:**

1000BASE-T, RJ-45 (8-pin)



Pin No.	Signal Name	Function
1	TRX1+	Transmitted/Received data (+)
2	TRX1-	Transmitted/Received data (-)
3	TRX2+	Transmitted/Received data (+)
4	TRX3+	Transmitted/Received data (+)
5	TRX3-	Transmitted/Received data (-)
6	TRX2-	Transmitted/Received data (-)
7	TRX4+	Transmitted/Received data (+)
8	TRX4-	Transmitted/Received data (-)

#### 1-7-2. XKS-S8110

IN 1-16, 17-32, 33-48, 49-64, 65-80, 81-96, 97-112, 113-128, 129-144, 145-160:

BMC × 16, 75  $\Omega$ 

SDI input: SMPTE259M-C/292M, SMPTE424M/425-1/5

## 1-7-3. XKS-S8165

OUT FC1-4, 1-12/ FC5-8, 13-24/FC9-12, 25-36/ FC13-18, 37-48/SPARE1-4, MV1-8, 49-52:

BNC × 16, 75  $\Omega$ 

SDI output: SMPTE259M-C/292M, SMPTE424M/425-1/5

#### 1-7-4. XKS-T8110

IN 1-16, 17-32, 33-48, 49-64, 65-80, 81-96, 97-112, 113-128, 129-144, 145-160:

 $RJ-45 (8-pin) \times 8$ 

Signal name: 10GBASE-T Function: IP Video input



(External View)

Pin No.	Signal Name	Function
1	TRX1+	Transmitted/Received data (+)
2	TRX1-	Transmitted/Received data (-)
3	TRX2+	Transmitted/Received data (+)
4	TRX3+	Transmitted/Received data (+)
5	TRX3-	Transmitted/Received data (-)
6	TRX2-	Transmitted/Received data (-)
7	TRX4+	Transmitted/Received data (+)
8	TRX4-	Transmitted/Received data (-)

## 1-7-5. XKS-T8165

OUT FC1-4, 1-12/ FC5-8, 13-24/FC9-12, 25-36/ FC13-18, 37-48/SPARE1-4, MV1-8, 49-52:

 $RJ-45 (8-pin) \times 8$ 

Signal name: 10GBASE-T Function: IP Video output



(External View)

Signal Name	Function
TRX1+	Transmitted/Received data (+)
TRX1-	Transmitted/Received data (-)
TRX2+	Transmitted/Received data (+)
TRX3+	Transmitted/Received data (+)
TRX3-	Transmitted/Received data (-)
TRX2-	Transmitted/Received data (-)
TRX4+	Transmitted/Received data (+)
TRX4-	Transmitted/Received data (-)
	TRX1+ TRX1- TRX2+ TRX3+ TRX3- TRX2- TRX4+

xvs-8000 1-11 (E)

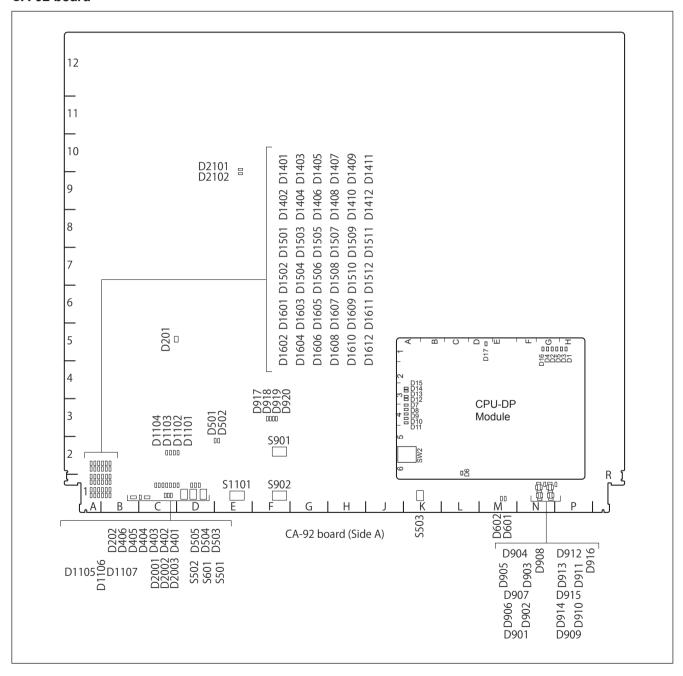
# 1-8. Description of On-board Switches and LEDs

## 1-8-1. XVS-8000

## Note

The number shown in the parentheses ( ) indicated the address on the circuit board.

## CA-92 board



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D1101-4 (C-2, D-2): Debug LED0-4

Used only for design.

D1105 (B-1): PBS Power

For future function expansion.

D1106 (C-1): PBS Beacon

For future function expansion.

**D1107 (C-1): PBS Status** 

For future function expansion.

D1401 (A-2): SW0 Port0 LED1 (SLOT-14: Not used)

10/100 Link/Act status indication.

Goes off: no link, Lights: 10/100 Link/Act, Blinks: activity

D1402 (A-2): SW0 Port0 LED0 (SLOT-14: Not used)

1000 Link/Act status indication.

Goes off: no link, Lights: 1000 Link/Act, Blinks: activity

D1403 (A-2): SW0 Port1 LED1 (SLOT-05: DVP-53 board)

10/100 Link/Act status indication.

Goes off: no link, Lights: 10/100 Link/Act, Blinks: activity

D1404 (A-2): SW0 Port1 LED0 (SLOT-05: DVP-53 board)

1000 Link/Act status indication.

Goes off: no link, Lights: 1000 Link/Act, Blinks: activity

D1405 (A-2): SW0 Port2 LED1 (SLOT-06: MIX-57)

10/100 Link/Act status indication.

Goes off: no link, Lights: 10/100 Link/Act, Blinks: activity

D1406 (A-2): SW0 Port2 LED0 (SLOT-06: MIX-57)

1000 Link/Act status indication.

Goes off: no link, Lights: 1000 Link/Act, Blinks: activity

D1407 (B-2): SW0 Port3 LED1 (SLOT-07: MIX-57)

10/100 Link/Act status indication.

Goes off: no link, Lights: 10/100 Link/Act, Blinks: activity

D1408 (B-2): SW0 Port3 LED0 (SLOT-07: MIX-57)

1000 Link/Act status indication.

Goes off: no link, Lights: 1000 Link/Act, Blinks: activity

D1409 (B-2): SW0 Port4 LED1 (No connection)

This LED does not light.

D1410 (B-2): SW0 Port4 LED0 (No connection)

This LED does not light.

D1411 (B-2): SW0 Port5 LED1 (CPU-DP board)

Port5 Link/Act status indicator.

Goes off: no link, Lights: link, Blinks: activity

D1412 (B-2): SW0 Port5 LED0 (SW1 PORT5)

Port6 Link/Act status indicator.

Goes off: no link, Lights: link, Blinks: activity

D1501 (A-1): SW1 Port0 LED1 (SLOT-08: MIX-57)

10/100 Link/Act status indication.

Goes off: no link, Lights: 10/100 Link/Act, Blinks: activity

D1502 (A-1): SW1 Port0 LED0 (SLOT-08: MIX-57)

1000 Link/Act status indication.

Goes off: no link, Lights: 1000 Link/Act, Blinks: activity

D1503 (A-1): SW1 Port1 LED1 (SLOT-10: MIX-57)

10/100 Link/Act status indication.

Goes off: no link, Lights: 10/100 Link/Act, Blinks: activity

D1504 (A-1): SW1 Port1 LED0 (SLOT-10: MIX-57)

1000 Link/Act status indication.

Goes off: no link, Lights: 1000 Link/Act, Blinks: activity

D1505 (A-1): SW1 Port2 LED1 (SLOT-09: MIX-57)

10/100 Link/Act status indication.

Goes off: no link, Lights: 10/100 Link/Act, Blinks: activity

D1506 (A-1): SW1 Port2 LED0 (SLOT-09: MIX-57)

1000 Link/Act status indication.

Goes off: no link, Lights: 1000 Link/Act, Blinks: activity

D1507 (B-1): SW1 Port3 LED1 (SLOT-04: DVP-53)

10/100 Link/Act status indication.

Goes off: no link, Lights: 10/100 Link/Act, Blinks: activity

D1508 (B-1): SW1 Port3 LED0 (SLOT-04: DVP-53)

1000 Link/Act status indication.

Goes off: no link, Lights: 1000 Link/Act, Blinks: activity

D1509 (B-1): SW1 Port4 LED1 (No connection)

This LED does not light.

D1510 (B-1): SW1 Port4 LED0 (No connection)

This LED does not light.

xvs-8000 1-13 (E)

## D1511 (B-1): SW1 Port5 LED1 (SW0 Port6)

Port5 Link/Act status indicator.

Goes off: no link, Lights: link, Blinks: activity

## D1512 (B-1): SW1 Port5 LED0 (SW2 Port5)

Port6 Link/Act status indicator.

Goes off: no link, Lights: link, Blinks: activity

## D1601 (A-1): SW2 Port0 LED1 (SLOT-11: VPR-129)

10/100 Link/Act status indication.

Goes off: no link, Lights: 10/100 Link/Act, Blinks: activity

#### D1602 (A-1): SW2 Port0 LED0 (SLOT-11: VPR-129)

1000 Link/Act status indication.

Goes off: no link, Lights: 1000 Link/Act, Blinks: activity

# D1603 (A-1): SW2 Port1 LED1 (Rear panel NET-WORK-A)

10/100 Link/Act status indication.

Goes off: no link, Lights: 10/100 Link/Act, Blinks: activity

# D1604 (A-1): SW2 Port1 LED0 (Rear panel NET-WORK-A)

1000 Link/Act status indication.

Goes off: no link, Lights: 1000 Link/Act, Blinks: activity

## D1605: SW2 Port2 LED1 (Rear panel NETWORK-B)

10/100 Link/Act status indication.

Goes off: no link, Lights: 10/100 Link/Act, Blinks: activity

# D1606 (A-1): SW2 Port2 LED0 (Rear panel NET-WORK-B)

1000 Link/Act status indication.

Goes off: no link, Lights: 1000 Link/Act, Blinks: activity

## D1607 (B-1): SW2 Port3 LED1 (CPU-DP board)

10/100 Link/Act status indication.

Goes off: no link, Lights: 10/100 Link/Act, Blinks: activity

#### D1608 (B-1): SW2 Port3 LED0 (CPU-DP board)

1000 Link/Act status indication.

Goes off: no link, Lights: 1000 Link/Act, Blinks: activity

## D1609 (B-1): SW2 Port4 LED1 (No connection)

This LED does not light.

## D1610 (B-1): SW2 Port4 LED0 (No connection)

This LED does not light.

#### D1611 (B-1): SW2 Port5 LED1 (SW1 Port6)

Port5 Link/Act status indication.

Goes off: no link, Lights: link, Blinks: activity

## D1612 (B-1): SW2 Port5 LED0 (No connection)

This LED does not light.

## D2001 (C-1): NO LOCK status LED

REF IN status indication.

This LED lights when the setting for the format of signals that are input to the REF IN connector differs from the switcher format setting.

#### D2002 (C-1): NO ALGIN status LED

This LED indicates the alignment status of the internal REF signal generated from REF IN.

This LED is lit when the alignment is deviated.

#### D2003 (C-1): NO REF status LED

REF IN status indication.

This LED is unlit while the REF signal is input to the REF IN connector, and is lit while the REF signal is not recognized.

## D201 (C-5): eFUSE status ED

Lights in red: eFUSE has been turned OFF by the overload. Lights in green: eFUSE has turned ON in the normal state.

#### D202 (C-1): +12 V-1 power supply status

Lights when the +12 V -1 power is supplied.

## D401 (C-1): 3.3 V-1 power supply status

Lights when the +3.3 V - 1 power is supplied.

## D402 (C-1): 1.2 V-1 power supply status

Lights when the +1.2V -1 power is supplied.

## D403 (C-1): 3.3 V-2 power supply status

Lights when the +3.3 V-2 power is supplied.

## D404 (C-1): 1.8 V-1 power supply status

Lights when the +1.8 V-1 power is supplied.

## D405 (C-1): 5 V-1 power supply status

Lights when the +5 V-1 power is supplied.

## D406 (C-1): 2.5 V-1 power supply status

Lights when the +2.5 V-1 power is supplied.

1-14 (E) xvs-8000

#### D2101 (E-10): REF EXT

Not used: This LED does not light.

## D2102 (E-9): PLL\_LOCK

Not used: This LED does not light.

## D501 (E-2): CAD1

This LED goes out when configuration of the IC1 is completed correctly.

#### D502 (E-2): CAD2

This LED goes out when configuration of the IC2 is completed correctly.

## D503 (D-1): CA\_RESET\_B

CA-92 board reset status indication.

This LED lights in the following when:

- Switch S501 (CA RESET) is pressed
- Switch S502 (RESET) is pressed
- · A software reset request is received
- IC1 and IC2 are not working correctly

#### D504 (D-1): CA RESET A

CA-92 board reset status indication.

This LED lights in the following when:

- Switch S501 (CA RESET) is pressed
- Switch S502 (RESET) is pressed
- · A software reset request is received
- IC1 and IC2 are not working correctly

#### D505 (D-1): SWER RESET

System reset status indication.

This LED lights in the following when:

- Switch S502 (RESET) is pressed
- · A software reset request is received
- IC1 and IC2 are not working correctly

## **D601 (M-1): DP GbE LINK**

Communication status of GbA for communication of inside the unit

This LED lights at link.

#### **D602 (M-1): DP GbE ACT**

Communication status of GbE for communication of inside the unit

This LED blinks during the transmission and reception of data.

#### D901-8 (N-1): 7seg digit1

D909-16 (N-1): 7seg digit0

#### D917-20 (F-3): BUS LED

Used only for design.

#### <Switch>

#### S1101 (E-1): GROUP/UNIT ID switch

This switch is used to set GROUP ID and UNIT ID connected to the LAN.

#### S501 (D-1): CA RESET switch

Reset switch for CA-92 board.

#### S502 (D-1): RESET switch

Reset switch for the overall the unit.

## S503 (K-1): CPU RESET switch

Reset switch for CPU-DP board.

## S601 (D-1): MON RESET switch

Software reset switch for CPU-DO board.

## S901 (F-2): SETTING2 switch

Used only for design.

#### S902 (F-1): SETTING1 switch

Used only for design.

#### <LED on the CPU-DP Module>

D1 (L-6): 3.3 V

Power supply status indication.

Lights when +3.3 V power is supplied to the regulator on CPU-DP module.

## D2 (L-6): 2.5 V

Power supply status indication.

Lights when +2.5 V power is supplied normally.

#### D3 (L-6): 1.8 V DDR

Power supply status indication.

Lights when +1.8 V power is supplied normally to the DDR2 on CPU-DP module.

## **D4** (L-6): 1.2 V

Power supply status indication.

Lights when +1.2 V power is supplied normally.

#### D5 (L-6): 1.1 V

Power supply status indication.

Lights when +1.1 V power is supplied normally.

xvs-8000 1-15 (E)

## D6 (R-4): CD

Lights when the connector on CPU-DP module connected to the base board normally.

## D7 (N-2): RUN

Lights when complete the boot process normally of CPU-DP module.

## **D8 to D11 (N-2): STATUS LED1 to 4**

Internal status indication of CPU-DP module. Controlled by software

## D12 (N-2): GbE1

Lights when linked Ethernet 1 on CPU-DP module. On this board, corresponds to Network A connection.

## D13 (N-2): GbE2

Lights when linked Ethernet 2 on CPU-DP module. On this board, corresponds to Network B connection.

#### D14 (N-2): SGMII1

Lights when linked SGMII 1 on CPU-DP module. This board supports Ethernet connections for internal communications.

#### D15 (N-2): SGMII2

Lights when linked SGMII 2 on CPU-DP module. Not used on this board.

## D16 (L-6): 3.3 V\_LV

Power supply status indication. Lights when +3.3 V power is supplied normally.

## D17 (L-4): 1.8 V PHY

Power supply status indication.

Lights when +1.8 V power is supplied normally to the PHY on CPU-DP module.

## <Switch on the CPU-DP Module>

## SW2 (P-2): CPU-DP MODE

Sets the start-up mode of CPU-DP module.

Default setting when shipped from the factory is all OFF.

1-16 (E) xvs-8000

## **OUT-38** board

```
13
- 10
12
    D104
11
10
9
8
6
                   D1<u>1</u>01
       S501
M
                                     G | H |
                                                                         NΙ
                                                                      OUT-38 board (Side A)
```

xvs-8000 1-17 (E)

## D1002 (B-1): POWER

Power supply status indication.

Lights in green: All powers are supplied on the board correctly.

Goes off: This LED goes off when the power supply has failure.

#### D103 (A-12): POWER

HOT SWAP CONTROLLER (IC101) status indication. Lights in green: The +12 V power is supplied correctly in the board.

Lights in red: Power supply in the board is blocked. The board failure may have occurred such as the power supply short.

Goes off: This LED goes off when the fuse (F102) may have blown.

#### D104 (A-11): +12 V

+12 V power supply status indication. Lights when the +12 V power is supplied.

#### D301 (C-1): +3.3 V

+3.3 V power supply status indication. Lights when the +3.3 V power is supplied.

## D302 (C-1): +2.5 V

+2.5 V power supply status indication. Lights when the +2.5 V power is supplied.

#### D303 (C-1): +1.8 V

+1.8 V power supply status indication. Lights when the +1.8 V power is supplied.

## D305, D306, D308 (D-1): +1.1 V-1, -2, -A

+1.1 V power supply status indication. Lights when the +1.1 V power is supplied.

#### D307 (D-1): REG

Analog power supply status indication. Lights when all power (+1.5 V-A1, -A2 and +1.2 V) are supplied.

## D401 (C-2): CC1 UNLOCK

Indicates lock/unlock of the clock conditioner for OUT, MSD, CCR.

If this LED lit, the clock conditioner can possibly be unlocked.

## D1001 (C-1): STATUS

For future function expansion.

#### D1003 (C-1): PLL UNLOCK

Indicates lock/unlock of the PLL (Phase Locked Loop) in the FPGA.

If this LED lit, the PLL can possibly be unlocked.

## D1004 (B-1): BECON

For future function expansion.

## D1101 (D-3): CADEC

Indicates the configuration error of the FPGA. If this LED lit, the FPGA (IC001) can possibly be working incorrectly.

## D1303 (F-1): OUT2

Indicates the configuration error of the FPGA. If this LED lit, the FPGA (IC003) can possibly be working incorrectly.

## D1304 (F-1): OUT1

Indicates the configuration error of the FPGA. If this LED lit, the FPGA (IC002) can possibly be working incorrectly.

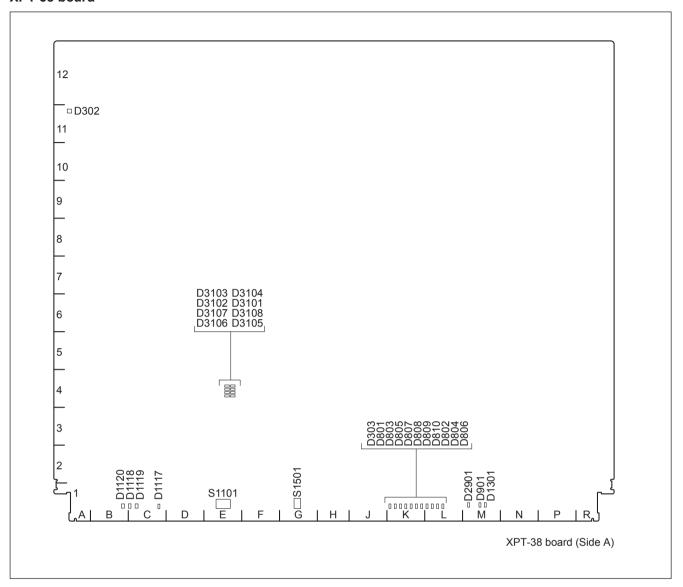
#### <Switch>

## S501 (M-1): RST

Used only for design.

1-18 (E) xvs-8000

## XPT-38 board



xvs-8000 1-19 (E)

## D1120 (B-1): POWER

Power supply status indication.

Lights in green: when all power supply on the board are normally. If the power supply has abnormality, turns off. Goes off: This LED goes off when the power supply has failure.

#### D302 (A-11): POWER

HOT SWAP CONTROLLER (IC301) status indication Lights in green: The +12 V power is supplied correctly in the board.

Lights in red: Power supply in the board is blocked. The board failure may have occurred such as the power supply short.

Goes off: This LED goes off when the fuse (F102) may have blown.

#### D303 (K-1): +12 V

+12 V power supply status indication.

Lights in green when the +12 V power is supplied.

#### D801 (K-1): +3.3 V

+3.3 V power supply status indication.

Lights in green when the +3.3 V power is supplied.

#### D802 (L-1): +2.5 V-XPT

+2.5 V-XPT power supply status indication.

Lights in green when the +2.5 V-XPT power is supplied.

#### D803 (K-1): +2.5 V

+2.5 V power supply status indication.

Lights in green when the +2.5 V power is supplied.

## D804 (L-1): +1.8 V-XPT

+1.8 V-XPT power supply status indication.

Lights in green when the +1.8 V-XPT power is supplied.

## D805 (K-1): +1.8 V

+1.8 V power supply status indication.

Lights in green when the +1.8 V power is supplied.

## D806 (L-1): +1.2 V-XPT

+1.2 V-XPT power supply status indication.

Lights in green when the +1.2 V-XPT power is supplied.

#### D807 (K-1): +1.5 V

+1.5 V power supply status indication.

Lights in green when the +1.5 V power is supplied.

#### D808 (K-1): +1.2 V

+1.2 V power supply status indication.

Lights in green when the +1.2V power is supplied.

#### D809 (K-1): +1.0 V

+1.0 V power supply status indication.

Lights in green when the +1.0 V power is supplied.

#### D810 (L-1): +0.95V

+0.95 V power supply status indication.

Lights in green when the +0.95 V power is supplied.

## D901 (M-1): CC UNLOCK

Indicates lock/unlock of the clock conditioner (IC902) for CADEC, MV.

If this LED lit, the clock conditioner can possibly be unlocked.

#### D2901 (M-1): CC2 UNLOCK

Indicates lock/unlock of the clock conditioner (IC2902) for MV.

If this LED lit, the clock conditioner can possibly be unlocked.

#### D1117 (C-1): CONF ERR

Status indication for CADEC (IC001) configuration operation

Lights in red when the configuration has been started up. Goes off when the configuration of IC001 is completed. If the LED does not go off, there is a possibility of failure of the IC001.

#### D1118 (C-1): BECON

For future function expansion.

#### D1119 (C-1): STATUS

For future function expansion.

## D1301 (M-1): MV

Status indication for FPGA (IC003) configuration opera-

Lights in green when the configuration of IC003 is completed.

If the LED does not light in green, there is a possibility of failure of the IC003.

## D3101 to D3108 (D-4): MV FPGA DEBUG LED

Used only for design

#### <Switch>

## S1101 (E-1): CADEC DEBUG

Used only for design.

## S1501 (G-1): RESET

Used only for design.

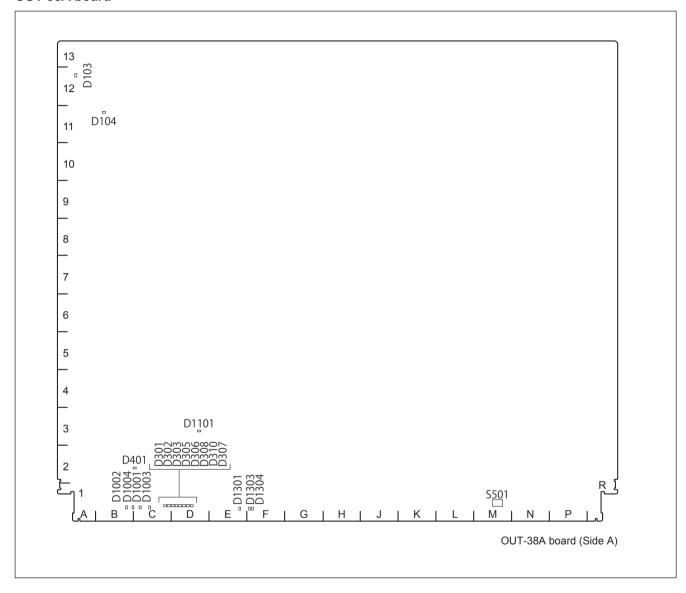
1-20 (E) xvs-8000

# 1-8-2. XKS-8160

## Note

The number shown in the parentheses ( ) indicated the address on the circuit board.

## **OUT-38A** board



xvs-8000 1-21 (E)

## D1002 (B-1): POWER

Power supply status indication.

Lights in green: All power are supplied on the board correctly.

Goes off: This LED goes off when the power supply has failure.

#### D103 (A-12): POWER

HOT SWAP CONTROLLER (IC101) status indication. Lights in green: The +12 V power is supplied correctly in the board.

Lights in red: Power supply in the board is blocked. The board failure may have occurred such as the power supply short.

Goes off: This LED goes off when the fuse (F102) may have blown.

#### D104 (A-11): +12 V

+12 V power supply status indication.

Lights when the +12 V power is supplied.

## D301 (C-1): +3.3 V

+3.3 V power supply status indication.

Lights when the +3.3 V power is supplied.

## D302 (C-1): +2.5 V

+2.5 V power supply status indication.

Lights when the +2.5 V power is supplied.

#### D303 (C-1): +1.8 V

+1.8 V power supply status indication.

Lights when the +1.8 V power is supplied.

## D305, D306, D308 (D-1): +1.1 V-1, -2, -A

+1.1 V power supply status indication.

Lights when the +1.1 V power is supplied.

#### D307 (D-1): REG

Analog power supply status indication.

Lights when all power (+1.5 V-A1, -A2 and +1.2 V) are supplied.

## D310 (D-1): +0.9 V

+0.9 V power supply status indication.

Lights when the +0.9 V power is supplied.

#### D401 (C-2): CC1 UNLOCK

Indicates lock/unlock of the clock conditioner for OUT, MSD, CCR.

If this LED lit, the clock conditioner can possibly be unlocked.

## D1001 (C-1): STATUS

For future function expansion.

## D1003 (C-1): PLL UNLOCK

Indicates lock/unlock of the PLL (Phase Locked Loop) in the FPGA.

If this LED lit, the PLL can possibly be unlocked.

#### D1004 (B-1): BECON

For future function expansion.

#### D1101 (D-3): CADEC

Indicates the configuration error of the FPGA.

If this LED lit, the FPGA (IC001) can possibly be working incorrectly.

#### D1301 (E-1): VPR

Indicates the configuration error of the FPGA.

If this LED lit, the FPGA (IC005) can possibly be working incorrectly

## D1303 (F-1): OUT2

Indicates the configuration error of the FPGA.

If this LED lit, the FPGA (IC003) can possibly be working incorrectly.

## D1304 (F-1): OUT1

Indicates the configuration error of the FPGA.

If this LED lit, the FPGA (IC002) can possibly be working incorrectly.

## <Switch>

#### S501 (M-1): RST

Used only for design.

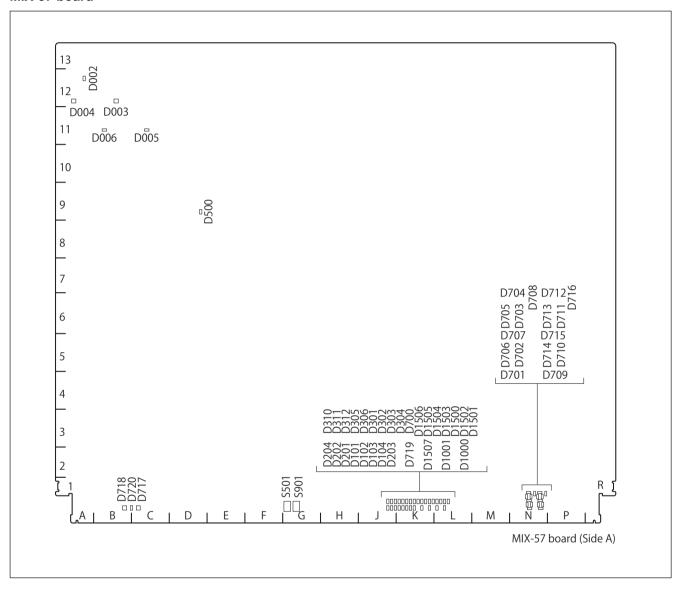
1-22 (E) xvs-8000

## 1-8-3. XKS-8210

## Note

The number shown in the parentheses ( ) indicated the address on the circuit board.

## MIX-57 board



xvs-8000 1-23 (E)

## D718 (B-1): POWER

Power supply status indication.

Lights in green when all power supply on the board are normally.

## D002 (A-12): 12V

+12 V power supply status indication.

Lights when the +12 V power is supplied.

If this LED does not light, the fuse (F001) may have blown.

#### D003 (B-12): PWR1

HOT SWAP CONTROLLER (IC002) status indication.

Lights in green: The +12 V power is supplied correctly in the board.

Lights in red: Power supply in the board is blocked. The board failure may have occurred such as the power supply short.

Goes off: The fuse (F001) may have blown.

## D004 (A-12): PWR2

HOT SWAP CONTROLLER (IC003) status indication.

Lights in green: The +12 V power is supplied correctly in the board.

Lights in red: Power supply in the board is blocked. The board failure may have occurred such as the power supply short..

Goes off: The fuse (F001) may have blown.

#### D005 (C-11): 12V-A

+12 V-A power supply status indication.

Lights when the +12 V-A power is supplied.

## D006 (B-11): 12V-B

+12 V-B power supply status indication.

Lights when the +12 V-B power is supplied.

## D101, D102, D103, D104 (K-1): +1.1 V-1, -2, -3, -4

+1.1 V power supply status indication.

Lights when the +1.1 V power is supplied.

#### D201 (J-1): +1.8 V

+1.8 V power supply status indication.

Lights when the +1.8 V power is supplied.

#### D202 (J-1): +2.5 V

+2.5 V power supply status indication.

Lights when the +2.5 V power is supplied.

#### D203 (K-1): +1.1 V-5

+1.1 V power supply status indication.

Lights when the +1.1 V power is supplied.

#### D204 (J-1) +3.3 V

+3.3 V power supply status indication.

Lights when the +3.3 V power is supplied.

#### D301, D302, D303, D304 (K-1): 1.1 V-A1, -A2, -A3, -A4

+1.1 V analog power supply status indication.

Lights when the +1.1 V power is supplied.

## D305, D306 (K-1): 1.2 V-A1, -A2

+1.2 V analog power supply status indication.

Lights when the +1.2 V power is supplied.

#### D310, D311, D312 (J-1): 1.5 V-A1, -A2, -A3

+1.5 V analog power supply status indication.

Lights when the +1.5 V power is supplied.

## D500 (D-9): CC UNLOCK

Indicates lock/unlock of the clock conditioner.

If this LED lit, the clock conditioner can possibly be unlocked.

## D700 (K-1): CAD1

Indicates the configuration error of the FPGA.

If this LED lit, the FPGA (IC1) can possibly be working Incorrectly.

## D701 to D716 (N-1): CPU status

Indicates the CPU status on the board.

## D717 (C-1): STATUS

For future function expansion.

## D719 (K-1): PLL UNLOCK

Indicates lock/unlock of the PLL (Phase Locked Loop) in

If this LED lit, the PLL can possibly be unlocked

#### D720 (B-1): BECON

For future function expansion.

#### D1000 (L-1): INET LINK

Lights when Ethernet 1 on CPU module links with CA board.

1-24 (E) xvs-8000

#### D1001 (L-1): INET ACT

Ethernet 1 on CPU module links with CA board and blinks when data send or receive is in progress.

## D1500 (L-1): WPG

Indicates the configuration error of the FPGA. If this LED lit, the FPGA (IC202) can possibly be working Incorrectly.

#### D1501 (L-1): R34

Indicates the configuration error of the FPGA. If this LED lit, the FPGA (IC302) can possibly be working incorrectly.

#### D1502 (L-1): R12

Indicates the configuration error of the FPGA. If this LED lit, the FPGA (IC301) can possibly be working incorrectly.

## D1503 (L-1): MIX

Indicates the configuration error of the FPGA. If this LED lit, the FPGA (IC201) can possibly be working incorrectly.

#### D1504 (K-1): K34

Indicates the configuration error of the FPGA. If this LED lit, the FPGA (IC402) can possibly be working incorrectly.

#### D1505 (K-1): K12

Indicates the configuration error of the FPGA. If this LED lit, the FPGA (IC401) can possibly be working incorrectly.

## D1506 (K-1): CAD2

Indicates the configuration error of the FPGA. If this LED lit, the FPGA (IC21) can possibly be working incorrectly.

## D1507 (K-1): CONF ERR

Indicates the configuration error of the FPGA. If this LED lit, any FPGA can possibly be working incorrectly.

#### <Switch>

## S501 (G-1): RST

Reset the MIX board. Pressing this switch initializes the MIX board.

When pressing this switch, the initialization of the MIX board is performed.

#### S901 (G-1): MON

For monitor switch used in maintaining through the terminal connector.

#### <LED on the CPU-DP Module>

Refer to <LED on the CPU-DP Module> in "CA-82 board".

## <Switch on the CPU-DP Module>

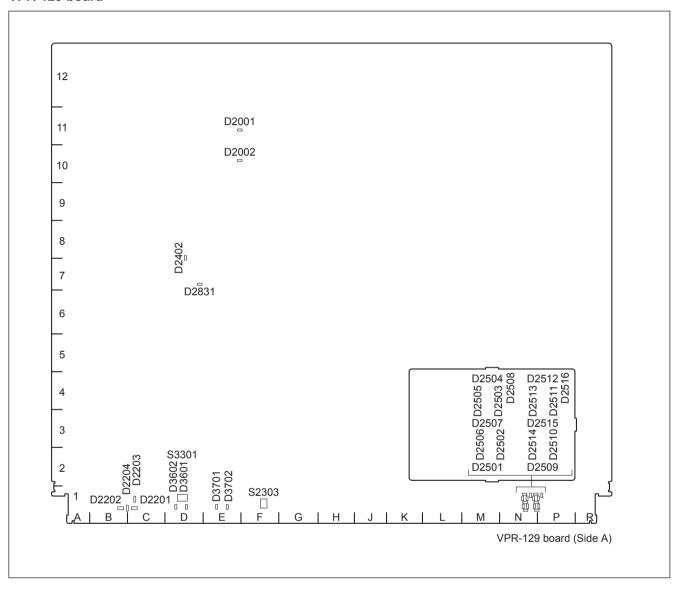
Refer to <Switch on the CPU-DP Module> in "CA-82 board".

# 1-8-4. XKS-8440

## Note

The number shown in the parentheses ( ) indicated the address on the circuit board.

## VPR-129 board



1-26 (E) xvs-8000

## D2001 (E-11): CC UNLOCK

Indicates lock/unlock of the clock conditioner (IC2005). If this LED lit, the clock conditioner can possibly be unlocked.

## D2002 (E-10): CC UNLOCK

Indicates lock/unlock of the clock conditioner (IC2004). If this LED lit, the clock conditioner can possibly be unlocked.

## D2201 (C-1): STATUS

For future function expansion.

## D2202 (B-1): POWER

Power supply status indication.

Lights in green: All power supply on the board are normally

Goes off: If the power supply has abnormality, goes off.

## D2203 (C-1): INIT ERR

Status indication for the initialization operation of CADEC (IC2)

Lights in red when the IC2 has been started up. Goes off when the initialization operation is completed.

If the LED does not go off, there is a possibility of failure of the IC2.

#### D2204 (C-1): BECON

For future function expansion.

## D2402 (D-8): CONF ERR

Status indication for CADEC (IC2) configuration operation.

Lights in red when the configuration has been started up. Goes off when the configuration of IC2 is completed. If the LED does not go off, there is a possibility of failure of the IC2.

## D2831 (D-7): CPU RESET

Lights when CPU (IC3) has been reset.

#### D2501 to D2516 (P-1): CPU STATUS

Status indication for CPU on the board

#### D3601 (D-1): LED1

ETHERNET\_LED1

## D3602 (D-1): LED0

ETHERNET\_LED0

D3701 (E-1): LED3

Not used.

D3702 (E-1): LED2

Not used.

#### <Switch>

## S2303 (F-1): BOOT SEL SWITCH

This switch is used to select the area of the Boot ROM.

#### S3301 (D-1): RE CONFIG

This switch is used to reboot the board manually.

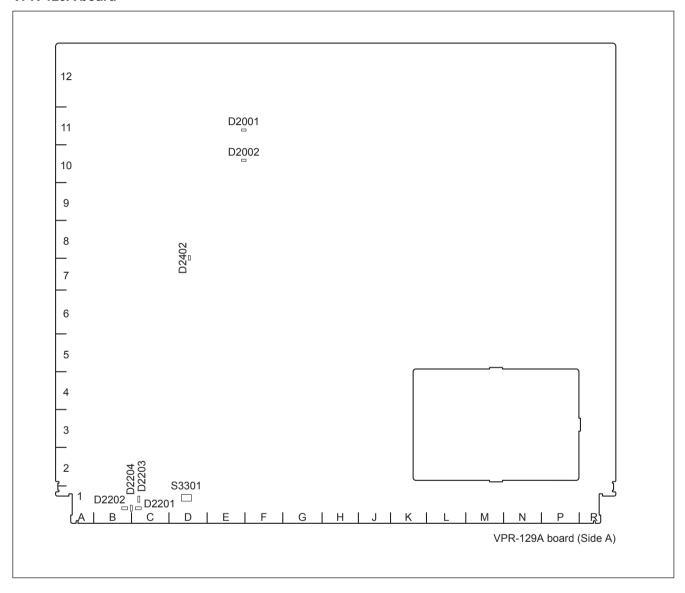
XVS-8000 1-27 (E)

# 1-8-5. XKS-8460

# Note

The number shown in the parentheses () indicated the address on the circuit board.

## VPR-129A board



1-28 (E) xvs-8000

# D2001 (E-11): CC\_UNLOCK

Indicates lock/unlock of the clock conditioner (IC2005). If this LED lit, the clock conditioner can possibly be unlocked.

## D2002 (E-10): CC UNLOCK

Indicates lock/unlock of the clock conditioner (IC2004). If this LED lit, the clock conditioner can possibly be unlocked.

## D2204 (C-1): BECON

For future function expansion.

## D2201 (C-1): STATUS

For future function expansion.

## D2202 (B-1): POWER

Power supply status indication.

Lights in green: All power supply on the board are normally.

Goes off: If the power supply has abnormality, goes off.

## D2203 (C-1): INIT ERR

Status indication for the initialization operation of CADEC (IC2).

Lights in red when the IC2 has been started up. Goes off when the initialization operation is completed.

If the LED does not go off, there is a possibility of failure of the IC2.

## D2402 (D-8): CONF ERR

Status indication for CADEC (IC2) configuration operation.

Lights in red when the configuration has been started up. Goes off when the configuration of IC2 is completed. If the LED does not go off, there is a possibility of failure of the IC2.

## <Switch>

## S3301 (D-1): RE CONFIG

This switch is used to reboot the board manually.

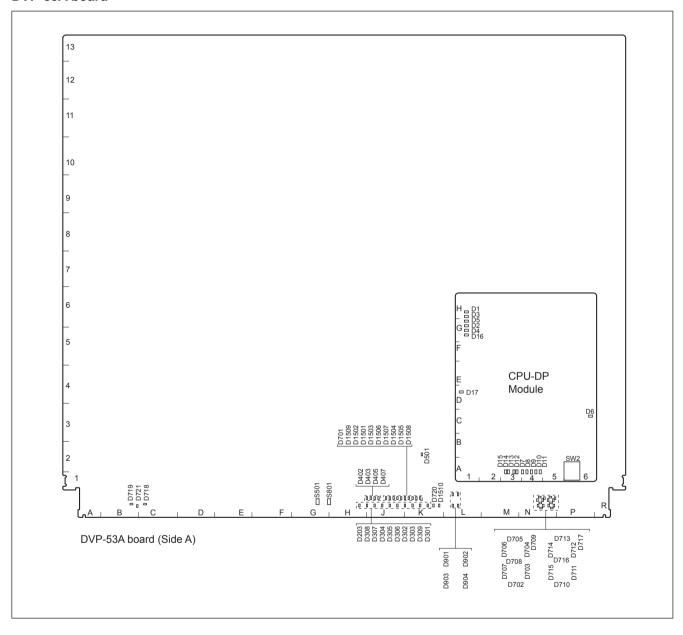
xvs-8000 1-29 (E)

## 1-8-6. XKS-8470

## Note

The number shown in the parentheses () indicated the address on the circuit board.

## **DVP-53A** board



1-30 (E) xvs-8000

#### <LED>

#### D719 (B-1): POWER

Power supply status indication.

Lights in green when all power supply on the board are normally.

## D203 (H-1): +12 V

+12 V power supply status indication.

Lights when the +12 V power is supplied. If this LED does not light, the fuse may have blown.

## D301 (K-1): +1.0 V

+1.0 V power supply status indication.

Lights when the +1.0 V power is supplied.

#### D302, D303, D309 (K-1): +1.1 V-1, -2, -3

+1.1 V power supply status indication.

Lights when the +1.1 V power is supplied.

#### D304, D305, D306 (J-1): +1.8 V-1, -2, -3

+1.8 V power supply status indication.

Lights when the +1.8 V power is supplied.

#### D307 (J-1): +2.5 V

+2.5 V power supply status indication.

Lights when the +2.5 V power is supplied.

#### D308 (J-1): +3.3 V

+3.3 V power supply status indication.

Lights when the +3.3 V power is supplied.

#### D402 (J-1): +1.0 V-AVCC1

+1.0 V analog power supply status indication.

Lights when the +1.0 V power is supplied.

## D403, D405 (J-1): +1.2 V-APLL1, +1.2 V-AVT1

+1.2 V analog power supply status indication.

Lights when the +1.2 V power is supplied.

#### D407 (J-1): +1.2 V

+1.2 V power supply status indication.

Lights when the +1.2 V power is supplied.

## **D501 (K-2): CC\_UNLOCK**

Indicates lock/unlock of the clock conditioner.

If this LED lit, the clock conditioner can possibly be

unlocked.

#### D701 (J-1): CAD1

Indicates the configuration error of the FPGA.

If this LED lit, the FPGA can possibly be working incorrectly

#### D702 to D717 (N-1): CPU

CPU on the board status indication.

#### D718 (C-1): STATUS

For future function expansion.

#### D720 (K-1): PLL UNLOCK

Indicates lock/unlock of the PLL (Phase Locked Loop) in the FPGA.

If this LED lit, the PLL can possibly be unlocked.

## D721 (A-1): BECON

For future function expansion.

## D901 (L-1): INET SWER LINK

Lights when Ethernet 1 on CPU module links with CA board.

#### D902 (L-1): INET SWER ACT

Ethernet 1 on CPU module links with CA board and blinks when data send or receive is in progress.

### D903 (L-1): INET DME LINK

For test LED.

#### D904 (L-1): INET DME ACT

For test LED.

#### D1509 (J-1): CAD2

Indicates the configuration error of the FPGA.

If this LED lit, the FPGA (IC21) can possibly be working incorrectly.

## D1510 (K-1): CONF ERR

Indicates the configuration error of the FPGA.

If this LED lit, any FPGA can possibly be working incorrectly.

## D1502 (J-1): SDI1

Indicates the configuration error of the FPGA.

If this LED lit, the FPGA (IC5) can possibly be working incorrectly.

xvs-8000 1-31 (E)

#### D1501 (J-1): SDI2

Indicates the configuration error of the FPGA. If this LED lit, the FPGA (IC6) can possibly be working incorrectly.

## D1503 (J-1): PVMX

Indicates the configuration error of the FPGA. If this LED lit, the FPGA (IC11) can possibly be working incorrectly.

#### D1506 (K-1): FINTP1

Indicates the configuration error of the FPGA. If this LED lit, the FPGA (IC7) can possibly be working incorrectly.

# D1507 (K-1): FINTP2

Indicates the configuration error of the FPGA. If this LED lit, the FPGA (IC8) can possibly be working incorrectly

#### D1504 (K-1): AINTP1

Indicates the configuration error of the FPGA. If this LED lit, the FPGA (IC9) can possibly be working incorrectly.

#### D1505 (K-1): AINTP2

Indicates the configuration error of the FPGA. If this LED lit, the FPGA (IC10) can possibly be working incorrectly.

#### D1508 (K-1): VMIX

Indicates the configuration error of the FPGA. If this LED lit, the FPGA (IC12) can possibly be working incorrectly.

#### <Switch>

#### S501 (G-1): RST

This switch is used to reset the DVP board. Pressing this switch initializes the DVP board.

#### S801 (G-1): MON

For monitor switch used in maintaining through the terminal pin.

## <LED on the CPU-DP Module>

Refer to <LED on the CPU-DP module> in "CA-92 board".

## <Switch on the CPU-DP Module>

Refer to <Switch on the CPU-DP module> in "CA-92 board".

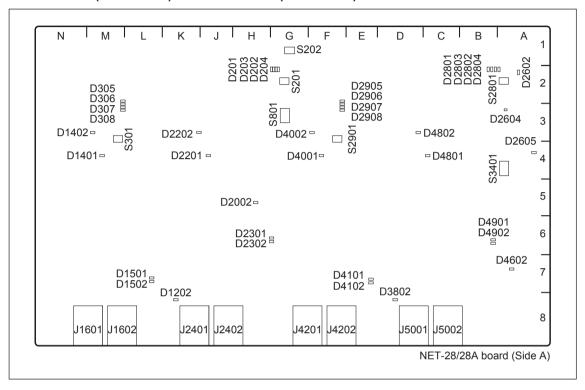
1-32 (E) xvs-8000

# 1-8-7. XKS-T8110/XKS-T8165

Note

The number shown in the parentheses () indicated the address on the circuit board.

# NET-28board (XKS-T8110) /NET-28A board (XKS-T8165)



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#### <LED>

## D201 (G-2): CADEC-A

Indicates the startup mode of the FPGA.

Lights in Recovery Mode.

## D202 (G-2): CADEC-A

Not used. (Default: Goes off)

#### D203 (G-2): CADEC-A

Indicates the startup mode of the FPGA.

Lights in Normal Mode.

#### D204 (G-2): CADEC-A

Indicates the configuration status of the FPGA.

Goes off when the configuration has been completed.

## D305 (M-2): FPGA-A

Indicates the System Clock.

Blinks if clock is supplied.

#### D306 (M-2): FPGA-A

Indicates the Video Clock 148 MHz.

Blinks if clock is supplied.

## D307 (M-2): FPGA-A

Not used. (Default: Goes off)

### D308 (M-2): FPGA-A

Not used. (Default: Goes off)

#### D1202 (K-8): NEP2-A

Blinks when the ASIC is started up.

#### D1401 (M-4): NEP2-A

Indicates the eTSync status.

Lights: Leader state

Goes off: Follower. state

#### D1402 (M-3): NEP2-A

Indicates the System frequency.

Lights: 1001 system Goes off: 1000 system

## D1501 (L-7): PHY-A

Indicates the Master/Slave of PHY Port0.

Lights: Master mode Goes off: Slave mode

#### D1502 (L-7): PHY-A

Indicates the Master/Slave of PHY Port1.

Lights: Master mode Goes off: Slave mode

## D2002 (H-8): NEP2-B

Blinks if the ASIC is started up.

#### D2201 (J-4): NEP2-B

Indicates the eTSync status.

Lights: Leader state Goes off: Follower. state

#### D2202 (J-3): NEP2-B

Indicates the System frequency.

Lights: 1001 system Goes off: 1000 system

#### D2301 (G-6): PHY-B

Indicates the Master/Slave of PHY Port0.

Lights: Master mode Goes off: Slave mode

#### D2302 (G-6): PHY-B

Indicates the Master/Slave of PHY Port1.

Lights: Master mode Goes off: Slave mode

#### **D2602 (A-1): POWER**

+12 V-P power supply status indication.

Lights when the +12 V-P power is supplied.

If this LED does not light, the fuse (F2601) may has blown.

#### D2604 (A-2): PWR1

Indicates the status of HOT SWAP CONTROLLER

(IC002)

Lights in green: The +12 V-A power is supplied correctly

in the board.

Lights in red: Power supply in the board is blocked. The board failure may have occurred such as the power supply

short.

#### D2605 (A-3): POWER

+12 V power supply status indication.

Lights when the +12 V power is supplied.

## D2801 (B-2): CADEC-B

Indicates the startup mode of the FPGA.

Lights in Recovery Mode.

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#### D2802 (B-2): CADEC-B

Not used. (Default: Goes off)

#### D2803 (B-2): CADEC-B

Indicates the startup mode of the FPGA.

Lights in Normal Mode.

## D2804 (B-2): CADEC-B

Indicates the configuration status of the FPGA Goes off when the configuration has been completed.

#### D2905 (F-2): FPGA-B

Indicates the System Clock. Blinks if clock is supplied.

#### D2906 (F-2): FPGA-B

Indicates the Video Clock 148 MHz. Blinks if clock is supplied.

## D2907 (F-2): FPGA-B

Not used. (Default: Goes off)

#### D2908 (F-2): FPGA-B

Not used. (Default: Goes off)

#### D3802 (D-8): NEP2-C

Blinks when the ASIC is started up.

#### D4001 (F-4): NEP2-C

Indicates the eTSync status.

Lights: Leader state
Goes off: Follower state

#### D4002 (F-3): NEP2-C

Indicates the System frequency.

Lights: 1001 system. Goes off: 1000 system

#### D4101 (E-7): PHY-C

Indicates the Master/Slave of PHY Port0.

Lights: Master mode Goes off: Slave mode

## D4102 (E-7): PHY-C

Indicates the Master/Slave of PHY Port1.

Lights: Master mode Goes off: Slave mode

#### D4602 (A-7): NEP2-D

Blinks when the ASIC is started up.

#### D4801 (C-4): NEP2-D

Indicates the eTSvnc status.

Lights: Leader state Goes off: Follower. state

## D4802 (D-3): NEP2-D

Indicates the System frequency.

Lights: 1001 system. Goes off: 1000 system

### D4901 (B-6): PHY-D

Indicates the Master/Slave of PHY Port0.

Lights: Master mode Goes off: Slave mode

#### D4902 (B-6): PHY-D

Indicates the Master/Slave of PHY Port1.

Lights: Master mode Goes off: Slave mode

#### J1601: RJ-45 LAN1 LED

Indicates the network status.

The left side LED (orange) is lit when the port has been linked up, and the right side LED (yellow green) is blinked when the port forwards the packet.

#### J1602: RJ-45 LAN2 LED

Indicates the network status.

The left side LED (orange) is lit when the port has been linked up, and the right side LED (yellow green) is blinked when the port forwards the packet.

#### J2401: RJ-45 LAN3 LED

Indicates the network status.

The left side LED (orange) is lit when the port has been linked up, and the right side LED (yellow green) is blinked when the port forwards the packet.

#### J2402: RJ-45 LAN4 LED

Indicates the network status.

The left side LED (orange) is lit when the port has been linked up, and the right side LED (yellow green) is blinked when the port forwards the packet.

#### J4201: RJ-45 LAN5 LED

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Indicates the network status.

The left side LED (orange) is lit when the port has been linked up, and the right side LED (yellow green) is blinked when the port forwards the packet.

#### J4202: RJ-45 LAN6 LED

Indicates the network status.

The left side LED (orange) is lit when the port has been linked up, and the right side LED (yellow green) is blinked when the port forwards the packet.

#### J5001: RJ-45 LAN7 LED

Indicates the network status.

The left side LED (orange) is lit when the port has been linked up, and the right side LED (yellow green) is blinked when the port forwards the packet.

#### J5002: RJ-45 LAN8 LED

Indicates the network status.

The left side LED (orange) is lit when the port has been linked up, and the right side LED (yellow green) is blinked when the port forwards the packet.

#### <Switch>

## S201 (G-2): CPLD-A SW

Used only for design.

Factory default setting: All off

Do not change the setting of this switch.

## S202 (G-1): Board RESET

Used only for design.

Factory default setting: All off

Do not change the setting of this switch.

## S301 (M-3): FPGA-A\_SW

Used only for design.

Factory default setting: All off

Do not change the setting of this switch.

## S801 (G-3): FPGA-A CFG

Used only for design.

Factory default setting: All off

Do not change the setting of this switch.

## S2801 (A-2): CPLD-B SW

Used only for design.

Factory default setting: All off

Do not change the setting of this switch.

## S2901 (F-3): FPGA-A\_SW

Used only for design.

Factory default setting: All off

Do not change the setting of this switch.

## S3401 (A-4): FPGA-B CFG

Used only for design.

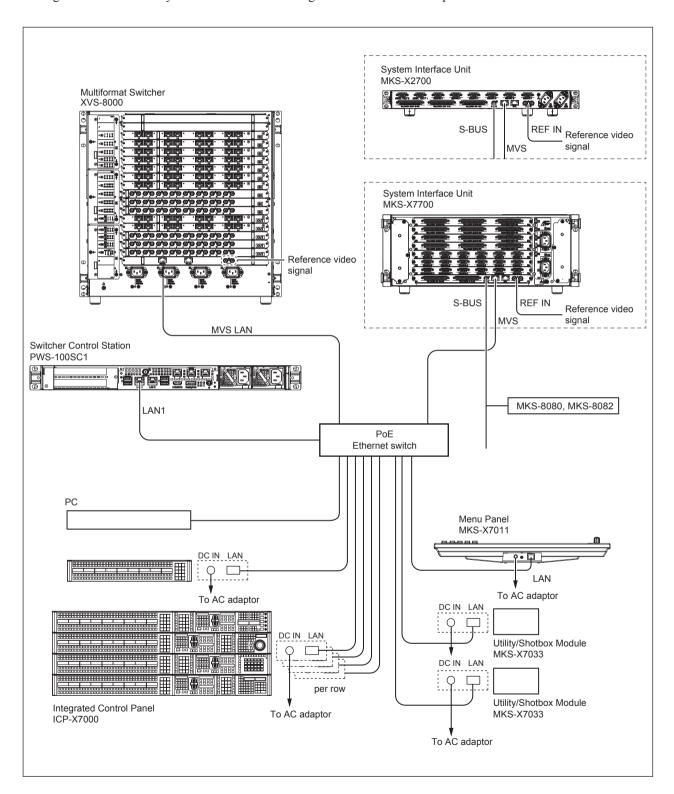
Factory default setting: All off

Do not change the setting of this switch.

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# 1-9. System Connection

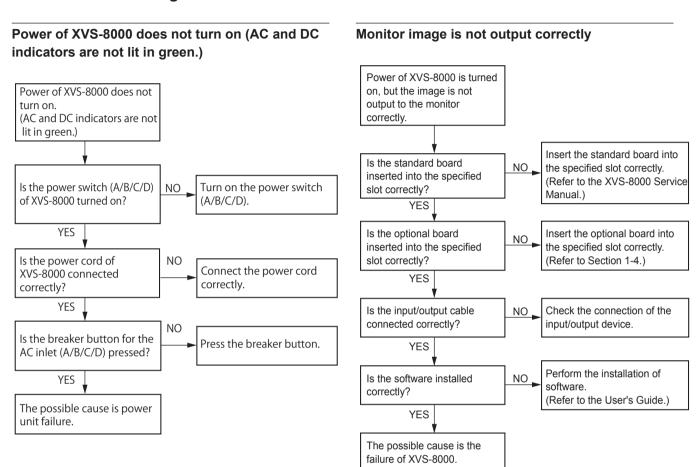
Configure the XVS-8000 system connections referring to the connection example as shown below.



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# Section 2 Service Overview

# 2-1. Troubleshooting



# 2-2. Periodic Inspection and Maintenance

## 2-2-1. Periodic Inspection

The following parts require periodic maintenance.

Refer to the period indicated in the following list for maintenance.

Part	Where used	Maintenance	Suggested period
Fan	Right side panel(rear side) of the XVS-8000	Cleaning Replacement	Once in a month Once in about four years
Filter	Front panel of the XVS-8000	Cleaning	Once in two months
Power supply unit	XVS-8000	Replacement	Once in about four and a half years
Lithium battery	VPR-129 board of XKS-8440	Replacement	Once in about five years

xvs-8000 2-1 (E)

## 2-2-2. Cleaning

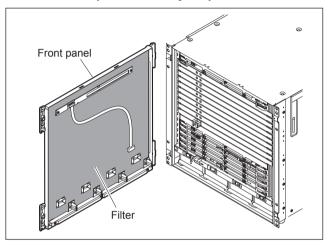
## 1. Front panel

The filter on the rear of the front panel can easily accumulate the dust. Be sure to remove dust by cleaning as follows.

- (1) Remove the front panel. (Refer to Section 1-4-1.)
- (2) Remove the dust accumulated on the filter with a vacuum cleaner.

## Note

Cleaning the filter by washing in water is recommended when there is a heavy accumulation of dust. Be sure to dry the filter completely after it has been washed.



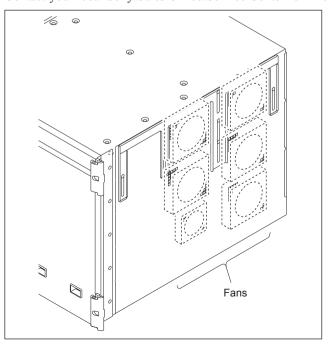
## 2. Fan

XVS-8000 is air-cooled by the fans.

If dust has accumulated in the intake of the fan, air is prevented from flowing smoothly and this may result in a temperature rise inside the unit. This may have an adverse effect on performance and the life of the machine.

Cleaning of the fan every month is recommended.

Contact your local Sony Sales Office/Service Center for information on cleaning the fan.



2-2 (E) xvs-8000