HDCC-B37MULTI

(CEA-608/CEA-708/ARIB-B37 Multi-Function Card)

Multi-Purpose, HD/SD-SDI Closed Caption Card: Inserter, Decoder, Bridge, Monitor, and Transcoder

Configuration Guide

Software Version: V2.33

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Part Number 821180, Revision A

Wohler CONFIDENCE.

VIDEO AUDIO CAPTIONING LOUDNESS

31055 Huntwood Avenue
Hayward, California
94544 USA

1 800 5 WOHLER
+1 (510) 870-0810
Fax +1 (510) 870-0811

www.wohler.com
info@wohler.com

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Customer Support

Wohler Technologies, Inc.
31055 Huntwood Avenue
Hayward, CA 94544
www.wohler.com

Phone: 510-870-0810
FAX: 510-870-0811
US Toll Free: 1-888-596-4537
(1-888-5-WOHLER)
Web: www.wohler.com
Sales: sales@wohler.com
Support: support@wohler.com

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CHAPTER 1
Setting the Switches

Introduction

Overview

Thank you for purchasing Wohler's HDCC card, a product that provides a variety of closed-captioning functions. This chapter explains how to install, set up, and use your card.

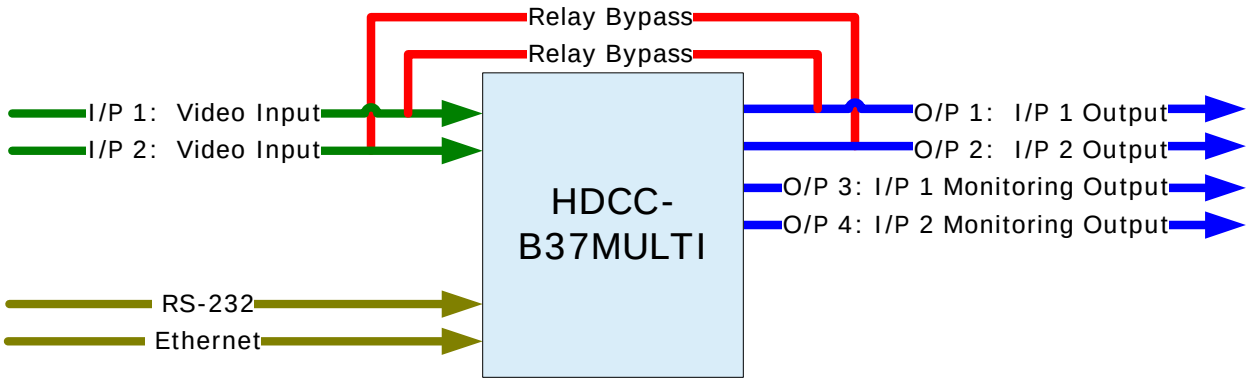
Topics

Basic Functionality

Inputs and Outputs

Figure 1–1 illustrates the basic inputs and outputs of the HDCC. In the event of a power failure, or when the system is powered down, the bypass relays engage and the outputs on O/P 1 and O/P 2 are identical to the input signals on I/P 1 and I/P 2 respectively.

Figure 1–1 I/O Functionality - No Power



Note: For Figure 1–1 above through Figure 1–8 on page 13, the illustrations depict either a Codan or Evertz rear panel adapter. The IRT has two RS-232 ports and no Ethernet port.

Processes

Encoding

Encoding allows the broadcaster to embed, or encode, caption text from a separate source into the video/audio stream. For example, the video/audio stream would come in on I/P 1, and the caption data would come in on the serial port from a PC on which an operator keys in the text of the caption data. Then the encoded video/audio/data stream would come out on O/P 1. O/P 3 provides burnt-in captions, literally superimposing the text of the caption data on the video display. Refer to Figure 1–2 on page 4 for a summary of settings for Switch 1 (SW1).

Decoding

Decoding is the process of extracting closed caption text data from an SDI video stream, interpreting it, and making it available in human-readable form. For example, if a video/audio stream with embedded caption data is supplied to I/P 1, the caption text is decoded and supplied through a serial and/or Ethernet connection to a host PC for display. Note that the closed captioned text is not removed from the input video stream. Refer to Figure 1–2 on page 4 for a summary of settings for Switch 1 (SW1).

Bridging / Transcoding

Bridging is the process of extracting closed captioning data from one SDI data stream and inserting it into another. For example, a video stream with embedded text would come in on I/P 1 and a clean video stream would come in on I/P 2. In this scenario, the HDCC card would copy the closed caption text data coming in on I/P 1 and embed it into the video stream of I/P 2. Note that the closed captioned text is not removed from the source input video stream. If the two signals are different, say SD and HD, then this scenario also exemplifies transcoding.

Note: The transcoding operation will only work with the 608/708 standards

Table 1–1 Valid Switch Settings Switch 2 (SW2)

Switch 2	Bridge Operation
0	None
1	A→ B (both fields)
2	B→ A (both fields)
3	A→ B (Field 1)
4	B → A (Field 1)
5	A→ B (Field 2)
6	B → A (Field 2)
7 thru F	None

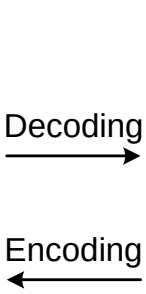
Monitoring

The HDCC also provides burnt-in caption outputs (also called open captions) on O/P 3 and O/P 4 to monitor the closed captions on O/P 1 and O/P 2 respectively.

Ethernet and Serial Communications Summary

Figure 1–2 illustrates the direction of communications between the input channels (I/P 1 and I/P 2, aka Channel A and Channel B) and the serial and Ethernet ports, depending on the settings of SW1.

Figure 1–2 Serial and Ethernet Communications for SW1 Settings)

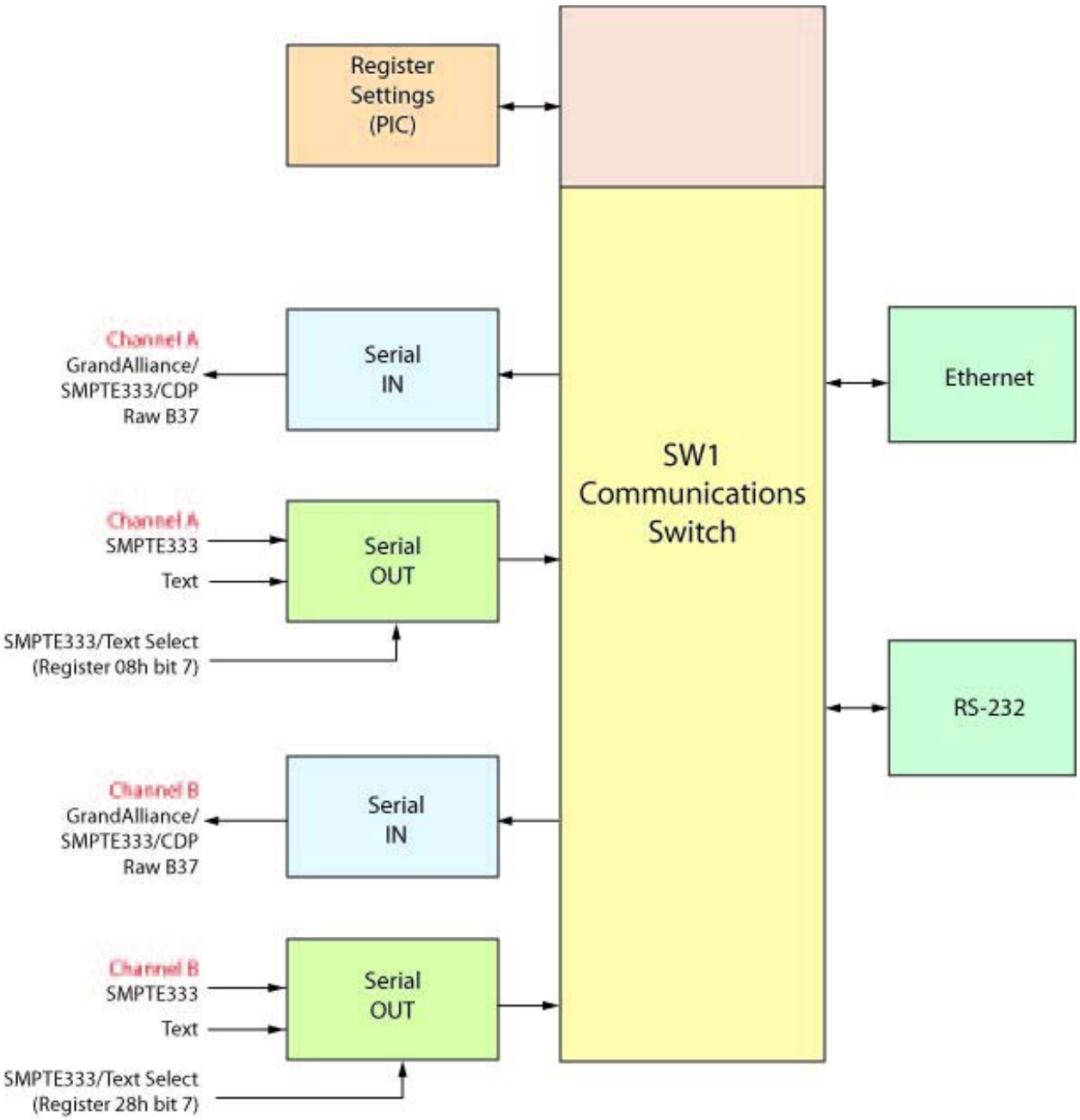


SW 1	Channels	Ports
0	A ← Serial B ← Ethernet	
1	A ← Serial B ← Ethernet	
2	A ← Serial B ← Ethernet	
3	A ↔ Serial B ↔ Ethernet	
4	A ↔ Serial B ↔ Ethernet	
5	A ↔ Serial B ↔ Ethernet	
6	A ← Serial B → Ethernet	
7	A → Serial B ← Ethernet	
8,B	A → Serial B → Ethernet	
9,A	A → Serial B → Ethernet	
E	Register Settings	Serial 38400 baud
F	Register Settings	Ethernet 38400 baud

Communications Paths

Figure 1–3 below illustrates the serial and Ethernet communication as it relates to the SMPTE333 handshake settings in Registers 08h and 28h.

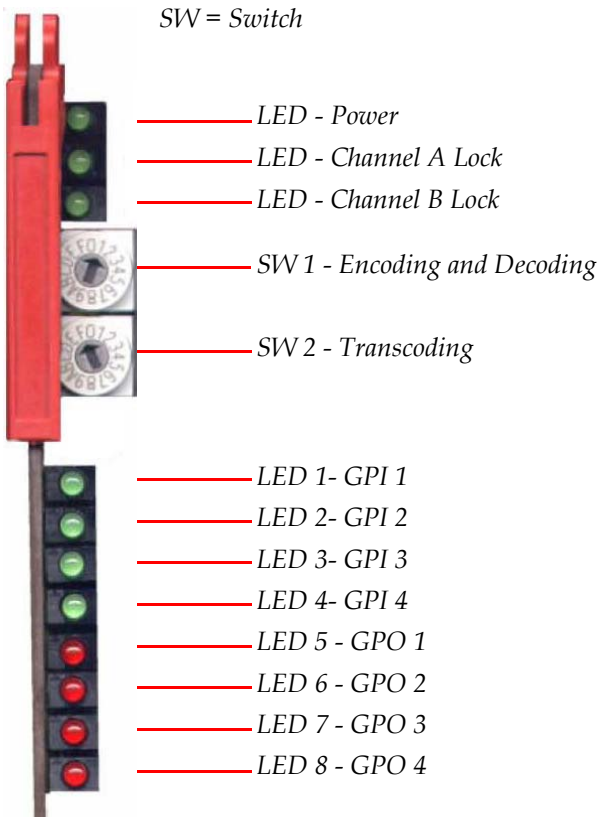
Figure 1–3 Serial and Ethernet Communications



Using the Card

Using the HDCC card amounts to little more than setting the switches, attaching the I/O cables, and connecting a PC through either an Ethernet or a serial port to encode or decode. (But a PC connection is not needed for bridging or transcoding.)

Figure 1–4 HDCC Front Panel



Note: The general purpose input and/or output (GPI/O) functions and pin outs are described in Connector Pin Assignments on page 47.

Refer to Register 0Eh for LED assignments: Table 2-3 on page 25.

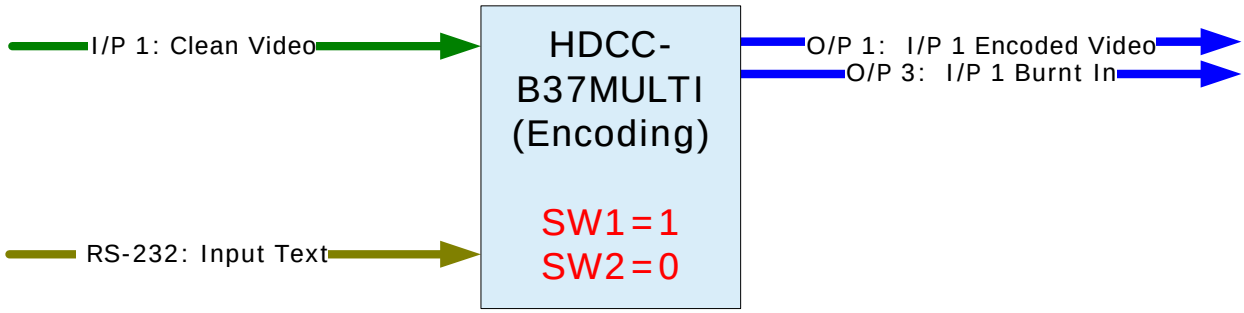
- **Switch 1 (SW 1)** controls whether the card is set to **encoding** (settings 0 through 2), **encoding and decoding** (settings 3 through 7) or **decoding** (settings 8 or 9). This is the communications router.
- **Switch 2 (SW 2)** controls whether the card performs the **bridging** and **transcoding** functions (settings 1 or 2).

Also refer to Figure 1–2 on page 4.

Encoding Only

When encoding, a signal that comes in on I/P 1 goes out on the odd-numbered outputs, and a signal that comes in on I/P 2 goes out on the even-numbered outputs. See Figure 1–5 below.

Figure 1–5 Typical Scenario for Encoding Only



Example: This simple example demonstrates the encoding feature.

I/P 1: Input clean video stream.

I/P 2: NC

O/P 1: Output video stream with encoded closed captions.

O/P 2: NC

O/P 3: Monitor video output with burnt-in (OSD) decoded captions.

O/P 4: NC

Serial: Closed caption data input.

Ethernet: NC

1. Connect a clean video source to I/P 1.
2. Connect an output cable to O/P 1.
3. Connect a video monitor to O/P 3.
4. Connect a data source for closed captioned text to the RS-232 port.

   Note: To use the Ethernet port as a virtual serial port, refer to Appendix A of the *Installation Guide* (PN 821135) to download, install, and configure the Ethernet connectivity application.

5. Set SW 1 according the port through which you will insert the closed caption text. For our example, set SW 1 to 1. See Table 1–2 below a list of additional options.

Table 1–2 Switch 1 Settings - Encoding Only

SW 1	Insert Text From
0	Inserts captions on I/P 1 from the RS-232 port. Inserts captions on I/P 2 from the Ethernet port.
1	Inserts captions on I/P 1 and on I/P 2 from the RS-232 port.
2	Inserts captions on I/P 1 and on I/P 2 from the Ethernet port.

6. Set SW 2 to 0.

7. Launch the closed caption text insertion application and verify that you have connected to the HDCC using the correct com port at the correct baud rate.

8. Begin sending text from your closed caption text insertion application.

Providing the HDCC card is locked on an video signal, as soon as it receives closed caption data, it will immediately encode this closed caption data into the output video streams O/P1 and O/P 3.

Note: You can repeat the previous steps 1 through 5 (using I/P 2, O/P 2, and O/P 4 in Steps 1 through 3 respectively) to encode a second video stream with either the same or a different text source/communications port since the HDCC is a dual-channel card. Moreover, the signals need not be synchronous.

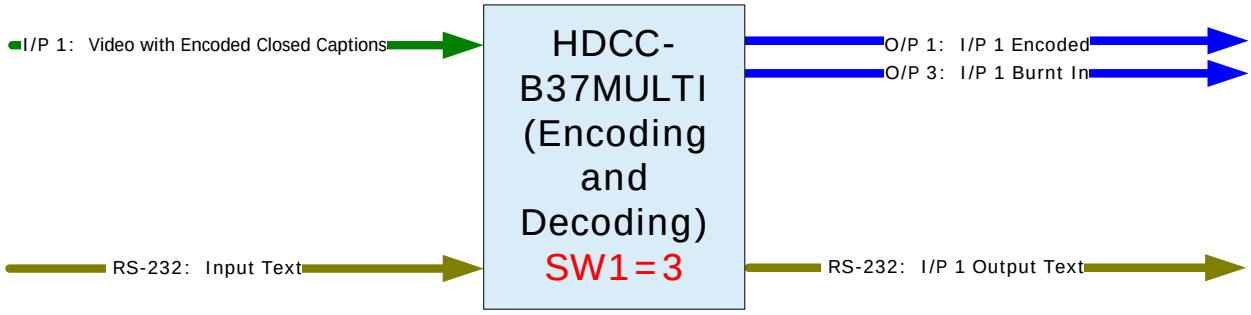
Important: If you have difficulty getting the correct results on the ouput connectors/ports, you may need to modify some of the register settings to fit your particular installation. Refer to Chapter 2: Setting the Engineering Registers on page 15 for more information.

Encoding and Decoding

The HDCC card can simultaneously decode (extract) and encode closed captions on each channel.

Important: You cannot decode the captions you are currently encoding. To decode, the captions must already be on the incoming data stream.

Figure 1–6 Typical Scenario for Encoding and Decoding



Example: This simple example demonstrates the encoding and the decoding features.

I/P 1: Input video stream with encoded closed captions.

I/P 2: NC

O/P 1: Output video stream with encoded closed captions.

O/P 2: NC

O/P 3: Monitor video output with burnt-in (OSD) decoded captions.

O/P 4: NC

Serial or Ethernet: Send closed caption data to O/P 1, and receive closed caption data from I/P 1 (see table below).

1. Connect a video source with encoded closed captions to I/P 1.
2. Connect a video output cable to O/P 1.
3. (Optional) Connect a video monitor to O/P 3.
4. Connect a data source for closed captioned text to the RS-232 port.

   Note: To use the Ethernet port as a virtual serial port, refer to Appendix A of the *Installation Guide* (PN 821135) to download, install, and configure the Ethernet connectivity application, if you have not already done so.

5. Set SW 1 according the port through which you will insert the closed caption text. For our example, set SW 1 to 3. See Table 1–3 below for a list of additional options.

Table 1–3 Switch 1 Settings - Encoding and Decoding

SW 1	Description
3	Insert captions on O/P 1 from the serial port. Insert captions on O/P 2 from the Ethernet port. Decode I/P 1 and send the text data to the serial port. Decode I/P 2 and send the text data to the Ethernet port.
4	Insert captions on O/P 1 and O/P 2 from the serial port. Decode I/P 1 and send the text data out both the serial and the Ethernet ports.
5	Insert captions on O/P 1 and O/P 2 from the Ethernet port. Decode I/P 2 and send out text data on both the serial and the Ethernet ports.
6	Insert captions on O/P 1 and O/P 2 from the serial port. Decode I/P 2 and send out text data on both the serial and the Ethernet ports.
7	Insert captions on O/P 1 and O/P 2 from the Ethernet port. Decode I/P 1 and send out text data on both the serial and the Ethernet ports.

6. Launch your closed caption text insertion application and verify that you have connected to the HDCC using the correct com port at the correct baud rate.

7. Begin sending text from your closed caption text insertion application.

Providing the HDCC card is locked on an video signal, as soon as it receives closed caption data, it will immediately encode this closed caption data into the output video streams O/P1 and O/P 3.

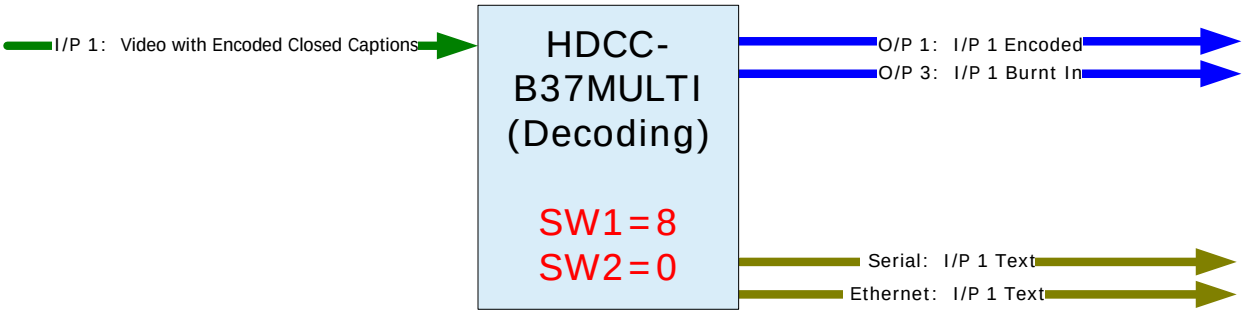
The serial port will operate in both directions: input to the serial port will be encoded as closed captions; output from the serial port will provide closed caption data decoded from the video input.

Important: If you have difficulty getting the correct results on the ouput connectors/ports, you may need to modify some of the register settings to fit your particular installation. Refer to Chapter 2: Setting the Engineering Registers on page 15 for more information.

Decoding Only

Like encoding, a signal that comes in on I/P 1 goes out on the odd-numbered outputs, and a signal that comes in on I/P 2 goes out on the even-numbered outputs. In decoding, however, the embedded text is captured and output to the Ethernet port and the RS-232 port. See Figure 1–7 below.

Figure 1–7 Typical Scenario for Decoding Only



Example: This simple example demonstrates the decoding feature.

I/P 1: Input encoded video source with encoded closed captions.

I/P 2: N/A

O/P 1: Connect output for encoded video stream (I/P 1).

O/P 2: N/A

O/P 3: Monitor the burned-in caption text from I/P 1.

O/P 4: N/A

Serial: Receive closed caption text from I/P 1.

Ethernet: N/A

1. Connect a video source with encoded closed captions to I/P 1.
2. Connect the output cable to O/P 1.
3. Connect a monitor to O/P 3.
4. Connect the PC to the serial or Ethernet port in the card.

Note: To use the Ethernet port as a virtual serial port, refer to : Appendix A of the *Installation Guide* (PN 821135) to download, install, and configure the Ethernet connectivity application.

5. For our example, set SW 1 to 8. Table 1–4 below also lists one additional option.

Table 1–4 Switch 1 Settings - Decoding Only

SW 1	Capturing Text From
8	Decodes caption data from **I/P 1** and outputs the text to both the serial and the Ethernet ports.
9	Decodes caption data from **I/P 2** and outputs the text to both the serial and the Ethernet ports.

6. Set SW 2 to 0.

Providing the HDCC card is locked on an video signal, output from the serial and Ethernet ports will provide closed caption data decoded from the video input.

Important: If you have difficulty getting the correct results on the ouput connectors/ports, you may need to modify some of the register settings to fit your particular installation. Refer to Chapter 2: Setting the Engineering Registers on page 15 for more information.

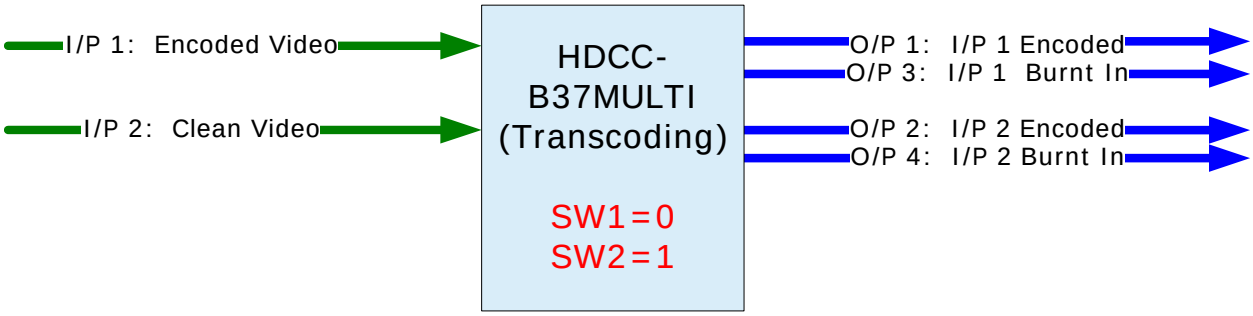
Bridging

Bridging means that encoded closed captions are copied from one video stream to the other, *transcoding* them on-the-fly if the two video streams are of a different format.

Note: It is not necessary to match the resolutions and/or refresh rates when using multiple inputs, with one exception: the target video frame rate must match or be faster than the source video stream.

For example, you can bridge and transcode from a SD-50Hz to a HD-60Hz signal but not the other way around.

Figure 1–8 I/O Functionality - Bridging Only



1. Connect the encoded video signal to I/P 1.
2. Connect the clean video signal to I/P 2.
3. Connect the output cables to O/P 1 and O/P 2.
4. Connect monitor cables to O/P 3 and/or O/P 4.
5. For our example, set SW 2 to 1. Set SW 1 to 0.

As soon as the HDCC card is locked on the two video signals, it should begin to bridge and if necessary transcode closed caption data from channel A (I/P 1) to channel B (O/P 2 and O/P 4).

Important: If you have difficulty getting the correct results on the ouput connectors/ports, you may need to modify some of the register settings to fit your particular installation. Refer to Chapter 2: Setting the Engineering Registers on page 15 for more information.

CHAPTER 2
Setting the Engineering Registers

Introduction

Overview

This chapter explains how to access the engineering registers to configure the HDCC card for your particular system.

Topics

Connecting to the Card

To perform this procedure you will need:

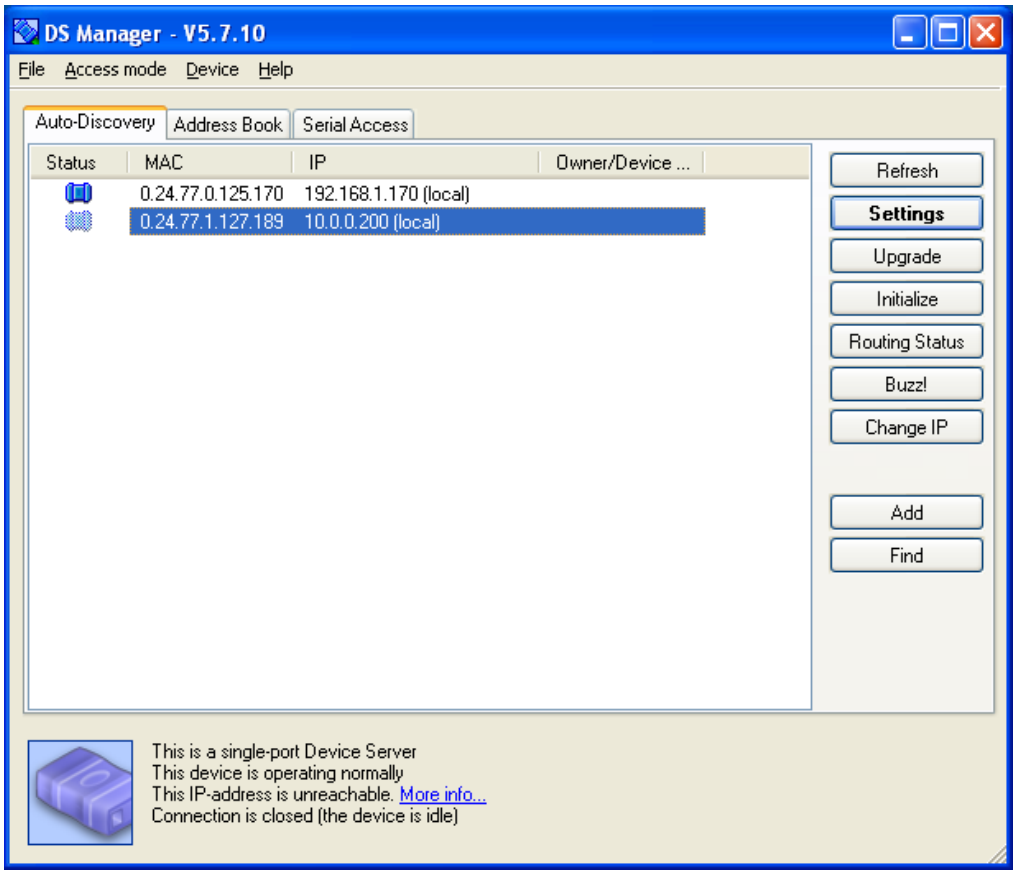
- A PC or laptop with a serial port and/or Ethernet port
- Either or both of the following:
  - A serial cable (connected from the serial port of the PC to the serial port on the HDCC). Refer to Accessing the Configuration on page 19 for more information.
  - An Ethernet cable (connected to the Ethernet ports of the HDCC card and the PC)

Important: You must obtain a valid IP address for your network from your network administrator. In the following example, we'll be using IP address 192.168.1.167 for our HDCC.

To change the IP address on the new HDCC and set-up a virtual serial port connection:

1. Refer to the section entitled "Downloading the Software" in Appendix A of the *Installation Guide* for instructions for downloading the Ethernet configuration application. Continue through the end of "Installing the Software."
2. Launch the DS Manager.
3. Click the **Auto-Discovery** tab if it is not already highlighted.
4. New HDCC cards (with Codan or Evertz backplanes) will have a default IP address of 10.0.0.200. Highlight the HDCC with IP 10.0.0.200.

Figure 2–1 DS Manager Configuration Screen



Note that the color under the **Status** column shows IP 10.0.0.200 disabled (i.e., this card is currently not accessible to the network). The card with IP 192.168.1.170 is an existing HDCC on the network.

5. Click the **Change IP** button.

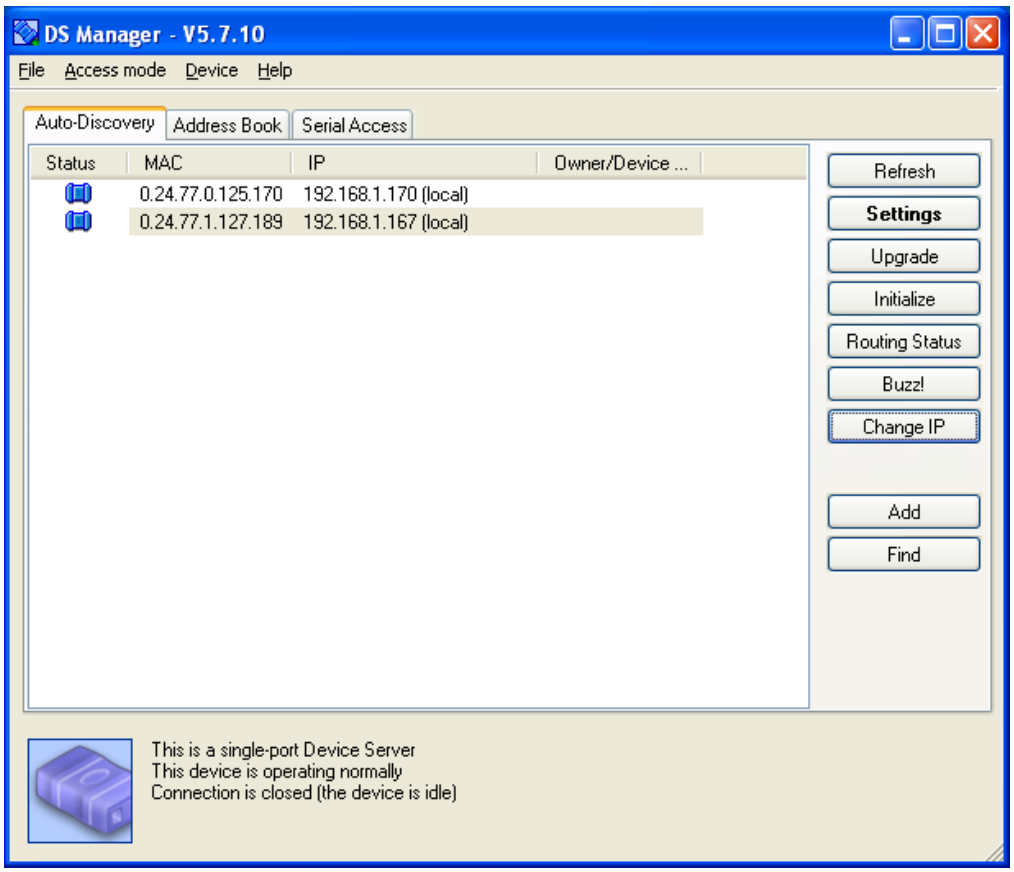
Figure 2–2 Default IP Address Screen

6. In our example we're changing the IP to 192.168.1.167. Type in the address your network administrator gave you.

Figure 2–3 New IP Address Screen

7. Click **OK**.

Figure 2–4 Enabled New IP Address



The new HDCC with IP 192.168.1.167 is now accessible through the network.

8. Close the DS Manager application.

Accessing the Configuration

The card has a number of registers to allow easy configuration of various card parameters that can be accessed from both the RS-232 interface and the Ethernet interface.

Note: An active SDI (SD or HD) signal connected to one of the input interfaces (I/P-1 or I/P-2) is required to activate the Ethernet port.

To access the configuration registers:

1. Determine whether you want to access the engineering menu from the Ethernet port or from the RS-232 port.
2. Depending how you will access the menu, turn SW 1 to either E or F according to Table 2–1 below.

Table 2–1 Switch 1 Settings - Encoding

SW 1	Insert Text From
E	Access engineering menu through RS232 port.
F	Access engineering menu through Ethernet port.

3. Connect the serial or Ethernet cable from the host computer's port to the port on the rear panel adaptor.

   Note: IRT adaptors do not provide an Ethernet port.

4. Launch your **HDCCRegEdit** program. Refer to the *HDCCRegEdit Guide* for instructions for using this application.
5. Verify that your communication settings are 38400, n, 8, 1.

Register Reference

WARNING! If a register is not listed in Table 2–2 below, do not modify it. Do so will cause unpredictable results.

Table 2–2 Register Table Summary

Channel		Description
A	B	
00h	20h	HD Line for CEA-708 Insertion, Extraction, and Monitoring
01h	21h	HD Line for ARIB B37 Insertion and Extraction
02h	22h	SD Line for CEA-608 Insertion, Extraction, and Monitoring
03f	23h	708 Features (1)
04h	24h	708 Features (2)
05h	25h	708 Language Code (1st Letter)
06h	26h	708 Language Code (2nd Letter)
07h	27h	708 Language Code (3rd Letter)
08h	28h	Transmission Features
09h	29h	Horizontal Timing Offset for GPI SD Insertion Line (Reserved)
0Ah	2Ah	SD Line for GPI Data Insertion and Decoding
0Bh	2Bh	HD Line for GPI Data Insertion and Decoding
0Ch	2Ch	Not Used
0Dh	2Dh	Not Used
0Eh		GPI LED Settings
0Fh	2Fh	GPI Rx/Tx Polarity Control
10h	30h	Special Features 2
11h	31h	Regen/Blank Control With External GPI
12h	32h	GPI Data Insertion Control and Encoder Timeout
13h	33h	GPI Mapping for Tx
14h	34h	GPI Mapping for Per-Field Blank Control (Reserved)
15h	35h	Not Used
16h	36h	Not Used
17h	37h	Not Used
18h	38h	Not Used
19h	39h	Not Used
1Ah	3Ah	Not Used
1Bh	3Bh	Not Used
1Ch	3Ch	Not Used
1Dh	3Dh	Decoded Channel on Monitor Output (CC1, CC2, etc.)
1Eh	3Eh	Not Used
1Fh	3Fh	Not Used
4Dh		Timing Offset—Do Not Change
4Eh		GPO 1 and 2 Mapping
4Fh		GPO 3 and 4 Mapping

Table 2–3 Register Settings with Descriptions, Domains, and Defaults

<table>
<tr><th>CHA Reg #</th><th>CHB Reg #</th><th>Description</th><th>Default</th></tr>
<tr><td>00h</td><td>20h</td><td>HD Line for CEA-708 Insertion, Extraction, and Monitoring
Min: 07 – Max: 19</td><td>09h</td></tr>
<tr><td>01h</td><td>21h</td><td>HD Line for ARIB B37 Insertion and Extraction
Min: 07 – Max: 19</td><td>13h</td></tr>
<tr><td>02h</td><td>22h</td><td>SD Line for CEA-608 Insertion, Extraction, and Monitoring
Min: 06 – Max: 16</td><td>15h</td></tr>
<tr><td>03h</td><td>23h</td><td>708 Services Features (1)
Min: N/A – Max: N/A
<table>
<tr><th>Bit(s)</th><th>Function</th></tr>
<tr><td>7</td><td>Aspect Ratio Flag
0 = 4:3
1 = 16:9</td></tr>
<tr><td>6</td><td>Easy Reader Flag
0 = Easy reader flag not set
1 = Easy reader flag set</td></tr>
</table></td><td>00h</td></tr>
<tr><td>04h</td><td>24h</td><td>708 Service Features (2)
Min: N/A – Max: N/A
<table>
<tr><th>Bit(s)</th><th>Function</th></tr>
<tr><td>7</td><td>Digital/Analog Flag
0 = Analog
1 = Digital</td></tr>
<tr><td>6:0</td><td>Service Number (if Bit 7=1):
000 0001
.
.
.
111 1111</td></tr>
<tr><td>0</td><td>Field Number (if Bit 7=0):
0 = Field 1
1 = Field 2</td></tr>
</table></td><td>81h</td></tr>
</table>

Table 2–3 Register Settings with Descriptions, Domains, and Defaults (Continued)

CHA Reg #	CHB Reg #	Description	Default
05h	25h	**708 Language Code (1st Letter)** *Min: "a" 61h – Max: "z" 7Ah*	"e" 65h
06h	26h	**708 Language Code (2nd Letter)** *Min: "a" 61h – Max: "z" 7Ah*	"n" 6Eh
07h	27h	**708 Language Code (3rd Letter)** *Min: "a" 61h – Max: "z" 7Ah*	"g" 67h

Table 2–3 Register Settings with Descriptions, Domains, and Defaults (Continued)

<table>
<tr><th>CHA Reg #</th><th>CHB Reg #</th><th>Description</th><th>Default</th></tr>
<tr><td>08h</td><td>28h</td><td>Transmission Features
Min: N/A – Max: N/A
<table>
<tr><th>Bit(s)</th><th>Function</th></tr>
<tr><td>7</td><td>Enable SMPTE 333 Protocol
(Not Yet Used)
0 = Disabled
1 = Enabled</td></tr>
<tr><td>6:5</td><td>Com Port Speed
00 = 9600 baud
01 = 19200 baud
10 = 38400 baud
11 = 115200 baud</td></tr>
<tr><td>4</td><td>Decoded Data Output Format
0 = Normal: Grand Alliance (^A)
1 = Raw Data</td></tr>
<tr><td>3</td><td>Not Used</td></tr>
<tr><td>2</td><td>Enable VANC 608/708 Insertion in SD
0 = Normal
1 = Insert</td></tr>
<tr><td>1</td><td>Disable “Smart XDS” Insertion Feature
0 = Normal: Smart XDS Insertion Feature Enabled
1 = Smart XDS Feature Disabled</td></tr>
<tr><td>0</td><td>Disable VANC 608/708 Insertion in HD Unless Receiving CDP Data
0 = Normal: Insertion Enabled
1 = Insertion Disabled</td></tr>
</table></td><td>00h</td></tr>
<tr><td>09h</td><td>29h</td><td>Reserved—Do Not Change</td><td>—</td></tr>
<tr><td>0Ah</td><td>2Ah</td><td>SD Line for GPI Data
Min: 06h – Max: 16h</td><td>0Dh</td></tr>
</table>

Table 2–3 Register Settings with Descriptions, Domains, and Defaults (Continued)

CHA Reg #	CHB Reg #	Description	Default
0Bh	2Bh	**HD Line for GPI Data** *Min: 07h – Max: 19h*	0Dh
0Ch	2Ch	Not Used	–
0Dh	2Dh	Not Used	–

Table 2–3 Register Settings with Descriptions, Domains, and Defaults (Continued)

<table>
<tr><th>CHA Reg #</th><th>CHB Reg #</th><th>Description</th><th>Default</th></tr>
<tr><td>0Eh</td><td></td><td>
GPI LED Settings
Min: N/A – Max: N/A
<table>
<tr><th>Bits</th><th>Function</th></tr>
<tr><td>0:3</td><td>
<table>
<tr><th colspan="2">GPI 4</th></tr>
<tr><th>LED</th><th>Description</th></tr>
<tr><td>0000</td><td>GPI Rx Ch A on LEDs 0 to 3
GPI Rx Ch B on LEDs 4 to 7</td></tr>
<tr><td>0001</td><td>GPI Rx Ch A OR GPI-In 0 to 3 on LEDs 0 to 3
GPI Rx Ch B OR GPI-In 0 to 3 on LEDs 4 to 7</td></tr>
<tr><td>0010</td><td>GPI Rx Ch A on LEDs 0 to 3
GPI Rx Ch B OR GPI-In 0 to 3 on LEDs 4 to 7</td></tr>
<tr><td>0011</td><td>GPI Rx Ch A on LEDs 0 to 3
GPI Rx Ch B OR GPI-In 0 to 3 on LEDs 4 to 7</td></tr>
<tr><td>0100</td><td>GPI Tx Ch A on LEDs 0 to 3
GPI Tx Ch B on LEDs 4 to 7</td></tr>
<tr><td>Else</td><td>Show status on LEDs 0 to 3. See tables below.</td></tr>
</table>
<table>
<tr><th>LED</th><th>LED 4 Description</th></tr>
<tr><td>0</td><td>Interleaved Mode</td></tr>
<tr><td>1</td><td>Progressive Mode</td></tr>
</table>
<table>
<tr><th rowspan="2">Description</th><th colspan="3">LED</th></tr>
<tr><th>1</th><th>2</th><th>3</th></tr>
<tr><td>576 (PAL)</td><td>0</td><td>0</td><td>1</td></tr>
<tr><td>480 (NTSC)</td><td>0</td><td>1</td><td>0</td></tr>
<tr><td>720</td><td>0</td><td>1</td><td>1</td></tr>
<tr><td>1035</td><td>1</td><td>0</td><td>0</td></tr>
<tr><td>Not Used</td><td>1</td><td>0</td><td>1</td></tr>
<tr><td>1080</td><td>1</td><td>1</td><td>0</td></tr>
</table>
</td></tr>
<tr><td>4:7</td><td>Not Used</td></tr>
</table>
</td><td>00</td></tr>
</table>

Table 2–3 Register Settings with Descriptions, Domains, and Defaults (Continued)

<table>
<tr><th>CHA Reg #</th><th>CHB Reg #</th><th>Description</th><th>Default</th></tr>
<tr><td>0Fh</td><td>2Fh</td><td>GPI Tx/Rx Polarity
Min: N/A – Max: N/A

<table>
<tr><th>Bit(s)</th><th>Type</th><th>Function</th></tr>
<tr><td>0</td><td>GPI Rx 1</td><td rowspan="8">0 = Normal (Active Low)
1 = Inverted (Active High)</td></tr>
<tr><td>1</td><td>GPI Rx 2</td></tr>
<tr><td>2</td><td>GPI Rx 3</td></tr>
<tr><td>3</td><td>GPI Rx 4</td></tr>
<tr><td>4</td><td>GPI Tx 1</td></tr>
<tr><td>5</td><td>GPI Tx 2</td></tr>
<tr><td>6</td><td>GPI Tx 3</td></tr>
<tr><td>7</td><td>GPI Tx 4</td></tr>
</table></td><td>00h</td></tr>
<tr><td>10h</td><td>30h</td><td>Special Features—2
Min: N/A – Max: N/A

This register controls miscellaneous functions on the card including the video outputs.

<table>
<tr><th>Bit(s)</th><th>Function</th></tr>
<tr><td>3:4[a]</td><td>Video Output Mode:
00 = Normal
01 = Both outputs set to monitoring
10 = Both outputs are clean
11 = Not Used</td></tr>
<tr><td>5</td><td>GPI Tx Source:
0 = GPI mapped input (See Registers 13h and 33h.)
1 = GPI mapped input ORed with GPI Rx</td></tr>
</table>
a Normal (default) = Monitoring O/Ps are 3 and 4; Clean O/Ps are 1 and 2
Monitoring = Burned-in captions on all O/Ps
Clean = No burned-in captions on any O/Ps</td><td>00h</td></tr>
</table>

Table 2–3 Register Settings with Descriptions, Domains, and Defaults (Continued)

<table>
<tr><th>CHA Reg #</th><th>CHB Reg #</th><th>Description</th><th>Default</th></tr>
<tr><td>11h</td><td>31h</td><td>
Regen/Blank Control with External GPI
Min: N/A – Max: N/A
<table>
<tr><th>Bit(s)</th><th>Value</th><th>Blank Controlled by...</th></tr>
<tr><td rowspan="5">0:3</td><td>0000</td><td>GPI Control (Regen Operation)</td></tr>
<tr><td>0001</td><td>External GPI 1</td></tr>
<tr><td>0010</td><td>External GPI 2</td></tr>
<tr><td>0011</td><td>External GPI 3</td></tr>
<tr><td>0100</td><td>External GPI 4</td></tr>
</table>
<table>
<tr><th>Bit(s)</th><th>Polarity Control</th><th>Function</th></tr>
<tr><td>4</td><td>GPI 1</td><td rowspan="4">0 = Normal (Active Low)
1 = Inverted (Active High)</td></tr>
<tr><td>5</td><td>GPI 2</td></tr>
<tr><td>6</td><td>GPI 3</td></tr>
<tr><td>7</td><td>GPI 4</td></tr>
</table>
</td><td>00h</td></tr>
</table>

Table 2–3 Register Settings with Descriptions, Domains, and Defaults (Continued)

<table>
<tr><th>CHA Reg #</th><th>CHB Reg #</th><th>Description</th><th>Default</th></tr>
<tr><td>12h</td><td>32h</td><td>GPI Insertion Control and Insert Mode Timeout
Min: N/A – Max: N/A
<table>
<tr><th>Bit(s)</th><th>Function</th></tr>
<tr><td>0:3</td><td>Insert Mode Time Out
(0.5 second intervals):
0000 = 0 seconds
0001 = 0.5 seconds
0010 = 1.0 seconds
.
.
.
1111 = 7.5 seconds</td></tr>
<tr><td>4:5</td><td>Time Out
00 = Normal (Automatic Fall Back to Regen Mode After Timeout)
01 = Permanent Insert Mode in Field 0
10 = Permanent Insert Mode in Field 1
11 = Permanent Insert Mode in Both Fields</td></tr>
<tr><td>6</td><td>Omit 708 Packets on Decoder Output
(Send B37 Packets Only)
0 = Disabled
1 = Enabled</td></tr>
<tr><td>7</td><td>GPI Data Insertion
0 = Off
1 = On</td></tr>
</table></td><td>12h</td></tr>
</table>

Table 2–3 Register Settings with Descriptions, Domains, and Defaults (Continued)

<table>
<tr><th>CHA Reg #</th><th>CHB Reg #</th><th>Description</th><th>Default</th></tr>
<tr><td>13h</td><td>33h</td><td>GPI Mapping for Tx
Min: N/A – Max: N/A
<table><tr><th>Bit(s)</th><th>GPI Tx Source</th><th>Function</th></tr><tr><td>0:1</td><td>0</td><td rowspan="4">External GPIs:
00 = GPI 1
01 = GPI 2
10 = GPI 3
11 = GPI 4</td></tr><tr><td>2:3</td><td>1</td></tr><tr><td>4:5</td><td>2</td></tr><tr><td>6:7</td><td>3</td></tr></table></td><td>E4h</td></tr>
<tr><td>14h</td><td>34h</td><td>Not Used</td><td>–</td></tr>
<tr><td>15h</td><td>35h</td><td>Not Used</td><td>–</td></tr>
<tr><td>16h</td><td>36h</td><td>Not Used</td><td>–</td></tr>
<tr><td>17h</td><td>37h</td><td>Not Used</td><td>–</td></tr>
<tr><td>18h</td><td>38h</td><td>Not Used</td><td>–</td></tr>
<tr><td>19h</td><td>39h</td><td>Not Used</td><td>–</td></tr>
<tr><td>1Ah</td><td>3Ah</td><td>Not Used</td><td>–</td></tr>
<tr><td>1Bh</td><td>3Bh</td><td>Not Used</td><td>–</td></tr>
<tr><td>1Ch</td><td>3Ch</td><td>Not Used</td><td>–</td></tr>
<tr><td>1Dh</td><td>3Dh</td><td>Decoded Channel on Monitor Output
Min: 00h – Max: 03h
0 = CC1
1 = CC2
2 = CC3
3 = CC4</td><td>00h</td></tr>
<tr><td>1Eh</td><td>3Eh</td><td>Not Used</td><td>–</td></tr>
<tr><td>1Fh</td><td>3Fh</td><td>Not Used</td><td>–</td></tr>
<tr><td colspan="2">4Dh</td><td>Timing Offset – Do Not Change
Min: N/A – Max: N/A</td><td>06h</td></tr>
</table>

Table 2–3 Register Settings with Descriptions, Domains, and Defaults (Continued)

<table>
<tr><th>CHA Reg #</th><th>CHB Reg #</th><th>Description</th><th>Default</th></tr>
<tr><td colspan="2">4Eh</td><td>GPO 0 and 1 Source
Min: N/A – Max: N/A

<table>
<tr><th>Bit(s)</th><th>Function</th></tr>
<tr><td>0:3</td><td>GPO 0 Source
0000 = Ch 1 GPI Rx 1
0001 = Ch 1 GPI Rx 2
0010 = Ch 1 GPI Rx 3
0011 = Ch 1 GPI Rx 4
0100 = Ch 2 GPI Rx 1
0101 = Ch 2 GPI Rx 2
0110 = Ch 2 GPI Rx 3
0111 = Ch 2 GPI Rx 4</td></tr>
<tr><td>4:7</td><td>GPO 1 Source
0000 = Ch 1 GPI Rx 1
0001 = Ch 1 GPI Rx 2
0010 = Ch 1 GPI Rx 3
0011 = Ch 1 GPI Rx 4
0100 = Ch 2 GPI Rx 1
0101 = Ch 2 GPI Rx 2
0110 = Ch 2 GPI Rx 3
0111 = Ch 2 GPI Rx 4</td></tr>
</table></td><td>10h</td></tr>
</table>

Table 2–3 Register Settings with Descriptions, Domains, and Defaults (Continued)

<table>
<tr><th>CHA Reg #</th><th>CHB Reg #</th><th>Description</th><th>Default</th></tr>
<tr><td>4Fh</td><td></td><td>GPO 2 and 3 Source
Min: N/A – Max: N/A
<table>
<tr><th>Bit(s)</th><th>Function</th></tr>
<tr><td>0:3</td><td>GPO 2 Source
0000 = Ch 1 GPI Rx 1
0001 = Ch 1 GPI Rx 2
0010 = Ch 1 GPI Rx 3
0011 = Ch 1 GPI Rx 4
0100 = Ch 2 GPI Rx 1
0101 = Ch 2 GPI Rx 2
0110 = Ch 2 GPI Rx 3
0111 = Ch 2 GPI Rx 4</td></tr>
<tr><td>4:7</td><td>GPO 3 Source
0000 = Ch 1 GPI Rx 1
0001 = Ch 1 GPI Rx 2
0010 = Ch 1 GPI Rx 3
0011 = Ch 1 GPI Rx 4
0100 = Ch 2 GPI Rx 1
0101 = Ch 2 GPI Rx 2
0110 = Ch 2 GPI Rx 3
0111 = Ch 2 GPI Rx 4</td></tr>
</table></td><td>00h</td></tr>
</table>

CHAPTER 3
Using the General Purpose Inputs/Outputs

Introduction

Overview

This chapter describes the functionality of the GPI/O connectors on the adaptors.

Topics

Functionality

The GPI interface allows the user to encode contact closure triggers into a HD or SD video stream frame accurately. The encoded data uses a proprietary algorithm designed to withstand severe degradation and prevent false triggering or releasing of GPIs. Error checking information is embedded within the GPI data stream which accomplishes this task and is far more advanced than simple CRC.

Typical triggers that users can encode from automation include (but are not limited to:

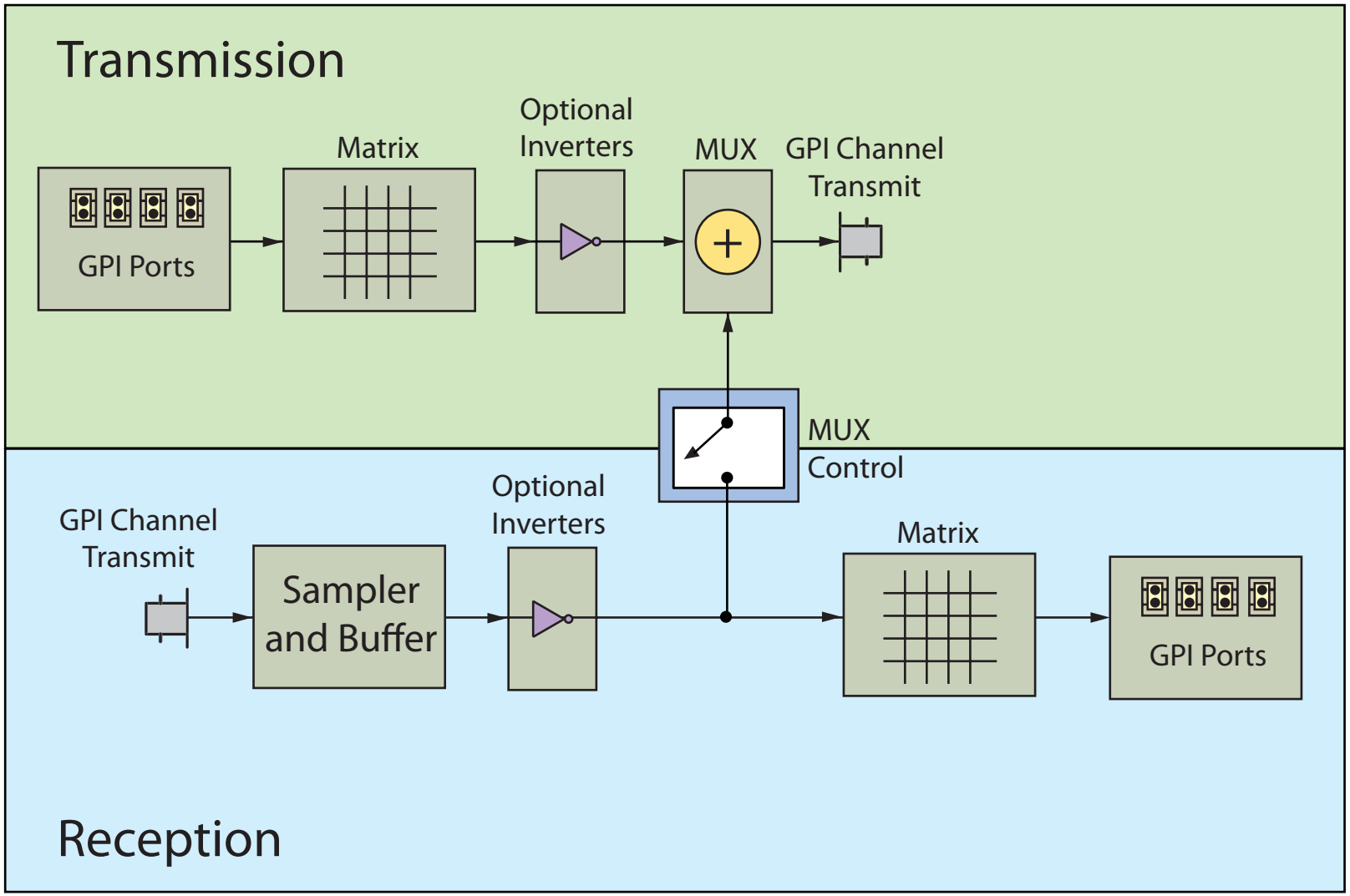
- ARC switching
- Commerical cue triggers (more accurate than the antiquated cue tone system)
- Machine control
- Regional commercial insertion cards

Typically the GPI data is encoded on Line 13 for both the HD-SDI signals and the SD-SDI signals. However, the HDCC is flexible enough to allow you to assign the lines on which you want the GPI data. See Register Reference on page 19 for details.

You can also use the GPI interface to control other operational aspects of the card.

Figure 3–1 on page 35 illustrates the GPI/O signal flow through the HDCC. For our purposes, a GPI is an input signal to the HDCC card supplied by the user through the physical GPI/O port that can activate certain modes in the HDCC card, and/or can be encoded onto the outgoing SDI video stream to notify downstream equipment of some condition, event, or command. A GPO is a signal the HDCC card receives on the incoming SDI video stream that is output to the physical GPI/O port to signify some condition, event, or command generated by upstream equipment.

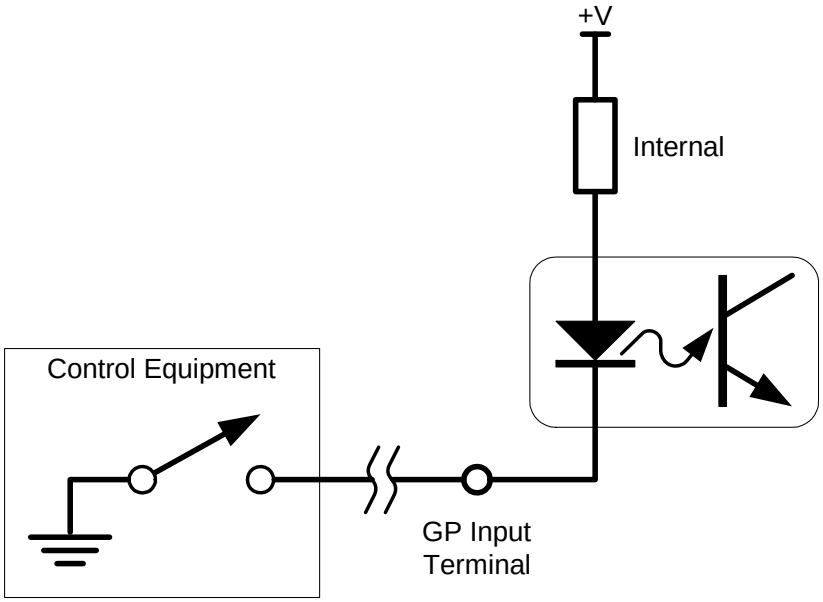
Figure 3–1 GPI/O Functional Diagram



GPIs

The GP inputs are designed to be asserted by switching the closures to ground. Asserting a GP input will result in that input state being encoded on the appropriate line of the video signal and/or activate the required function.

Figure 3–2 Input Diagram



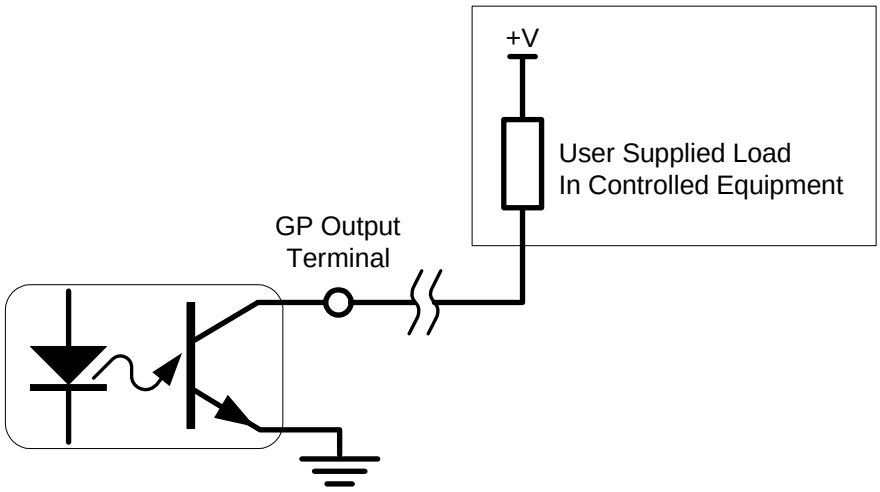
GPOs

If the incoming SDI data stream contains GP data on the appropriate line of the video signal, the corresponding GP output on the card will be activated. The GP output is asserted when its voltage is high.

Important: The GP outputs are optically isolated and "open collector" so the user must provide the appropriate pull-up resistor for each GP output. See Figure 3–3 below.

The HDCC provides four GPOs. (See Connector Pin Assignments on page 47 in Chapter 4 for connectivity.)

Figure 3–3 Output Diagram



GPI/O Polarities

You can control the polarities of the encoded GPIs, both at the receiving stage (Rx) and at the encoding stage (Tx).

GPI Encoded Polarity (Rx)

Channel	Register	Bit(s)	Values
A	0Fh	0 through 3	See Table 3–1 below.
B	2Fh	0 through 3	

The polarity settings are listed in Table 3–1 below.

Table 3–1 GPI Assertion Polarity

Bits	GPI	Active Low (default)	Active High
0	GPI-1	0	1
1	GPI-2	0	1
2	GPI-3	0	1
3	GPI-4	0	1

GPI Encoded Polarity (Tx)

Channel	Register	Bit(s)	Values
A	0Fh	4 through 7	See Table 3–2 below.
B	2Fh	4 through 7	

You can set the polarity of the GPI as encoded on the data stream as shown in Table 3–2 below.

Table 3–2 Register 0Fh and 2Fh: Bits 4 through 7

Bits	GPI	Active Low (default)	Active High
4	GPI-1	0	1
5	GPI-2	0	1
6	GPI-3	0	1
7	GPI-4	0	1

Enabling/Disabling GPI Transmission

Channel	Register	Bit(s)	Values
A	12h	7	0=Disable 1=Enable
B	22h	7	

The HDCC can be configured to enable or disable transmission of GPI data by setting bit 7 of register 12h for Channel A, or 32h for Channel B.

CHAPTER 4
Features and Specifications

Introduction

Overview

This chapter explains the features of the HDCC closed caption card and details all of the specifications.

Topics

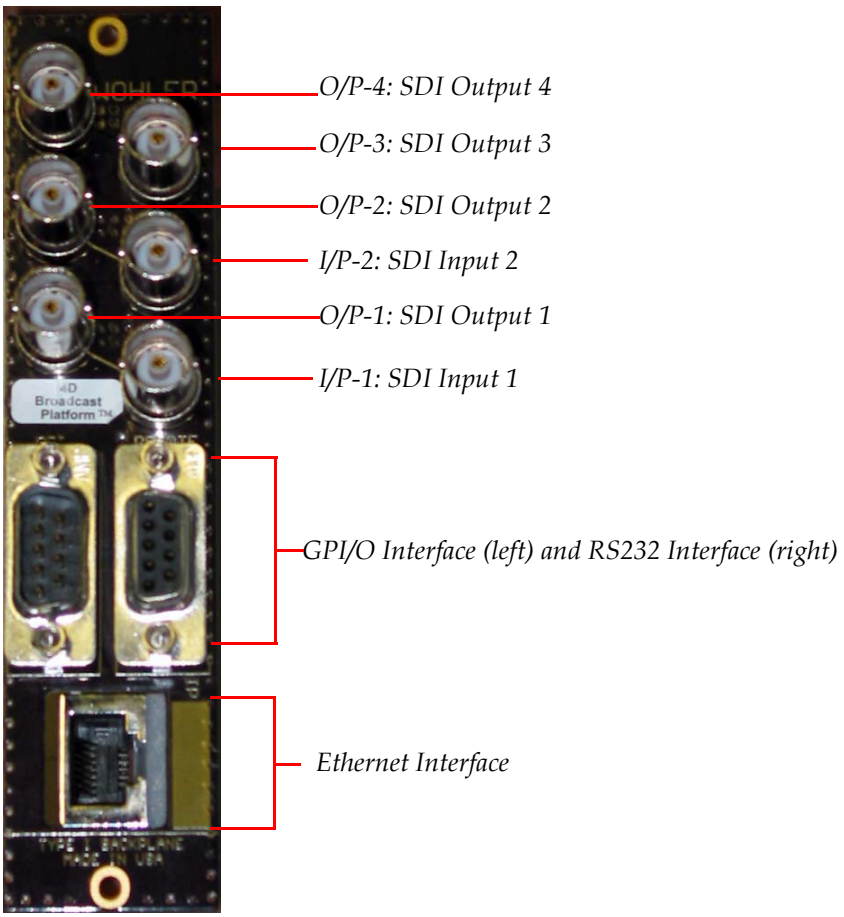
Features

Description

Hardware

The image and call outs in Figure 4–1 provide a reference for the detailed interface descriptions provided below.

Figure 4–1 Codan Adapter and Interface Layout



BNC Interfaces

- **I/P-1 and I/P-2:** These interfaces (Channel A and Channel B, respectively) accept SDI channel inputs that conform to the SMPTE 259M standard for SD and SMPTE 292M standard for HD.

- **O/P-1 and O/P-2:** These SDI outputs provide closed caption data encoded video signal. They are relay bypassed on power failure, card removal, or card failure.

- **O/P-3 and O/P 4:** These SDI outputs (Channel A and Channel B, respectively) provide closed caption data as well as an open caption display of the encoded data. The open caption display is fed from the final output stage of the card.

GPI Interface

Four GPI inputs and four GPI outputs are provided for products that use the GPI I/O functionality. All GPI I/Os are opto-coupled through the card.

Ethernet and Serial Interfaces

The ethernet interface (100BT) and the serial (RS-232) interfaces receive caption data into the card when used as closed caption inserters and to get caption data from the video signal when used as closed caption analyzers.

Software

The HDCC is a dual-channel card that delivers closed captioned data and performs a variety of other tasks related to closed captioning or GPI transmission. The card receives closed caption data through an RS-232 serial port and/or an ethernet port. The five functions of the card are:

- Closed Caption OSD Monitoring
- HD/SD Closed Caption Encoding/Inserting
- HD/SD Closed Caption Decoding/Analyzing
- HD/SD Closed Caption Bridging and Transcoding
- GPI Encoding and Decoding

Functionality

The HD/SD Closed-Caption Inserter (Encoding)

The HD/SD-SDI closed caption inserter can encode two independent SD-SDI sources with identical closed caption data, or two independent HD-SDI sources with unique data where the signal is intended to be used for different markets. The two signals do not need to be synchronous.

The HD/SD Closed Caption Decoder/Analyzer (Decoding)

The HD/SD-SDI closed caption decoder/analyzer can be used to decode two SD or HD sources; the video format and captions standard is automatically detected. The two inputs are not required to be synchronous nor of the same format.

The HD/SD Bridge and Transcoder (Bridging and Transcoding)

The SD/HD bridge copies encoded closed caption data from one video channel to the other, transcoding it if required (SD to HD or HD to SD).

Connectivity

Interfaces

A serial port (RS-232) and an ethernet interface (adaptor-dependent) are provided to insert closed caption data. Either interface can be used to control one or both channels of the card as required. Captioning workstations that use GrandAlliance, CDP, or NBR15610 protocols can be connected to the card through either interface to allow the encoding of closed captions. The Ethernet interface allows you to control the card from virtually any location within the facility as long as you have a network connection.

GrandAlliance, CDP, and NBR15610 Protocols

GrandAlliance and CDP Protocols are protocols used by a number of captions preparation workstations to create closed caption data. The closed caption encoder takes the GrandAlliance, CDP, or NBR15610 data input and converts it into the Closed Caption data. The inserter then inserts the encoded data into the video stream.

Data Insertion

Each channel of the card has a “clean” output that carries the encoded closed caption data and which is protected by a bypass relay, and an open-captioned output that provides the burnt-in display of encoded closed caption data, allowing direct monitoring with a standard SDI video monitor.

Automatic Regen / Insert Changeover

The closed caption inserter automatically switches back from insert to regen mode, depending on the closed caption data being delivered to the serial and/or Ethernet interfaces. This automatic changeover timeout is configurable, and can be forced to either mode, independantly for Field 1 or Field 2.

GPI Interfaces

Four optically isolated GPIs and four optically isolated GPOs are available to control some of the card's features, including inserting and extracting GPI/O data in the VBI or HD-VANC.

Supported Formats

The HDCC supports the following video formats:

- 480i60
- 576i50
- 720p (all field rates)
- 1035i (all field rates)
- 1080i (all field rates)

Advanced Operation

User Controls

Table 4–1 below lists all the values for SW 1.

Table 4–1 Switch 1 Communications Settings[a]

Setting	Function
0	Insert captions on Channel A from the serial port and on Channel B from the ethernet port. No decoding.
1	Insert captions on Channel A and Channel B from the serial port. No decoding.
2	Insert captions on both Channel A and Channel B from the ethernet port. No decoding.
3	Insert captions on Channel A from the serial port and on Channel B from the ethernet port. No decoding.
4	Insert captions on both Channel A and Channel B from the serial port. Decode video caption data from Channel A and send out the ethernet and serial port.
5	Insert captions on Channel A and Channel B from the ethernet port. Decode video caption data from each video Channel B and send out through both the ethernet and the serial ports.
6	Insert captions on Channel A and Channel B from the serial port. Decode video caption data from each video Channel B and send out through both the ethernet and the serial ports.
7	Insert captions on Channel A and Channel B from the ethernet port. Decode video caption data from each video Channel A and send out through both the ethernet and the serial ports.
8, B	Analyzer Mode Only - Decode caption data from Channel 1 out to the serial and ethernet ports.
9, A	Analyzer Mode Only - Decode caption data from Channel 2 out to the serial and ethernet ports.
C thru D	Not used
E	Access configuration through serial port
F	Access configuration through Ethernet port

a See also Figure 1–2 on page 4.

Table 4–2 below lists the valid settings for SW 2.

Table 4–2 Valid Switch Settings Switch 2 (SW2)

Switch 2	Bridge Operation
0	None
1	A→ B (both fields)
2	B→ A (both fields)
3	A→ B (Field 1)
4	B → A (Field 1)
5	A→ B (Field 2)
6	B → A (Field 2)
7 thru F	None

Status Indicators

Table 4–3 below describes the LED status indicators on the front of the HDCC card..

Table 4–3 Front Panel Status Indicators

LED #	Label	Color	Function
LED 1	Power	Green	Lights to indicate that the card is receiving power.
LED 2	SDI Channel A Input	Green	Confirms that a valid serial digital input is present. This LED will light only when the signal is present and locked. If the signal input fails or is not stable, the LED will flash at a 1 Hz rate.
LED 3	SDI Channel B Input	Green	Confirms that a valid serial digital input is present. This LED will light only when the signal is present and locked. If the signal input fails or is not stable, the LED will flash at a 1 Hz rate.
LED 4	GPI-1	Green	Refer to Register 0Eh—See Table 2-3 on page 25 for details.
LED 5	GPI-2		
LED 6	GPI-3		
LED 7	GPI-4		
LED 8	GPO-1	Red	
LED 9	GPO-2		
LED 10	GPO-3		
LED 11	GPO-4		

Specifications

Table 4–4 Physical Specifications

Specification	Value
Dimensions (H x W x D)	4″ x 8.7″x .5″ (101.60 mm x 220.98 mm x 127.00 mm)
Shipping Weight	1 lbs (.45 kg)
Space Requirements	3 RU
Supplied Accessories	Rear panel adaptor for user-specified frame
Power Requirements	Receives power from frame
Power Consumption	Approximately 10 W

Table 4–5 Technical Specifications

Specification	Value
Inputs	2 SD/HD-SDI autosensing on BNC 4 GPI (DB-9) (on 10-pin header on the Evertz rear panel adaptor)
Outputs	2 HD/SD-SDI Closed Captioned (BNC) 2 HD/SD-SDI Open Captioned (BNC) 4 GPO (DB-9) (on 10-pin header on the Evertz rear panel adaptor)
Inputs/Outputs	Ethernet (RJ-45) (not available on the IRT rear panel adaptor) 1 RS-232 (DB-9) (on Codan and IRT rear panel adaptors) 1 RS-232 (10-pin header on the Evertz rear panel adaptor)
Frame compatibility	• Codan • Evertz • IRT
Available functions	• Closed Caption OSD Monitoring (608 and 708 encapsulated 608) • HD/SD Closed Caption Encoding/Inserting • HD/SD Closed Caption Decoding/Analyzing • HD/SD Closed Caption Bridging and Transcoding • GPI Encoding and Decoding
Available communication protocols	• GrandAlliance • SMPTE333 • CDP • NBR15610

Table 4–5 Technical Specifications

Specification	Value
Supported closed caption specifications	CEA-608, CEA-708, (both 708 encapsulated 608 and native 708 for encoding and decoding) and ARIB-B37

Connector Pin Assignments

Table 4–6 GPI DB-9 Pin-Out Assignments (Codan and IRT Adaptors)

Pin	Label	Interface
1	Common GND	GND
2	GPO-4	Open Collector (Emitter to Ground)
3	GPO-3	
4	GPO-2	
5	GPO-1	
6	GPI-4	Active Low
7	GPI-3	
8	GPI-2	
9	GPI-1	

Figure 4–2 GPI DB-9 Male Pin-Out

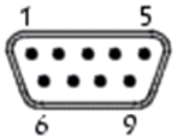


Table 4–7 RS-232 DB-9 Pin-Out Assignments (Codan and IRT Adaptors)

Pin	Label	Function	Data Flow Direction
1	N.C.	Not Connected	
2	RxD	RS-232 Data	Output
3	TxD	RS-232 Data	Input
4	Not Connected		
5	Common GND		
6	The IRT adaptor has the second serial port available on pins 6 and 7. See table 4-10 below		
7			
8			

Figure 4–3 RS-232 DB-9 Pin-Out

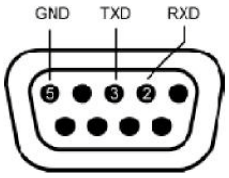


Table 4–8 14-Pin Header Assignments - RS-232 and GPI (Evertz Adaptor)

Pin	Label	Interface
1	GPO-1	Open Collector
2	GPI-1	Active Low
3	GPO-2	Open Collector
4	GPI-2	Active Low
5	GPO-3	Open Collector
6	GPI-3	Active Low
7	GPO-4	Open Collector
8	GPI-4	Active Low
9	RS-232 Tx	RS-232 Tx Data (to the card)
10	RS-232 Rx	RS-232 Rx Data (from the card)
11	Common GND	Reference Ground
12		
13		
14		

Figure 4–4 14-Pin Header Assignments

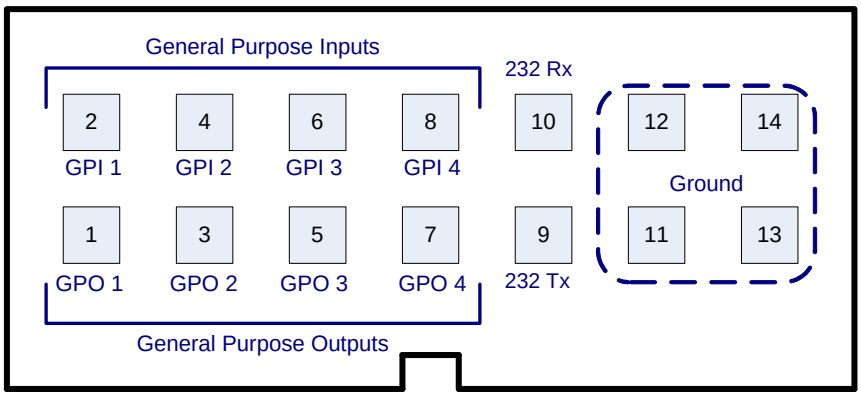


Table 4–9 DB9F Cable Connector Wiring (Codan and IRT Adaptors)

Pin	HDCC (DB9-M) to PC (DB9-F)
1	Do Not Connect.
2	Pin 2
3	Pin 3
4	Do Not Connect.
5	Pin 5
6	Do Not Connect.
7	
8	
9	

Note: Table 4–10 and Table 4–11 below provide the pin-out for the cable connecting the HDCC to the PC. A straight serial cable (not a null modem cable) will also work.

IMPORTANT: Pins 1, 4, 6, 7, 8, and 9 MUST NOT be connected.

Table 4–10 HDCC (IRT) to PC Interface RS-232 #1 Wiring

HDCC (IRT) DB-9M		Data Flow	PC DB-9F	
Pin	Description		Pin	Description
2	RxD	⇨	2	Rx D
3	TxD	⇦	3	Tx D
5	GND		5	GND
1, 4, 8, and 9: NC			1, 4, 6, 7, 8, and 9: NC	

Table 4–11 HDCC (IRT) to PC Interface RS-232 #2

HDCC (IRT) DB-9M		Data Flow	PC DB-9F	
Pin	Description		Pin	Description
7	RxD	⇨	2	Rx D
6	TxD	⇦	3	Tx D
5	GND		5	GND
1, 4, 8, and 9: NC			1, 4, 6, 7, 8, and 9: NC	

Technical Functional Overview

Figure 4–5 on page 50 illustrates the design of the HDCC.

Figure 4–5 HDCC Block Diagram