

PC-HELPER

High-Speed Up/Down
Counter Board for PCI Express
Low Profile

CNT-3204MT-LPE

User's Guide

CONTEC CO.,LTD.

Check Your Package

Thank you for purchasing the CONTEC product.

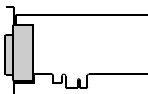
The product consists of the items listed below.

Check, with the following list, that your package is complete. If you discover damaged or missing items, contact your retailer.

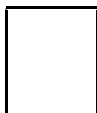
Product Configuration List

- Board [CNT-3204MT-LPE] ...1
- First step guide ...1
- CD-ROM *1 [API-PAC(W32)] ...1
- Standard-sized bracket ...1

*1 : The CD-ROM contains the driver software and User's Guide (this guide).



Board



First step guide



CD-ROM
[API-PAC(W32)]



Standard-sized
bracket

Copyright

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Table of Contents

Check Your Package	i
Copyright	ii
Trademarks	ii
Table of Contents	iii

1. BEFORE USING THE PRODUCT

1

About the Board	1
Features.....	1
Support Software.....	2
Cable & Connector (Option)	2
Accessories (Option).....	2
Customer Support.....	3
Web Site	3
Limited Three-Years Warranty.....	3
How to Obtain Service	3
Liability	3
Safety Precautions	4
Safety Information	4
Handling Precautions	5
Environment	6
Inspection	6
Storage.....	6
Disposal	6

2. SETUP

7

What is Setup?.....	7
Using the Board under Windows Using the Driver Library API-PAC(W32)	7
Using the Board under Windows Using Software other than the Driver Library API-PAC(W32).....	7
Using the board under an OS other than Windows	8
Step 1 Installing the Software.....	9
Starting the Install Program	9
Select the counter driver	10
Executing the Installation	11
Step 2 Setting the Hardware	12
Replacing the Bracket	12
Parts of the Board and Factory Defaults	13
Setting the Board ID	13
Plugging the Board.....	14

Step 3 Installing the Hardware.....	15
Turning on the PC	15
Setting with the Found New Hardware Wizard	15
Step 4 Initializing the Software.....	18
Invoking API-TOOL Configuration	18
Updating the Setup	18
Step 5 Checking Operations with the Diagnosis Program.....	19
What is the Diagnosis Program?.....	19
Check method 1: Checking the Board single-handedly (without external connection)	19
Check method 2: Checking the Board single-handedly (with external connection).....	20
Check method 3: Checking the Board using an external device	21
Using the Diagnosis Program.....	22
Setup Troubleshooting.....	28
Symptoms and Actions.....	28
If your problem cannot be resolved	28

3. EXTERNAL CONNECTION	29
------------------------------------	-----------

How to connect the connectors	29
Connector shape and optional cable connection	29
Connector Pin Assignment.....	31
How to Connect the Counter Input Signal.....	33
Example Connection for Counter Input Circuit	33
Example Connection with a Rotary Encoder	35
Example Connection with a Linear scale	35
Connecting the control signal input/output.....	36
Connection of a control input.....	36
Control input circuit and its sample connection	36
Connection of a control output.....	39
Control output circuit and its sample connection.....	39

4. FUNCTIONS	41
--------------------------	-----------

Types and Operations of Pulse Signals.....	41
Types of pulse signals	41
2-phase Input	41
Single-phase Input.....	42
Single-phase Input with Gate Control Attached.....	42
Multiplication of Count Input	43
Synchronous Clear.....	43
Asynchronous Clear	44
Phase-Z/CLR Input.....	44

Control of a counter	45
Counter start/Counter stop.....	45
Preset	46
Zero-clear	47
Register.....	47
Obtaining the count value	48
Obtaining the count value	48
Counter mode	48
Sampling mode.....	49
Totalizing/line receiver counter.....	50
Sampling function	51
Sampling function	51
Bus mastering	51
Interrupt (During bus mastering).....	51
Status, Count	52
Control of a sampling.....	53
Hardware event.....	54
Types of hardware events	54
Control input signal.....	55
Control output signal.....	56
Count match	57
Counter error	59
Sampling output signal	61
Status input	62
Pulse signal input states	62
Control input signal states	62
Error.....	62
Carry/Borrow	62
Count match	62
Other functions	63
Digital filter	63
Timer	63

5.	ABOUT SOFTWARE	65
-----------	-----------------------	-----------

CD-ROM Directory Structure	65
About Software for Windows	66
Accessing the Help File	67
Using Sample Programs.....	68
Uninstalling the API Function Libraries	70

6.	ABOUT HARDWARE	71
----	----------------	----

For detailed technical information	71
Hardware specification	71
Block Diagram	73

1. Before Using the Product

This chapter provides information you should know before using the product.

About the Board

This product is a PCI Express bus-compliant interface board for counting the pulses input from the external device.

The 32 bit up-and-down counter could have four channels and up to 10MHz maximum high speed pulse input. Moreover, it can be used to connect a rotary encoder and a linear scale, etc.

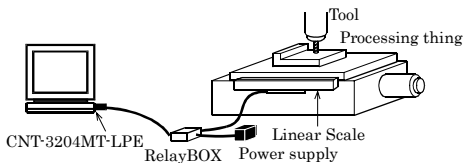
The pulse signal inputting interface is unisolated LVTTTL-level input that can input pulse signals at high speed. The application for this board can transfer data between the board and the PC at high speed using PCI bus mastering.

This product supports a Low Profile size slot and, if replaced with the supplied bracket, supports a standard size slot, too.

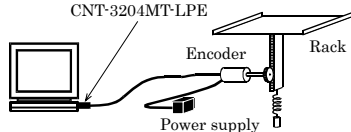
Using the bundled API function library package [API-PAC(W32)], you can create Windows application software for this board in your favorite programming language supporting Win32 API functions, such as Visual Basic or Visual C++.

< Example >

- Detecting a position of the table of a machine tool



- Detecting a change in weight



Features

- The 32 bit up-and-down counter could have four channels and input the pulse signal at 10MHz maximum(non-insulation LVTTTL level). (The minimum distinguishable phase of a two-phase input is 25nsec.)
- A two-phase signal such as a rotary encoder or a linear scale and a single-phase signal can be counted.
- One control signal input pin per channel (counter start/stop/preset)
- Supporting bus mastering, can count values sampling at a maximum sampling rate of 20 MHz (when using the internal clock)
- Can generate an interrupt, issuing an external signal, or presetting/zero-clearing the count value when it matches an arbitrary predefined value.
- Can be converted to a differential input interface using the differential unit (CTP-4D) and connection cable (CNT-68M/50M) which are sold separately
- Support for both of low-profile and standard size slots (interchangeable with a bundled bracket).

- Equipped with the same function as the PCI board CNT32-4MT(LPCI) and CardBus CNT32-4MT(CB).
- The connector shape and the signal assignment for the connector is compatible with the CNT32-4MT(LPCI) and CNT32-4MT(CB).

Support Software

You should use CONTEC support software according to your purpose and development environment.

Driver Library **API-PAC(W32)** (Bundled)

API-PAC(W32) is the library software that provides the commands for CONTEC hardware products in the form of Windows standard Win32 API functions (DLL). It makes it easy to create high-speed application software taking advantage of the CONTEC hardware using various programming languages that support Win32 API functions, such as Visual Basic and Visual C++.

It can also be used by the installed diagnosis program to check hardware operations.

CONTEC provides download services (at <http://www.contec.com/apipac/>) to supply the updated drivers and differential files.

For details, read Help on the bundled CD-ROM or visit the CONTEC's Web site.

< Operating environment >

OS	Windows XP, Server 2003, 2000
Adaptation language	Visual C++ .NET, Visual C# .NET, Visual Basic .NET, Visual C++, Visual Basic, Delphi, C++Builder, etc..

Cable & Connector (Option)

Shielded cable for CardBus counter input card

: CNT-68M/50M (0.5m)

Cable with 68-Pin D-sub Connector at either Ends (Mold Type)

: PCB68PS-0.5P (0.5m)

: PCB68PS-1.5P (1.5m)

Shielded cable with single connector for 68-pin 0.8mm pitch connector

: PCA68PS-0.5P (0.5m)

: PCA68PS-1.5P (1.5m)

Accessories (Option)

Termination Panel with Differential Receivers for Counter Input : CTP-4D *1

Screw Terminal (M3 x 50P) : EPD-50A *1

Screw Terminal (M3 x 68) : EPD-68A *2

*1 CNT-68M/50M optional cable is required separately.

*2 PCB68PS-0.5P or PCB68PS-1.5P optional cable is required separately.

* Check the CONTEC's Web site for more information on these options.

Customer Support

CONTEC provides the following support services for you to use CONTEC products more efficiently and comfortably.

Web Site

Japanese	http://www.contec.co.jp/
English	http://www.contec.com/
Chinese	http://www.contec.com.cn/

Latest product information

CONTEC provides up-to-date information on products.

CONTEC also provides product manuals and various technical documents in the PDF.

Free download

You can download updated driver software and differential files as well as sample programs available in several languages.

Note! For product information

Contact your retailer if you have any technical question about a CONTEC product or need its price, delivery time, or estimate information.

Limited Three-Years Warranty

CONTEC products are warranted by CONTEC CO., LTD. to be free from defects in material and workmanship for up to three years from the date of purchase by the original purchaser.

Repair will be free of charge only when this device is returned freight prepaid with a copy of the original invoice and a Return Merchandise Authorization to the distributor or the CONTEC group office, from which it was purchased.

This warranty is not applicable for scratches or normal wear, but only for the electronic circuitry and original boards. The warranty is not applicable if the device has been tampered with or damaged through abuse, mistreatment, neglect, or unreasonable use, or if the original invoice is not included, in which case repairs will be considered beyond the warranty policy.

How to Obtain Service

For replacement or repair, return the device freight prepaid, with a copy of the original invoice. Please obtain a Return Merchandise Authorization Number (RMA) from the CONTEC group office where you purchased before returning any product.

* No product will be accepted by CONTEC group without the RMA number.

Liability




The obligation of the warrantor is solely to repair or replace the product. In no event will the warrantor be liable for any incidental or consequential damages due to such defect or consequences that arise from inexperienced usage, misuse, or malfunction of this device.

Safety Precautions

Understand the following definitions and precautions to use the product safely.

Safety Information

This document provides safety information using the following symbols to prevent accidents resulting in injury or death and the destruction of equipment and resources. Understand the meanings of these labels to operate the equipment safely.

 DANGER	DANGER indicates an imminently hazardous situation which, if not avoided, will result in death or serious injury.
 WARNING	WARNING indicates a potentially hazardous situation which, if not avoided, could result in death or serious injury.
 CAUTION	CAUTION indicates a potentially hazardous situation which, if not avoided, may result in minor or moderate injury or in property damage.

Handling Precautions

DANGER

Do not use the product where it is exposed to flammable or corrosive gas. Doing so may result in an explosion, fire, electric shock, or failure.

CAUTION

- There are switches and jumpers on the board that need to be set in advance. Be sure to check these before installing the board.
 - Only set the switches and jumpers on the board to the specified settings. Otherwise, the board may malfunction, overheat, or cause a failure.
 - Do not strike or bend the board. Doing so could damage the board. Otherwise, the board may malfunction, overheat, cause a failure or breakage.
 - Do not touch the board's metal plated terminals (edge connector) with your hands. Otherwise, the board may malfunction, overheat, or cause a failure. If the terminals are touched by someone's hands, clean the terminals with industrial alcohol.
 - Do not install or remove the board to or from the extension slot while the computer's power is turned on. And also do not connect the board and external device while the power is turned on. Otherwise, the board may malfunction, overheat, or cause a failure. Be sure that the personal computer or the I/O extension unit power is turned off.
 - Make sure that your PC or extension unit can supply ample power to all the boards installed. Insufficiently energized boards could malfunction, overheat, or cause a failure.
 - The specifications of this product are subject to change without notice for enhancement and quality improvement. Even when using the product continuously, be sure to read the manual and understand the contents.
 - Do not modify the product. CONTEC will bear no responsibility for any problems, etc., resulting from modifying this product.
 - Regardless of the foregoing statements, CONTEC is not liable for any damages whatsoever (including damages for loss of business profits) arising out of the use or inability to use this CONTEC product or the information contained herein.
-

Environment

Use this product in the following environment. If used in an unauthorized environment, the board may overheat, malfunction, or cause a failure.

Operating temperature

0 - 50°C

Operating humidity

10 - 90%RH (No condensation)

Corrosive gases

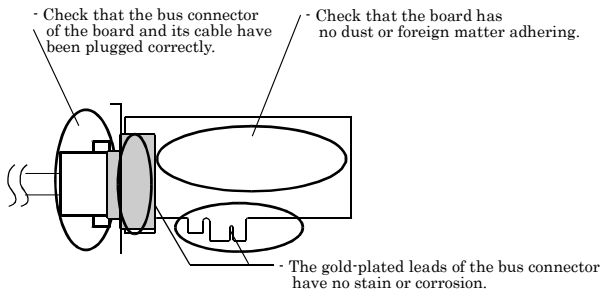
None

Floating dust particles

Not to be excessive

Inspection

Inspect the product periodically as follows to use it safely.



Storage

When storing this product, keep it in its original packing form.

- (1) Put the board in the storage bag.
- (2) Wrap it in the packing material, and then put it in the box.
- (3) Store the package at room temperature at a place free from direct sunlight, moisture, shock, vibration, magnetism, and static electricity.

Disposal

When disposing of the product, follow the disposal procedures stipulated under the relevant laws and municipal ordinances.

2. Setup

This chapter explains how to set up the board.

What is Setup?

Setup means a series of steps to take before the product can be used.

Different steps are required for software and hardware.

The setup procedure varies with the OS and applications used.

Using the Board under Windows

Using the Driver Library API-PAC(W32)

This section describes the setup procedure to be performed before you can start developing application programs for the board using the bundled CD-ROM “Driver Library API-PAC(W32)”.

Taking the following steps sets up the software and hardware. You can use the diagnosis program later to check whether the software and hardware function normally.

Step 1 Installing the Software

Step 2 Setting the Hardware

Step 3 Installing the Hardware

Step 4 Initializing the Software

Step 5 Checking Operations with the Diagnosis Program

If Setup fails to be performed normally, see the “Setup Troubleshooting” section at the end of this chapter.

Using the Board under Windows

Using Software other than the Driver Library API-PAC(W32)

For setting up software other than API-PAC(W32), refer to the manual for that software. See also the following parts of this manual as required.

This chapter Step 2 Setting the Hardware

This chapter Step 3 Installing the Hardware

Chapter 3 External Connection

Chapter 6 About Hardware

Using the board under an OS other than Windows

For using the board under Linux, see the following parts of this manual.

This chapter Step 2 Setting the Hardware

Chapter 3 External Connection

Chapter 6 About Hardware

Step 1 Installing the Software

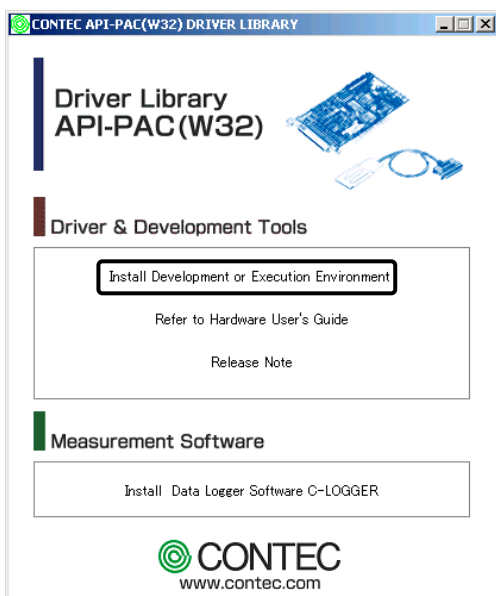
This section describes how to install the Driver libraries.

Before installing the hardware on your PC, install the Driver libraries from the bundled API-PAC(W32) CD-ROM.

The following description assumes the operating system as Windows XP. Although some user interfaces are different depending on the OS used, the basic procedure is the same.

Starting the Install Program

- (1) Load the CD-ROM [API-PAC(W32)] on your PC.
- (2) The API-PAC(W32) Installer window appears automatically.
If the panel does not appear, run (CD-ROM drive letter):\AUTORUN.exe.
- (3) Click on the [Install Development or Execution Environment] button.

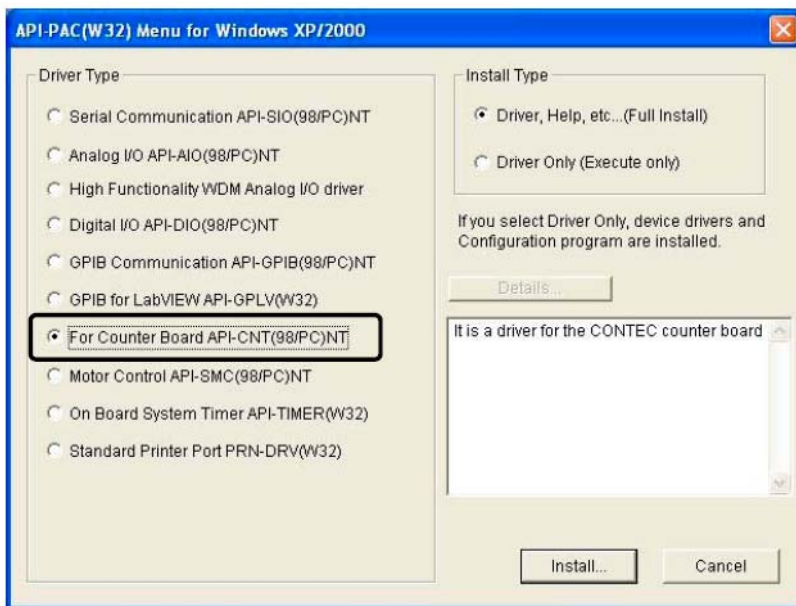


CAUTION

Before installing the software in Windows XP, 2000, log in as a user with administrator privileges.

Select the counter driver

- (1) The following dialog box appears to select “Driver Type” and “Install Type”.
- (2) Select “For Counter Board API-CNT(98/PC)NT”.
- (3) Select “Driver, Help, etc... (Full install)”.
- (4) Click on the [Install] button.



Executing the Installation

- (1) **Follow the on-screen instructions to proceed to install.**
- (2) When the required files have been copied, the “Perform a hardware setup now (API-TOOL Configuration)” and “Show readme file” check boxes are displayed.

When you are installing the software or hardware for the first time:

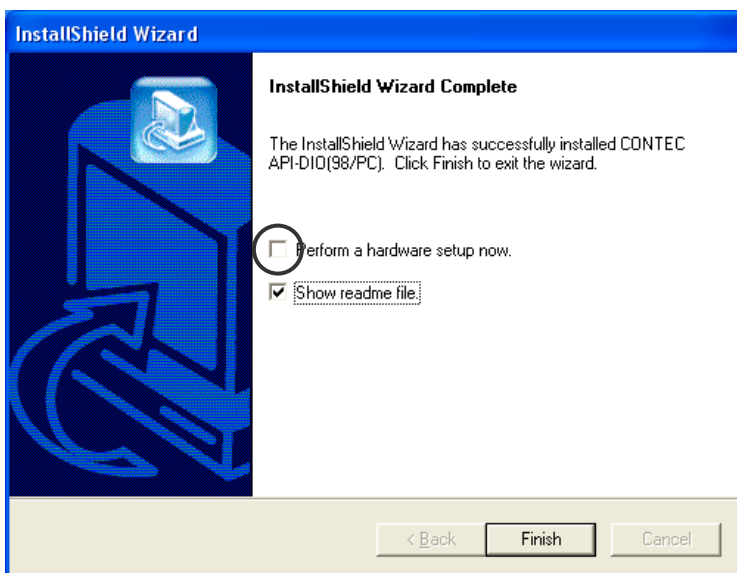
1) Uncheck “Perform a hardware setup now”.

2) Click on the [Finish] button.

Go to Step 2 to set and plug the hardware.

* When the hardware has already been installed:

“Check “Perform a hardware setup now (API-TOOL Configuration)”, then go to Step 4 “Initializing the Software”.



You have now finished installing the software.

Step 2 Setting the Hardware

This section describes how to set the board and plug it on your PC.

The board has some switches to be preset.

Check the on-board switches before plugging the board into an extension slot.

The board can be set up even with the factory defaults untouched. You can change board settings later.

Replacing the Bracket

This board is shipped with a Low Profile size bracket mounted. To plug the board into a standard size slot, replace the bracket with the bundled standard size bracket. The replacing method is as follows :

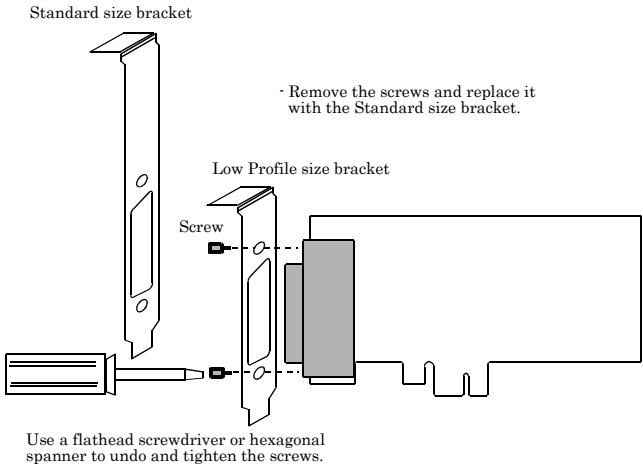


Figure 2.1. Replacing the Bracket

Parts of the Board and Factory Defaults

Figure 2.2. shows the names of major parts on the board.

Note that the switch setting shown below is the factory default.

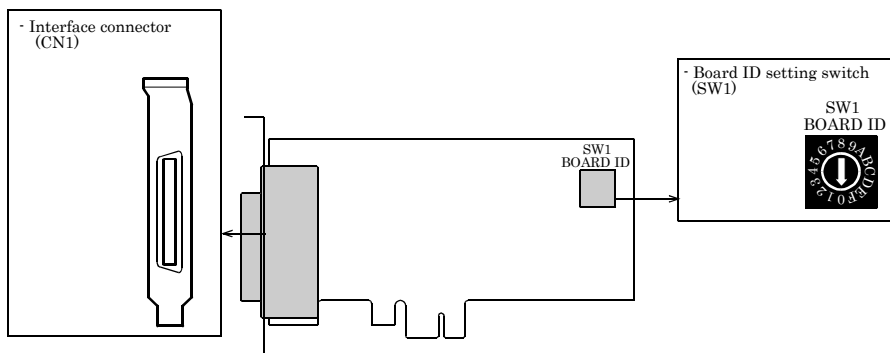


Figure 2.2. Component Locations

Setting the Board ID

If you install two or more boards on one personal computer, assign a different ID value to each of the boards to distinguish them.

The board IDs can be set from 0 to Fh to identify up to sixteen boards.

If only one board is used, the original factory setting (Board ID = 0) should be used.

Setting Procedure

To set the board ID, use the rotary switch on the board. Turn the SW1 knob to set the board ID as shown below.

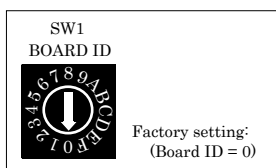


Figure 2.3. Board ID Settings (SW1)

Plugging the Board

- (1) Before plugging the board, shut down the system, unplug the power cord of your PC.
- (2) Remove the cover from the PC so that the board can be mounted.
- (3) Plug the board into an extension slot.
- (4) Attach the board bracket to the PC.
- (5) Put the cover back into place.



CAUTION

- Do not touch the board's metal plated terminals (edge connector) with your hands. Otherwise, the board may malfunction, overheat, or cause a failure.
If the terminals are touched by someone's hands, clean the terminals with industrial alcohol.
 - Do not install or remove the board to or from the slot while the computer's or extension unit's power is turned on.
Otherwise, the board may malfunction, overheat, or cause a failure.
Be sure that the personal computer or the I/O extension unit power is turned off.
 - Make sure that your PC or extension unit can supply ample power to all the boards installed.
Insufficiently energized boards could malfunction, overheat, or cause a failure.
-

Step 3 Installing the Hardware

For using an extension board under Windows, you have to let the OS detect the I/O addresses and IRQ to be used by the board. The process is referred to as installing the hardware.

In the case of using two or more boards, make sure you install one by one with the Add New Hardware Wizard.

Turning on the PC

Turn on the power to your PC.



CAUTION

- The board cannot be properly installed unless the resources (I/O addresses and interrupt level) for the board can be allocated. Before attempting to install the board, first determine what PC resources are free to use.
- The resources used by each board do not depend on the location of the PCI Express bus slot or the board itself.

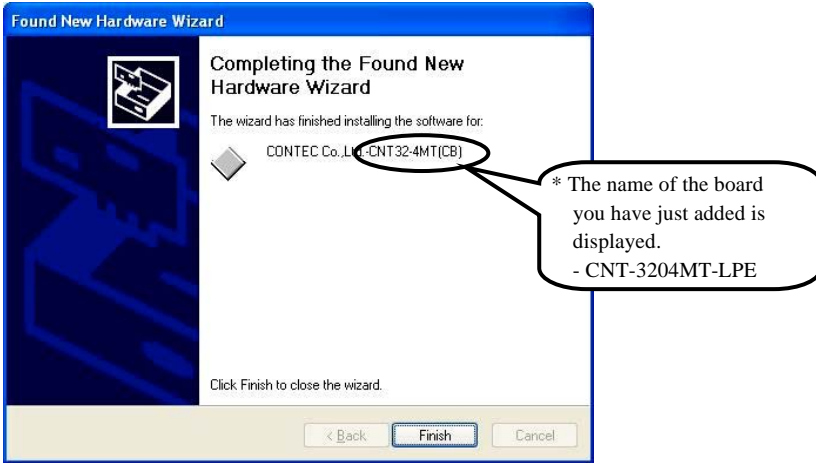
If you remove two or more boards that have already been installed and then remount one of them on the computer, it is unknown that which one of the sets of resources previously assigned to the two boards is assigned to the remounted board. In this case, you must check the resource settings.

Setting with the Found New Hardware Wizard

- (1) First, “PCI Standard PCI to PCI Bridge” will be checked and end automatically.
- (2) The “Found New Hardware Wizard” will be started.
Select “Install from a list or specific location[Advanced]”, then click on the [Next] button.



- (3) Specify that folder on the CD-ROM which contains the setup information (INF) file to register the board.

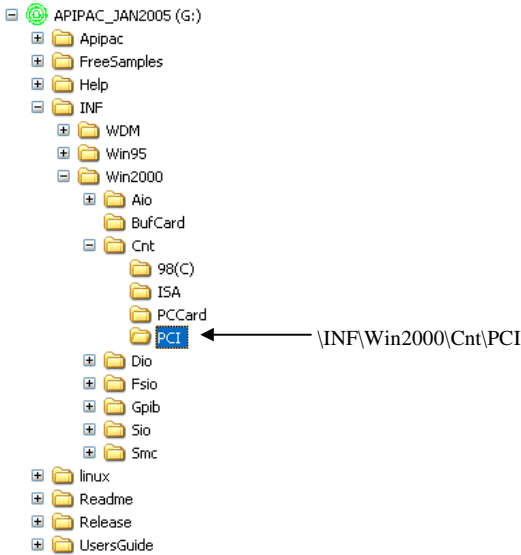


Source folder

The setup information (INF) file is contained in the following folder on the bundled CD-ROM.

Windows XP, 2000 \INF\Win2000\Cnt\PCI

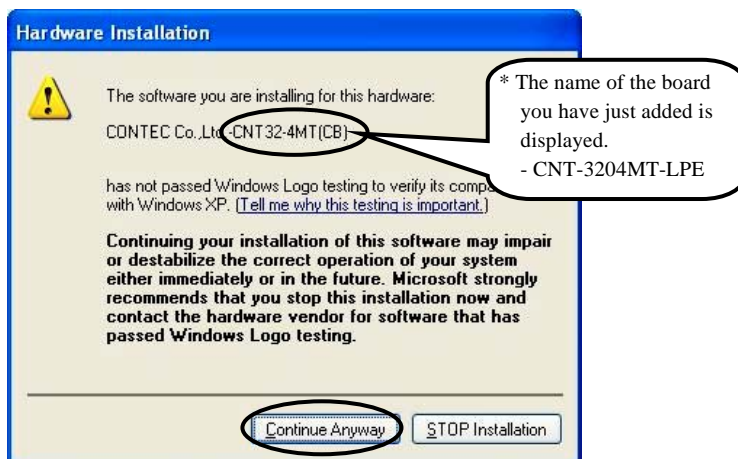
Example of specifying the folder for use this product under Windows XP



⚠ CAUTION

In Windows XP, the Hardware Wizard displays the following alert dialog box when you have located the INF file. This dialog box appears, only indicating that the relevant driver has not passed Windows Logo testing, and it can be ignored without developing any problem with the operation of the board.

In this case, click on the [Continue Anyway] button.



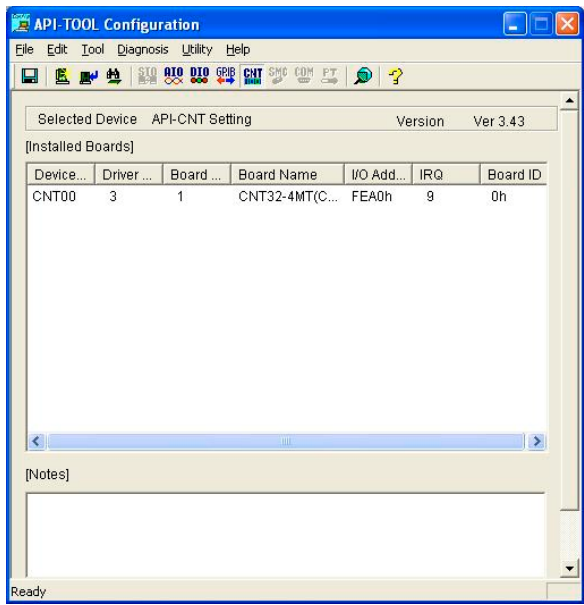
You have now finished installing the software.

Step 4 Initializing the Software

The Driver library requires the initial setting to recognize the execution environment. It is called the initialization of the Driver library.

Invoking API-TOOL Configuration

- (1) Open the Start Menu, then select “Programs” – “CONTEC API-PAC(W32)” – “API-TOOL Configuration”.



- (2) Click on the [CNT] icon.
API-TOOL Configuration detects boards automatically.
The detected boards are listed.

Updating the Setup

- (1) Please execute “File” – “Updating the Setup”.

You have now finished initializing the software.

Step 5 Checking Operations with the Diagnosis Program

Use the diagnosis program to check that the board and driver software work normally, thereby you can confirm that they have been set up correctly.

What is the Diagnosis Program?

The diagnosis program diagnoses the states of the board and driver software.

The program has the following diagnosis methods for checking the board as a single unit using its internal test pulses and the method for checking the board actually connected to an external device.

- Checking the board as a single unit (without external connection)
- Checking the board as a single unit (with external connection)
- Checking the board with an external device

To make sure that the hardware and software have been set up correctly, execute the program for “Checking the board as a single unit (without external connection)”.

You can use the methods for “Checking the board as a single unit (with external connection)” or “Checking the board with an external device” to simply check the board for wiring or for connection to an actually connected external device.

The program has the “diagnosis report” feature to report the driver settings, I/O status, interrupt status, and the presence or absence of the board.

Check method 1: Checking the Board single-handedly (without external connection)

The diagnosis program checks whether the board works normally as a single unit along with the driver using the on-board test pulse outputs. With the test pulse outputs set to internal, the board can count pulse signals without external connection as if the board were connected to an external device. Set the board to the factory defaults before using this method.

What is a test pulse output?

The board has one test pulse output for phase-A and another for phase-B to check whether the counter inputs work normally. The output pulses are LVTTTL level outputs fixed at 100 kHz.

The board can also internally output test pulses to each counter channel without supplying them to the outside. In that case, the board outputs two-phase pulses to all channels at the same time.

Check method 2: Checking the Board single-handedly (with external connection)

When the test pulse outputs are set to external outputs, the board outputs LVTTL level output pulses at 100 kHz from the output pins (TPOA and TPOB). Using the test pulse outputs for external connection, the diagnosis program can check whether the input circuit of the board normally works as a single unit along with the driver. Using the test pulse outputs allows the board to count pulse signals as if it were actually connected to an external device. See the following section for the connection.

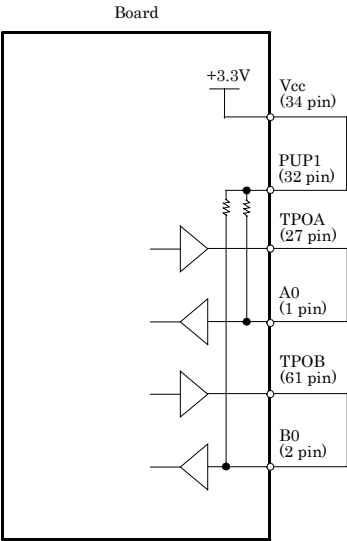
If the board fails to perform counting normally by check method 2, its input circuit may be defective.

Test pulse output circuit and its sample connection (TPOA and TPOB)

This product has one test pulse output for phase-A and another for phase-B for self-diagnosis purposes. The output pulses are LVTTL level output at fixed 100 kHz.

To check the board in single-phase input mode, connect either the phase-A or phase-B test pulse output only. The board performs up-counting (incremental counting) or down-counting (decremental counting) with only the phase-A or phase-B test pulse output connected, respectively.

Connect the pull up pin to the +3.3V output pin.



Pin numbers in the diagram shows those of connector on the board.

Figure 2.4. Sample connection to counter input circuit (ch0)

Check method 3: Checking the Board using an external device

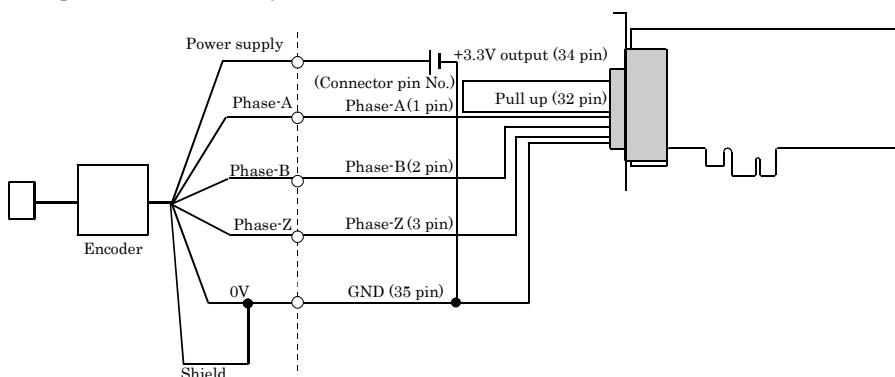
The diagnosis program tests the board actually connected to an external device to check whether count values are displayed correctly and whether signals are successfully turned on/off. See the following section about how they are connected.

If the board fails to perform counting normally by check method 3, the board may be connected incorrectly or the connected device may not be compliant with the specifications.

Connection diagram

Use the following connection to connect to CH0 via a PCA68PS-**P cable and a rotary encoder with a LVTTTL level output/open-collector output. For connection to another channel and for the details on signal position, see Chapter 3 "External Connection".

< Sample connection to rotary encoder (channel 0)>



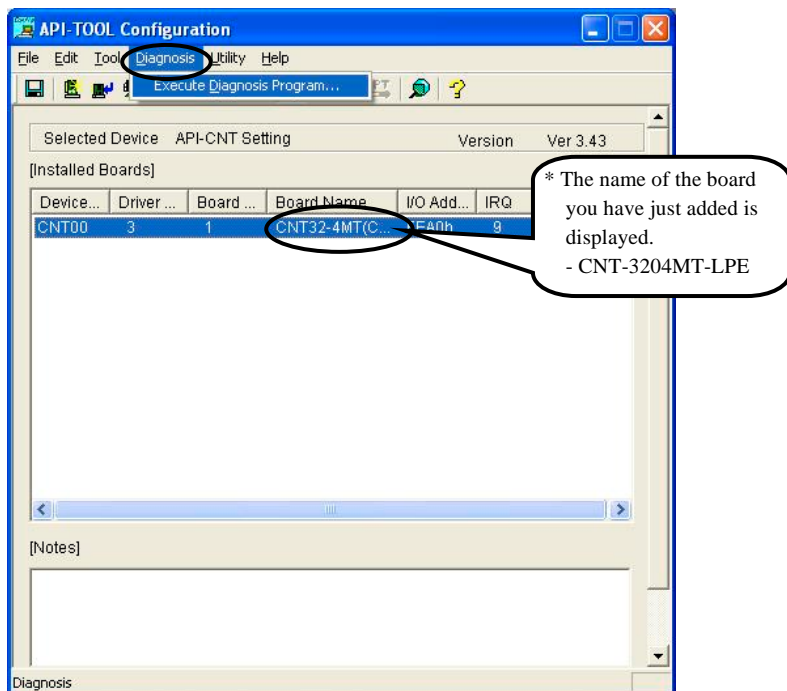
Pin numbers in the diagram shows those of connector on the board.

Figure 2.5. Connection diagram

Using the Diagnosis Program

Starting the Diagnosis Program

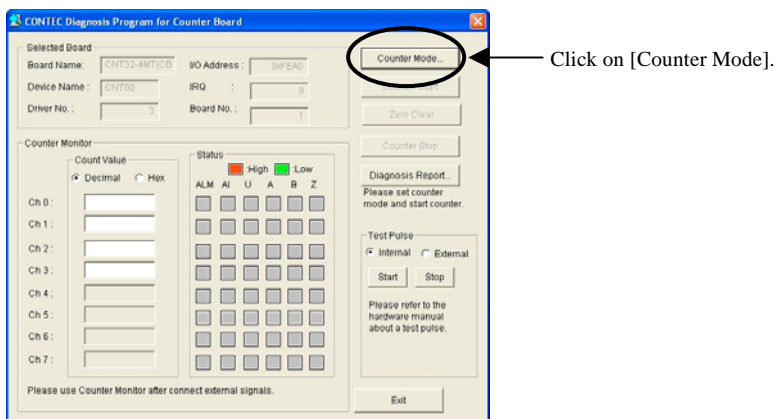
Select the board in the API-TOOL Configuration windows, then run the Diagnosis Program. Please operate in accordance with the instruction of following screen.



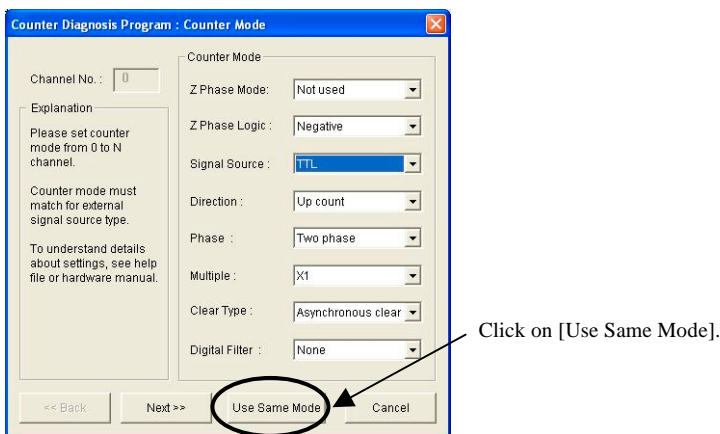
Setting counter operation conditions

- (1) Change counter mode settings. Click on [Counter Mode...].

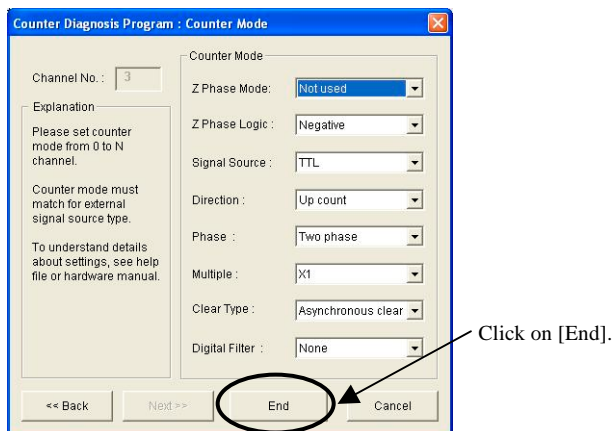
The Counter Mode setting dialog box appears.



- (2) Set the counter mode for channel 0. Leave the other settings at factory defaults. Click on [Use Same Mode]



(3) Click on [End].

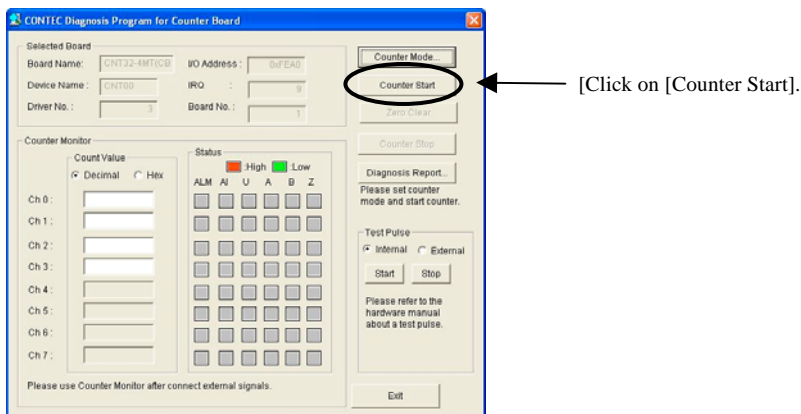


Checking counter operations

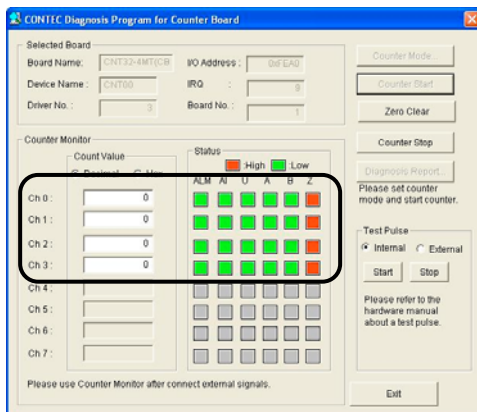
The following commands can be used to check the basic operations of the counter.

- [Counter Start] : Starts the counter.
- [Zero Clear] : Clears the counter to zero.
- [Counter Stop] : Stops the counter.

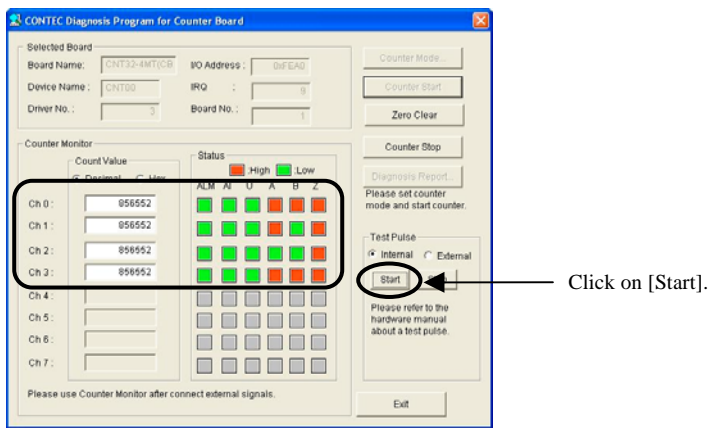
- (1) Click on [Counter Start].



- (2) The counter value of each channel is displayed along with its status (ALM, AI, U, A, B, Z).



- (3) Clicking on [Start] with “Test Pulse” set to “Internal” outputs two-phase line receiver signals to all channels, allowing you to check their count value and status.

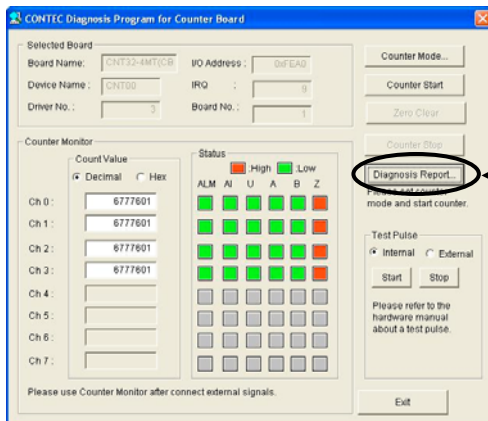


Diagnosis Report

- (1) Clicking on [Diagnosis Report...] displays detailed data such as board and channel settings and the diagnosis results as saved in text format.

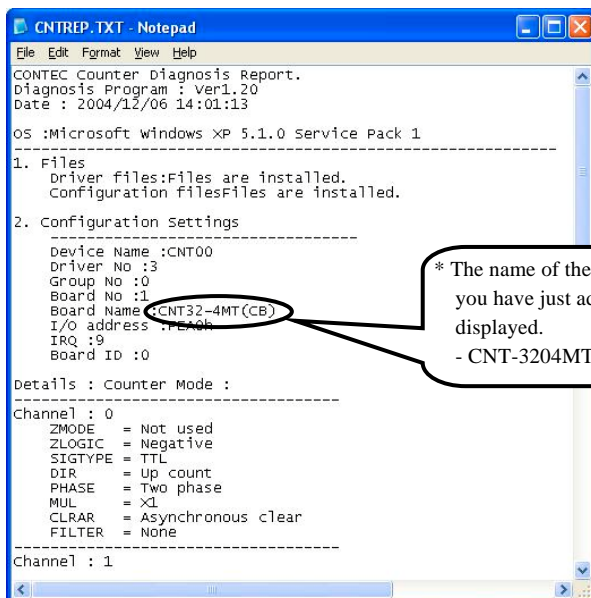
The saved results are displayed as a text file (CntRep.txt) in the install folder (Program Files\CONTEC\API-PAC(W32)).

The diagnosis program performed includes "Board presence/absence check", "interrupt test", "driver file test", "Board setting test".



Click on
[Diagnosis Report...]

- (2) A diagnosis report is displayed as shown below.



* The name of the board
you have just added is
displayed.
- CNT-3204MT-LPE

Setup Troubleshooting

Symptoms and Actions

No count value can be read.

The counter mode setting may be inappropriate.

The board won't work successfully unless the counter mode is set according to the input signal format. Refer to the function description in API-CNT HELP or the user's guide for the board to configure the appropriate counter mode.

The board works with the Diagnosis Program but not with an application.

The Diagnosis Program is coded with API-TOOL functions. As long as the board operates with the Diagnosis Program, it is to operate with other applications as well. In such cases, review your program while paying attention to the following points:

- Check the arguments to functions and their return values.
- Check whether the counter mode is appropriate for the incoming signal format.

The OS won't normally get started or detect the board. [Windows XP, 2000]

Turn off the power to your PC, then unplug the board. Restart the OS and delete the board settings of API-TOOL Configuration. Turn off the PC again, plug the board, and restart the OS. Let the OS detect the board and use API-TOOL Configuration to register board settings.

If your problem cannot be resolved

Contact your retailer.

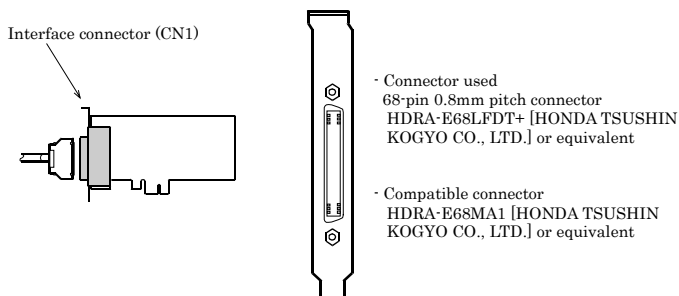
3. External Connection

This chapter describes the interface connectors on the board and the external I/O circuits.
Check the information available here when connecting an external device.

How to connect the connectors

Connector shape and optional cable connection

The on-board interface connector (CN1) is used when connecting this product and the external devices.



* Please refer to chapter 1 for more information on the supported cable and accessories.

Figure 3.1. Interface connector (CN1) and used connector

This shows the connection example of this product and optional cables (CNT-68M/50M or PCA68PS-**P, PCB68PS-**P)

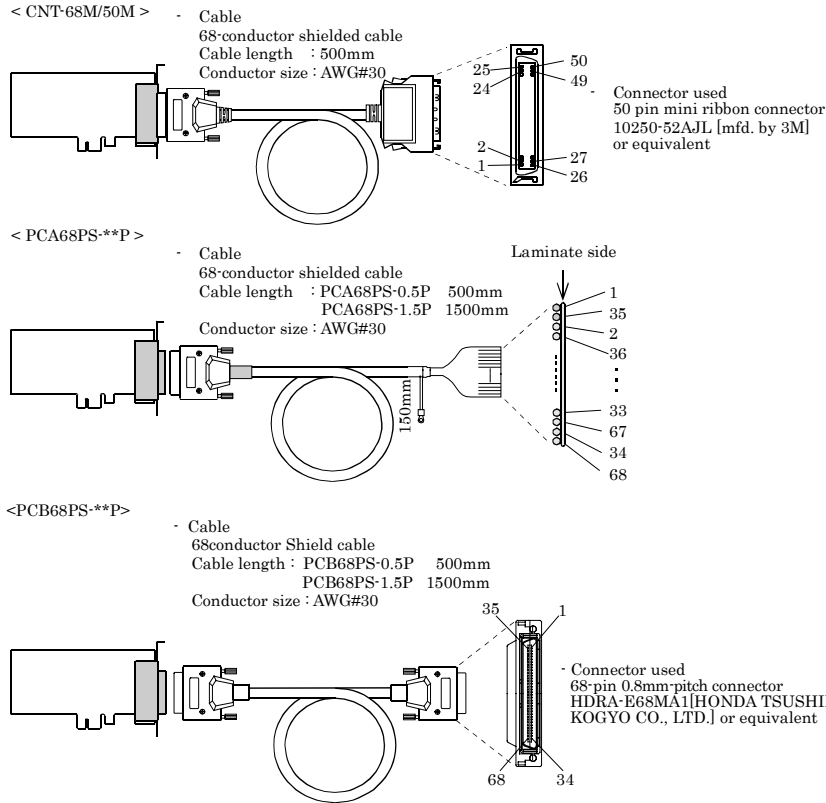


Figure 3.2. Connecting the interface connector (CN1) and optional cables

Connector Pin Assignment

Pin Assignments of Interface Connector (CN1)

CH0 Phase-A input	A0	1	35	- GND	Ground
CH0 Phase-B input	B0	2	36	- GND	Ground
CH0 Phase-Z input	Z0	3	37	- GND	Ground
CH0 control input *1	D0	4	38	- GND	Ground
Unconnection	N.C.	5	39	- N.C.	Unconnection
CH1 Phase-A input	A1	6	40	- GND	Ground
CH1 Phase-B input	B1	7	41	- GND	Ground
CH1 Phase-Z input	Z1	8	42	- GND	Ground
CH1 control input *1	D1	9	43	- GND	Ground
Unconnection	N.C.	10	44	- N.C.	Unconnection
CH2 Phase-A input	A2	11	45	- GND	Ground
CH2 Phase-B input	B2	12	46	- GND	Ground
CH2 Phase-Z input	Z2	13	47	- GND	Ground
CH2 control input *1	D2	14	48	- GND	Ground
Unconnection	N.C.	15	49	- N.C.	Unconnection
CH3 Phase-A input	A3	16	50	- GND	Ground
CH3 Phase-B input	B3	17	51	- GND	Ground
CH3 Phase-Z input	Z3	18	52	- GND	Ground
CH3 control input *1	D3	19	53	- GND	Ground
Unconnection	N.C.	20	54	- N.C.	Unconnection
Sampling clock input	CLKIN	21	55	- GND	Ground
Sampling stop input	STOPIN	22	56	- STARTIN	Sampling start input
Unconnection	N.C.	23	57	- N.C.	Unconnection
Sampling clock output	CLKOUT	24	58	- GND	Ground
Sampling stop output	STOPOUT	25	59	- STARTOUT	Sampling start output
Unconnection	N.C.	26	60	- N.C.	Unconnection
Test pulse Phase-A output	TPOA	27	61	- TPOB	Test pulse Phase-B output
Unconnection	N.C.	28	62	- N.C.	Unconnection
CH0 control output *2	DO0	29	63	- DO1	CH1 control output *2
CH2 control output *2	DO2	30	64	- DO3	CH3 control output *2
Unconnection	N.C.	31	65	- N.C.	Unconnection
Counter input signal pull up	PUP1	32	66	- PUP2	Control input signal pull up
Unconnection	N.C.	33	67	- N.C.	Unconnection
+3.3V output *3	Vcc	34	68	- Vcc	+3.3V output *3

*1 The control input can serve as the general-input, counter start / stop, preset and zero-clear.

*2 The control output can serve as the general-output, count match, abnormal input error and figital filter error.

*3 Supply-capable current is 500mA (Max.).

Figure 3.3. Pin Assignments of Interface Connector (CN1)

Signal assignment with the CNT-68M/50M used (50-pin connector side)

+3.3V Output *3	Vcc	--	25	50	--	Vcc	+3.3V Output *3
Counter Input signal pull-up	PUP1	--	24	49	--	PUP2	Counter Input signal pull-up
CH2 control output *2	DO2	--	23	48	--	DO3	CH3 control output *2
CH0 control output *2	DO0	--	22	47	--	DO1	CH1 control output *2
Test pulse Phase-A output	TPOA	--	21	46	--	TPOB	Test pulse Phase-B output
Sampling Stop Output	STOPOUT	--	20	45	--	STARTOUT	Sampling Start Output
Sampling Clock Output	CLKOUT	--	19	44	--	GND	Ground
Sampling Stop Input	STOPIN	--	18	43	--	STARTIN	Sampling Start Input
Sampling Clock Input	CLKIN	--	17	42	--	GND	Ground
CH3 Control Input *1	DI3	--	16	41	--	GND	Ground
CH3 Phase-Z input	Z3	--	15	40	--	GND	Ground
CH3 Phase-B input	B3	--	14	39	--	GND	Ground
CH3 Phase-A input	A3	--	13	38	--	GND	Ground
CH2 Control Input *1	DI2	--	12	37	--	GND	Ground
CH2 Phase-Z input	Z2	--	11	36	--	GND	Ground
CH2 Phase-B input	B2	--	10	35	--	GND	Ground
CH2 Phase-A input	A2	--	9	34	--	GND	Ground
CH1 Control Input *1	DI1	--	8	33	--	GND	Ground
CH1 Phase-Z input	Z1	--	7	32	--	GND	Ground
CH1 Phase-B input	B1	--	6	31	--	GND	Ground
CH1 Phase-A input	A1	--	5	30	--	GND	Ground
CH0 Control Input *1	DI0	--	4	29	--	GND	Ground
CH0 Phase-Z input	Z0	--	3	28	--	GND	Ground
CH0 Phase-B input	B0	--	2	27	--	GND	Ground
CH0 Phase-A input	A0	--	1	26	--	GND	Ground

- *1 The control input can serve as the general-input, counter start / stop, preset and zero-clear.
- *2 The control output can serve as the general-output, count match, abnormal input error and figital filter error.
- *3 Supply-capable current is 500mA (Max.).

Figure 3.4. Pin Assignments on 50-pin Connector Side

How to Connect the Counter Input Signal

You can connect to a rotary encoder or linear scale with a TTL level output circuit, or to an open-collector output circuit. The signal must be an LVTTTL level input and can be up to 10MHz.

As pull-up resistors are provided on the board, connect the pull-up voltage (3.3V - 5.5V max.) to the pull-up pins if connecting to an open collector output circuit/TTL level output circuit. (If using 3.3V, connect to the VCC pin on the board.) Not connecting the pull-up voltage may affect the counter input channel left unconnected.

For a two-phase input, connect both phase A and phase B. For a single phase input, connect to either phase A or phase B. If not using the Z phase, this does not need to be connected.

Remarks

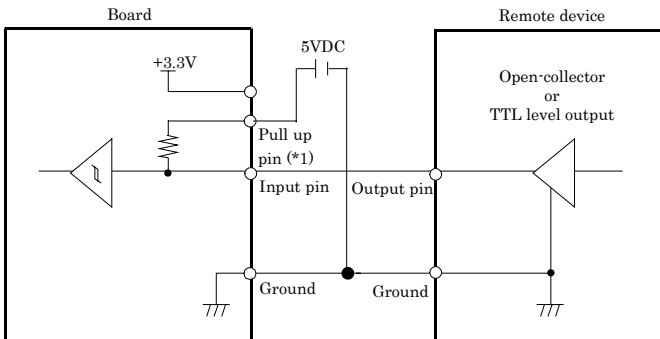
The pull-up pins are PUP1 (pin 32 *1) for the counter input signal and PUP2 (pin 66 *1) for the control input signal.

PUP1 (pin 32) : Pull-up for A, B, and Z phase input signal
(A0, B0, Z0, A1, B1, Z1, A2, B2, Z2, A3, B3, Z3).

PUP2 (pin 66) : Pull-up for the control input signals and for the sampling input signals
(DI0, DI1, DI2, CLKIN, STARTIN, STOPIN).

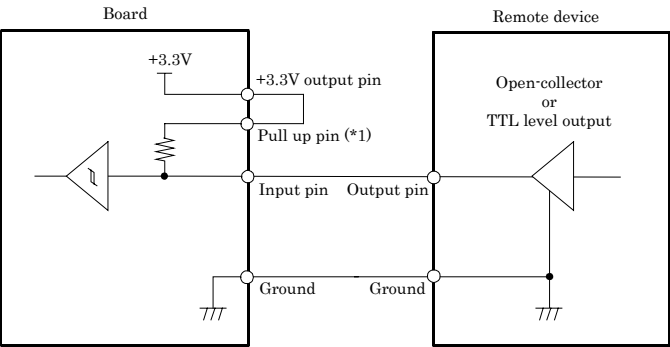
*1: Connector pin number on the board.

Example Connection for Counter Input Circuit



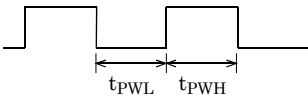
*1: The pull-up pins are PUP1 for the counter input signal and PUP2 for the control input signal.

Figure 3.5. Connection pulled up with external 5-V power (Counter Input)



*1: The pull-up pins are PUP1 for the counter input signal and PUP2 for the control input signal.

Figure 3.6. Connection pulled up with internal 3.3-V output power (Counter Input)



t_{PWH} : High-level count input pulse width 50nsec (Min.)

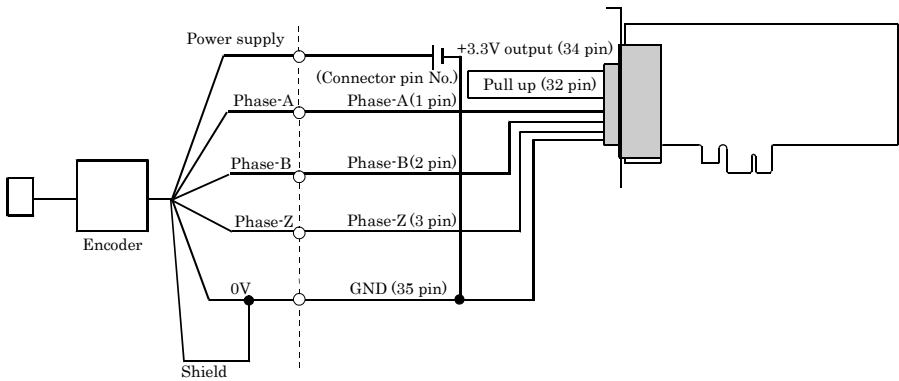
t_{PWL} : Low-level count input pulse width 50nsec (Min.)

Figure 3.7. Input signal

⚠ CAUTION

- The connection cable length should be within 1.5 m.
- To prevent noise from causing a malfunction, arrange the connection cable as away from any othersignal conductor or noise source as possible.

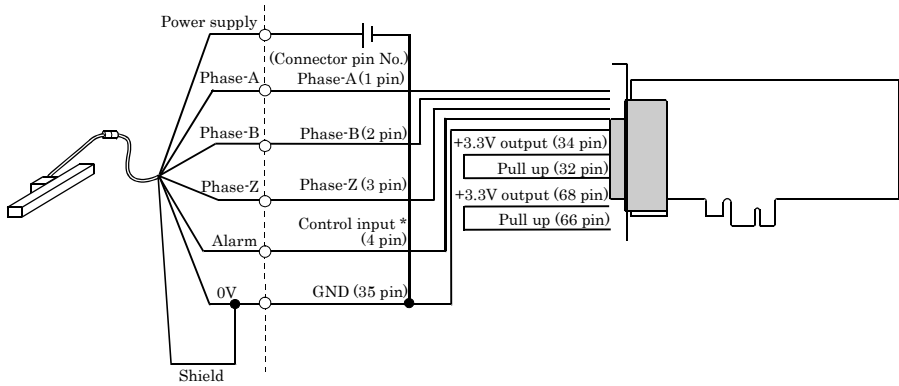
Example Connection with a Rotary Encoder



Pin numbers in the diagram shows those of connector on the board.

Figure 3.8. Sample connection to rotary encoder (Channel 0)

Example Connection with a Linear scale



* When the control input is set as a general-purpose input, the alarm output state can be checked.

When the control input is set as the counter stop input, the counter can be stopped at alarm output.

Pin numbers in the diagram shows those of connector on the board.

Figure 3.9. Example Connection with a Linear Scale (channel 0)

Connecting the control signal input/output

Connection of a control input

The control input signals consist of one pin per channel that can be selected as the channel's counter start/stop or preset, and one pin per board that can be used as the start, stop, and clock for sampling. The signals are LVTTTL-level (3.3V) inputs.

As pull-up resistors (10KΩ) are provided on the board, connect the pull-up voltage (3.0V - 5.5V max.) to the pull-up pins if connecting to an open collector output circuit/TTL level output circuit. (If using 3.3V, connect to the VCC pin on the board.) Not connecting the pull-up voltage may affect the control input pin left unconnected.

Remarks

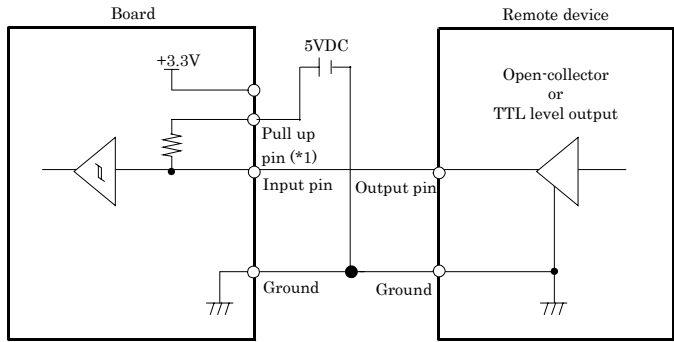
The pull-up pins are PUP1 (pin 32 *1) for the counter input signal and PUP2 (pin 66 *1) for the control input signal.

PUP1 (pin 32) : Pull-up for A, B, and Z phase input signal
(A0, B0, Z0, A1, B1, Z1, A2, B2, Z2, A3, B3, Z3).

PUP2 (pin 66) : Pull-up for the control input signals and for the sampling input signals
(DI0, DI1, DI2, DI3, CLKIN, STARTIN, STOPIN)

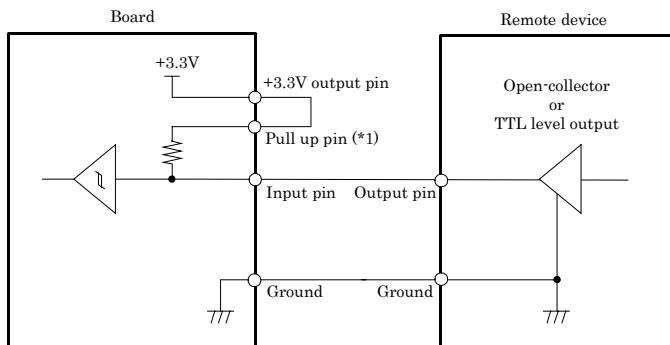
*1: Connector pin number on the board.

Control input circuit and its sample connection



*1 : The pull-up pins are PUP1 for the counter input signal and PUP2 for the control input signal.

Figure 3.10. Connection pulled up with external 5-V power
(Control input DI0, DI1, DI2, DI3, CLKIN, STARTIN, STOPIN)



*1 : The pull-up pins are PUP1 for the counter input signal and PUP2 for the control input signal.

Figure 3.11. Connection pulled up with internal 3.3-V output power
(Control input DI0, DI1, DI2, DI3, CLKIN, STARTIN, STOPIN)

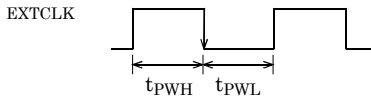


CAUTION

- The connection cable length should be within 1.5 m.
- To prevent noise from causing a malfunction, arrange the connection cable as away from any othersignal conductor or noise source as possible.

External sampling clock signal (EXTCLK)

Pin used to input the external pacer clock. The maximum frequency is 10MHz.
If the external clock input is selected as the sampling clock, sampling occurs on the falling edge of the signal.

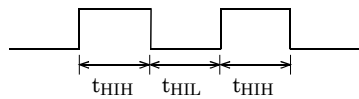


t_{PWH} : High-level clock pulse width 50nsec (Min.)
 t_{PWL} : Low-level clock pulse width 50nsec (Min.)

Figure 3.12. External sampling clock signal

Other control input signals (DI0 - DI3, EXTSTART, EXTSTOP)

These signals are TTL level compatible and the trigger edge is software-programmable at either the rising or falling edge. High- and low-level hold times of at least 50 nsec are required to detect an edge of the signal.



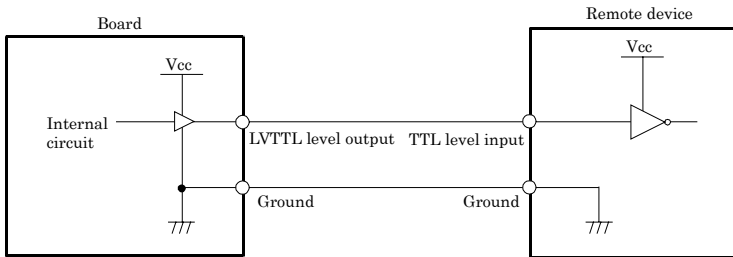
t_{HIH} : High-level hold time 50nsec (Min.)
 t_{HIL} : Low-level hold time 50nsec (Min.)

Figure 3.13. Control input signals

Connection of a control output

This outputs a general-purpose output signal (level output) or a one-shot pulse output to indicate a hardware event such as a count match. The signal is an LVTTTL level output and can be set to positive or negative logic by software.

Control output circuit and its sample connection



**Figure 3.14. Sample connection to control output circuit
(DO0 - DO3, CLKOUT, STARTOUT, STOPOUT)**

4. Functions

This section describes the features of the board.

Types and Operations of Pulse Signals

Types of pulse signals

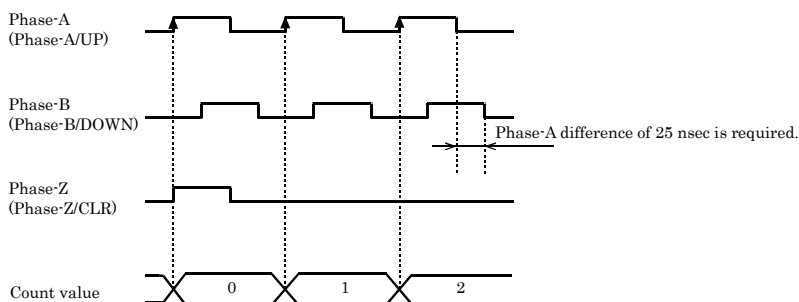
The following types of pulse signals (operation modes) can be set.

- 2-phase Input, Synchronous Clear, Multiply by 1
- 2-phase Input, Synchronous Clear, Multiply by 2
- 2-phase Input, Synchronous Clear, Multiply by 4
- 2-phase Input, Asynchronous Clear, Multiply by 1
- 2-phase Input, Asynchronous Clear, Multiply by 2
- 2-phase Input, Asynchronous Clear, Multiply by 4
- Single-phase Input, Asynchronous Clear, Multiply by 1
- Single-phase Input, Asynchronous Clear, Multiply by 2
- Single-phase Input with Gate Control Attached, Asynchronous Clear, Multiply by 1
- Single-phase Input with Gate Control Attached, Asynchronous Clear, Multiply by 2

2-phase Input

A two-phase pulse input consists of two pulse inputs phase A (advanced signal) and phase B (delayed signal) with a phase difference of 90 °C.

If the Z phase (reference position signal) is used, the two-phase pulse input can clear the count value to zero.

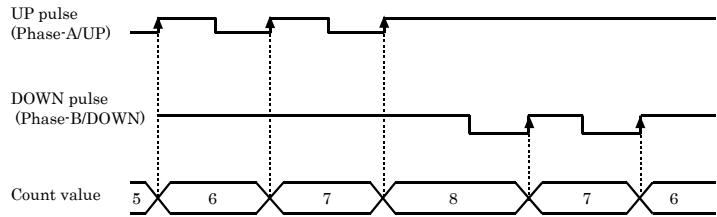


- * Count operation for incremental counting in the CW direction. When decremental counting in the CW direction is set, the board performs decremental counting at the rising edge of the phase-A signal.
- * The minimum phase difference between phases-A and B is 25 nsec. Counting is not performed normally if the phase difference is less than 25 nsec.

Figure 4.1. Example counting during 2-phase input

Single-phase Input

For a single-phase input, the count increments when an UP pulse is input and decremented when a DOWN pulse is input. The count remains unchanged if UP and DOWN pulses are input simultaneously.

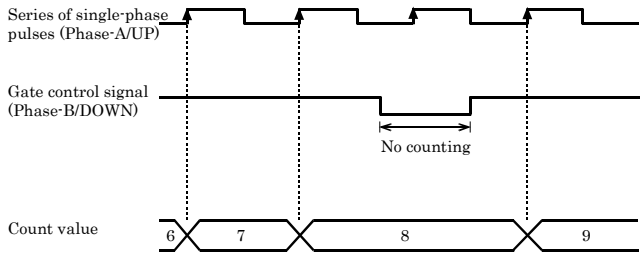


* Count operation for incremental counting in the CW direction. When decremental counting in the CW direction is set, the board performs decremental counting at the rising edges of positive pulse and incremental counting at the rising edges of negative pulses.

Figure 4.2. Example counting during single-phase input

Single-phase Input with Gate Control Attached

The counter can be started and stopped using a gate control signal input along with the single-phase pulse stream.



* Count operation for incremental counting in the CW direction. When decremental counting in the CW direction is set, the board performs decremental counting at the rising edges of the single-phase pulse train (phase-A/UP) while the gate control signal (phase-B/DOWN) goes high and stops counting while the gate control signal goes low.

Figure 4.3. Example counting during single-phase input with gate control attached

Multiplication of Count Input

Setting the count input multiplication setting to two or four times enables you to fine-tune controlling.

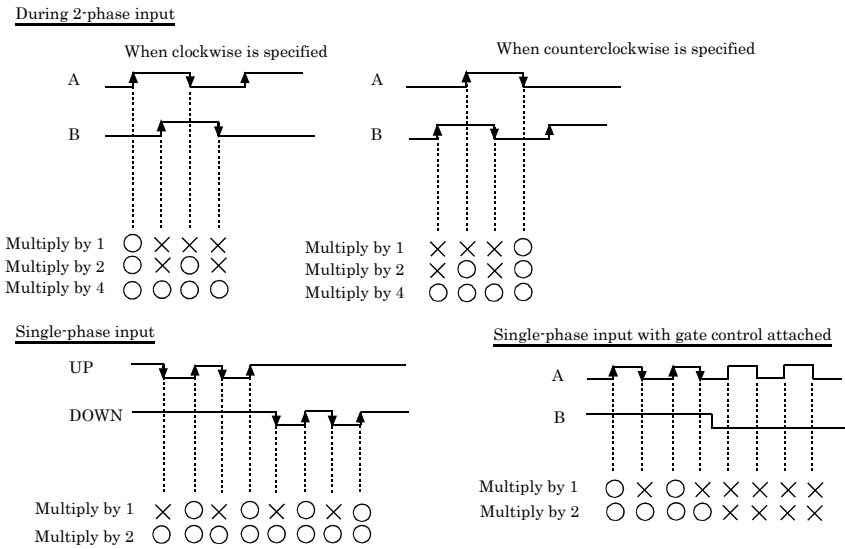
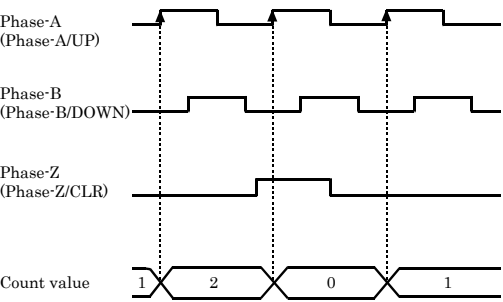


Figure 4.4. Example counting when count input multiplication is set

Synchronous Clear

When incremental counting in the CW (clockwise) direction is set with phase-Z positive logic, the board clears the counter at the rising edge of the phase-A signal while the phase-Z input goes high and starts counting at the rising edge of the phase-A signal after the phase-Z input goes low.

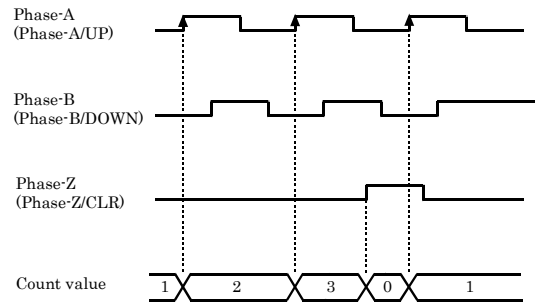


* When decremental counting in the CW direction is set, the board performs decremental counting at the rising edge of the phase-A signal while the phase-B input remains low.

Figure 4.5. Example counting during synchronous clear

Asynchronous Clear

When incremental counting in the CW (clockwise) direction is set with phase-Z positive logic, the board clears the counter when the phase-Z input goes high while phases A and B are in the input state. The board starts counting at the rising edge of the phase-A signal while phase-Z is in the input state.



* When incremental counting in the CW direction is set with phase-Z positive logic, the board performs decremental counting at the rising edge of the phase-A signal while the phase-B input remains low. When phase-Z negative logic is used, the signal is enabled while the phase-Z input remains low.

Figure 4.6. Example counting during asynchronous clear

Phase-Z/CLR Input

Phase-Z is the signal to clear the counter to zero. The number of phase-Z inputs can be specified by software.

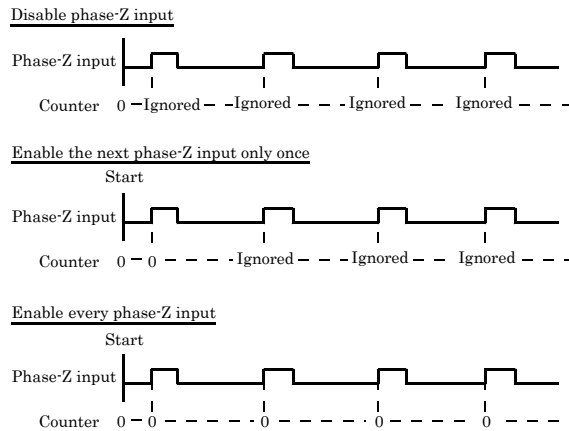


Figure 4.7. Phase-Z enable frequency (Positive logic)

CAUTION

- The initial setting is “only the next phase-Z input is enabled once”.
- Phase-Z (negative logic) is enabled while the phase-Z input goes low.
- When the phase-Z/CLR input is not used, be sure to disable the phase-Z input.

Control of a counter

Counter start/Counter stop

This product can be started and stopped individually for each channel or globally for all channels. The table below lists the counter start and stop triggers. Which triggers to use can be selected by software.

Table 4.1. Counter operation

Item	Factor	Description	Note
Counter start trigger	Software command (for all/each of channels)	Possible to start the counter for all or each of the channels.	
	Rise of control input signal	The counter is started at a level change (low-to-high transition).	Available only when the control input signal has been selected for counter start/stop.
	Fall of control input signal	The counter is started at a level change (high-to-low transition).	Available only when the control input signal has been selected for counter start/stop.
	Sampling start	Sampling start = counter start	
Counter stop trigger	Software command (for all/each of channels)	Possible to stop the counter for all or each of the channels.	
	Rise of control input signal	The counter is ended at a level change (low-to-high transition).	Available only when the control input signal has been selected for counter start/stop.
	Fall of control input signal	The counter is ended at a level change (high-to-low transition).	Available only when the control input signal has been selected for counter start/stop.
	Sampling stop	Sampling stop = counter stop	0

Software

The counter is started or stopped by software either for each channel or for all channels.

Rise/fall of an external input signal

The counter is started or stopped by an external input signal supplied through the control input pin. The rising or falling edge of the signal can be selected for starting or stopping the counter. If the “low-to-high” transition is set for both starting and stopping the counter, the counter is started or stopped if it is inactive or active, respectively, when the level changes from low to high.

- * When the control input pin is used for the counter start/stop signal, it cannot be used for the preset, zero-clear, or general-purpose input.

Sampling start/stop

When the counter start trigger is used for starting sampling, the board starts counting and sampling synchronously. When the counter stop trigger is used for stopping sampling, the board stops counting and sampling synchronously in the same way.

Preset

Presetting means setting the counter to an arbitrary value.
The value in the preset register is loaded into the counter. Preset methods are listed in the table below, which are software-selectable.

Table 4.2. Preset

Item	Factor	Description	Note
Preset method	Software command	Possible to preset for all channels	Always available
	Control input signal (rise)	Control input level change (Low to High)	Available only when the control input signal has been selected for presetting.
	Control input signal (fall)	Control input level change (High to Low)	Available only when the control input signal has been selected for presetting.
	Count match (Register0)	Count value = Comparison register 0	
	Count match (Register1)	Count value = Comparison register 1	

Software

The counter is presetting by software either for each channel or for all channels.

Rise/fall of an external input signal

The counter is presetting by an external input signal supplied through the control input pin. The control input pin is used for preset signal input. The rising or falling edge of the signal can be selected.

* When the control input pin is used for presetting, it cannot be used for the counter start/stop, zeroclear, or general-purpose input.

Count match

The counter is preset when the count value matches the value in comparison register 0 or 1.

Zero-clear

The counter is cleared to zero. Zero-clear methods are listed in the table below.
The zero-clear method is software-selectable.

Table 4.3. Zero-clear

Item	Factor	Description	Note
Zero-clear method	Software command	Possible to preset for all channels	Always available
	Phase Z input Phase	Z input level change	Always available
	Control input signal (rise)	Level change	Available only when the control input signal has been selected for Zero-clearing.
	Control input signal (fall)	Level change	Available only when the control input signal has been selected for Zero-clearing.
	Count match (Register0)	Count value = Comparison register 0	
	Count match (Register1)	Count value = Comparison register 1	

Software

The counter is zero-cleared by software either for each channel or for all channels.

Phase-Z Input

The counter is zero-cleared by the external phase-Z input signal. Software is used to select positive or negative logic and to enable or disable zero-clearing.

Rise/fall of an external input signal

The counter is zero-cleared by an external input signal supplied through the control input pin. The control input pin is used for preset signal input. The rising or falling edge of the signal can be selected.

- * When the control input pin is used for zero-clearing, it cannot be used for the counter start/stop, preset, or general-purpose input.

Count match

The counter is zero-cleared when the count value matches the value in comparison register 0 or 1.

Register

The board has a preset register and comparison registers.

Preset Register

The preset register is a 32-bit register to load the value in the preset register to the counter when presetting occurs.

Comparison register 0, Comparison register 1

These are 32-bit registers. A variety of events can occur when the counter value matches the value in comparison register 0 or 1.

Obtaining the count value

Obtaining the count value

There are two modes for obtaining the count value. One is the **counter mode** to directly read the count value without using bus mastering and the other is the **sampling mode** to sample the count value periodically using bus mastering.

This product is capable of bus mastering, enabling periodical sampling of the count value using the internal or external clock signal.

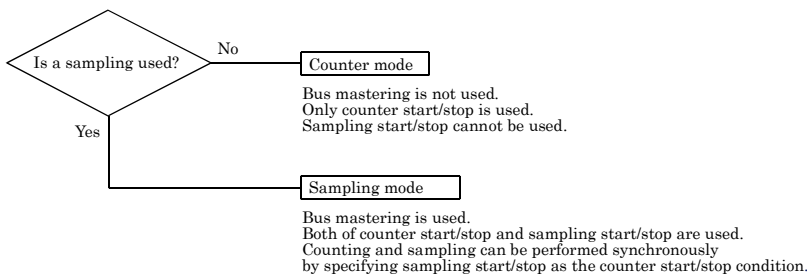


Figure 4.8. Counter mode and sampling mode

Counter mode

In the counter mode, the board starts the counter after setting counter operation conditions and performs counter operations such as reading the count value and status.

In addition, this mode allows the board to preset, zero-clear, start/stop the counter at the rising or falling edge of the control input signal. A one-shop pulse can be output to the control input signal at an occurrence of a count match or error.

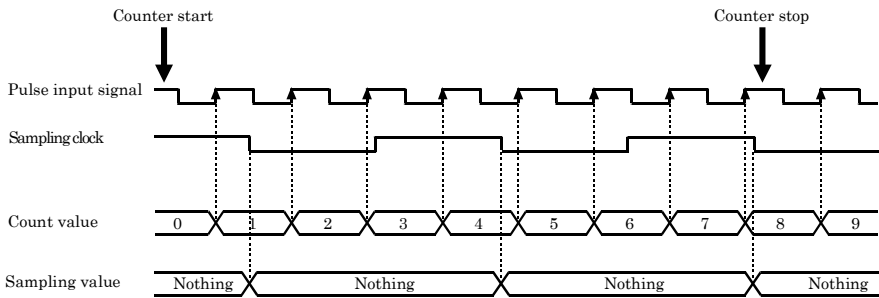


Figure 4.9. Timing chart (Counter mode)

Sampling mode

In the sampling mode, the board samples the count value periodically to load it into memory on the PC according to the specified internal or external clock. The area of memory to store sampling data is a maximum of 64 MB (16777216 data items), which is restricted depending on the OS used. In particular, Windows XP, 2000 allows less memory to be allocated relative to the total amount of physical memory. For use under such an OS, therefore, the area of memory that can be allocated should be checked with a sample program.

For the sampling mode, sampling operation conditions must be set as well as counter operation conditions. For details about sampling, see “Sampling function” described later in this chapter. The counter start can be synchronized with the sampling start.

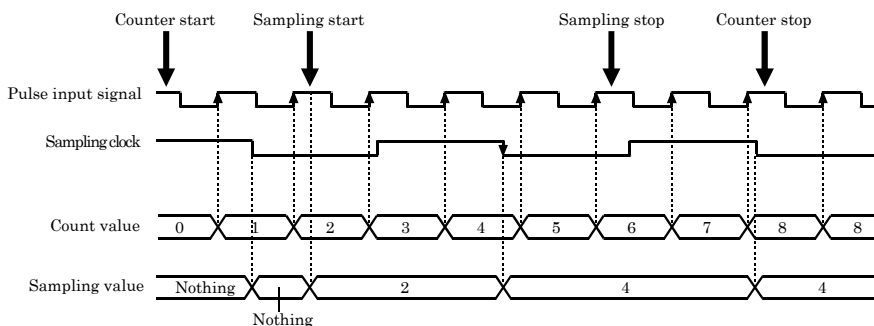


Figure 4.10. Timing chart (Counter-asynchronous sampling mode)

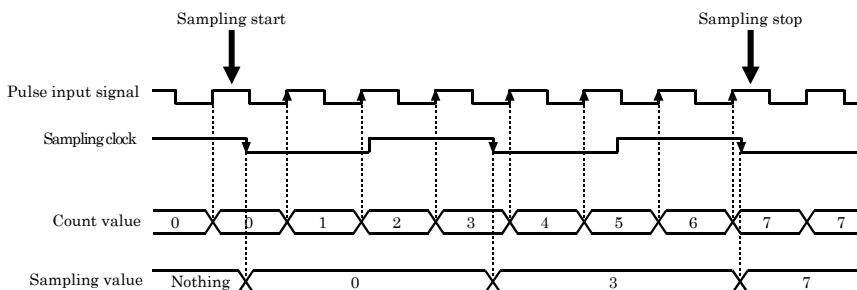
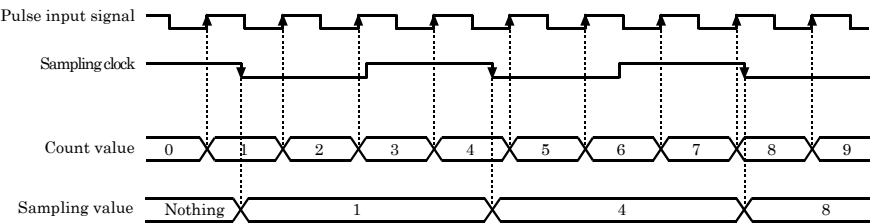


Figure 4.11. Timing chart (Counter-synchronous sampling mode)

Totalizing/line receiver counter

When using sampling mode, the counter can be used as a differential counter. In totalizing counter mode, the value is sampled at fixed time intervals as in normal up/down counter operation. In differential counter mode, the difference with the count at the previous sampling time is sampled. A totalizing counter or differential counter can be setup for each channel.

Totalizing counter mode



Line receiver counter mode

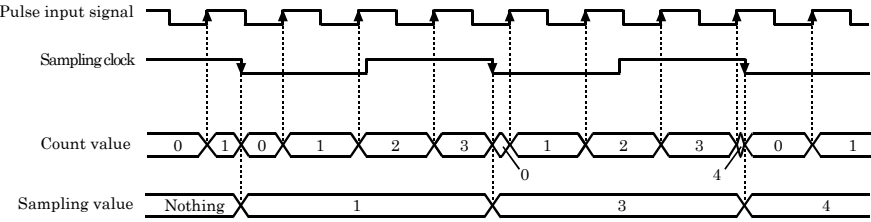


Figure 4.12. Totalizing/line receiver counter

Sampling function

Sampling function

The sampling function obtains count data periodically using the internal or external clock.

The sampling data is transferred to the PC's memory using bus master transfer. The sampling speed can be up to 20MHz using the internal clock (for 1ch). As bus master transfer is used, an error occurs and transfer halts if this product cannot obtain bus access in time for a transfer. Note that whether or not continuous transfer at 20MHz is possible depends on factors such as what other applications are running on the PC.

This product can perform sampling at various different timings based on the start condition, clock condition, and stop condition combination. See the "Sampling Control" section later in this manual for details of how to setup the sampling conditions.

Bus mastering

The bus master transfer function on this product performs DMA transfer between the board and the application's memory space by utilizing times when the bus is idle. Use a standard variable declaration in your program to define a static area to use as the application's memory space. As application memory space is defined by a logical address in operating systems such as Windows, the area may not be continuous in the physical address space. However, this product can still perform continuous data transfer to such discontinuous physical address spaces. Bus master transfer can transfer data to physical address spaces up to 64Mbytes. When setting up the transfer area in your application, note that the actual available memory size depends on the operating system you are using and on the size of memory installed on the PC.

The bus master transfer function can transfer data to memory as a one-time transfer or as a ring (cyclic) transfer. For a one-time transfer, the transfer halts when the end of the specified memory area is reached. For ring transfer, transfer starts again from the start of the specified memory area when the end of the memory area is reached. The transfer continues until the stop condition is detected or the transfer is halted by software.

Interrupt (During bus mastering)

The following interrupt features are available during bus mastering:

- Generating an interrupt upon completion of transfer of the specified number of data items
- Generating an interrupt upon completion of transfer

These interrupts can be passed to the application using the "API-CNT(98/PC)" routines in API-PAC(W32).

If transfer halts due to an error such as not being able to obtain bus access in time to perform a transfer, this product halts the transfer and generates the transfer completion interrupt. Whether or not an error has occurred can be determined by checking the status.

Status, Count

The following types of status (error) are available concerning bus mastering.

Table 4.4. Status about bus master (Error)

Status	Description
BUS MASTER STOP	Indicates that bus mastering transfer has completed.
CNT START	Indicates that counter sampling has been started.
CNT STOP	Indicates that counter sampling has been stoped.
TRIGGER IN	Indicates that the external start signal has been received.
OVER RUN	Indicates that the external start signal has been received at least twice. Transfer continues normally.

Error	Description
FIFO FULL	Indicates that FIFO memory has been full. This is mainly because a heavy load on the system prevented bus mastering from being executed in time. Take appropriate action, for example, lower the transfer rate or system load.
S/G OVER IN	Indicates a buffer overflow. The number of data items to be transferred exceeds the buffer size. Increase the buffer size.
TRG ERROR	Indicates that the external start and stop signals have been received. Transfer is not performed when this status is set. Check how the external start and stop signals are input.
CLOCK ERROR	Indicates that, during data input/output at an external clock pulse, the next clock pulse was received. If this status is set, consider lowering the external clock frequency.

These status can be obtained by using the relevant API-CNT(98/PC) function in API-PAC(W32).
The 32-bit or 64-bit transfer count can be obtained by using the relevant API-CNT(98/PC) function in API-PAC(W32). The transfer count is obtained as the number of data items (per channel) which have been transferred to the memory area for the user application.

Control of a sampling

This product can use a sampling clock to collect sampling data at fixed time intervals. The sampling clocks, sampling start triggers, and sampling stop triggers are listed below.

Table 4.5. Sampling clock / start / stop

Item	Factor	Description	Note
Sampling clock	Not used	Sampling is not used.	Set for counter mode.
	Internal clock	Internal clock (50nsec to 107sec) 25nsec unit	
	External clock	Fall of external sampling clock input (EXTCLK) (Maximum frequency response of 10 MHz)	
Sampling start trigger	Not used	Sampling is not used.	Set for counter mode.
	Software	Software command	
	Rise of an external signal	Rise of external sampling start signal (EXTSTART)	
	Fall of an external signal	Fall of external sampling start signal (EXTSTART)	
	Count match	When the count value for channel 0 - 3 matches the value in comparison register 0 or 1	
Sampling stop trigger	Not used	Sampling is not used.	Set for counter mode.
	Software	Software command	
	Rise of an external signal	Rise of external sampling stop signal (EXTSTOP)	
	Fall of an external signal	Fall of external sampling stop signal (EXTSTOP)	
	Count match	When the count value for channel 0 - 3 matches the value in comparison register 0 or 1	
	Specified number of times	Terminated after sampling for the specified number of times	
	Bus master error	When FIFO memory has become full	

- Sampling can be controlled by one clock, start, and stop trigger per board. One sampling start trigger per board and one sampling stop trigger per board are available. Triggering on the rising or falling edge can be selected.
- The first sampling data is collected when a sampling start trigger is input (not synchronized with the sampling clock). Collection of the second and subsequent sampling data is synchronized with the sampling clock. Note that this means that the time between the first and second samples may be less than the specified sampling clock period.
- Sampling halts immediately when the sampling stop trigger is input. No sampling data is collected at or after the time when sampling stops.
- Although the sampling clock can be set as fast as 50nsec, this is for sampling of one channel only. If the number of sampled channels is greater, the minimum sampling clock period becomes the number of sampling channels x 50nsec.
Example: Minimum sampling clock for 4-channel sampling = 4 x 50 nsec = 200 nsec

Hardware event

Types of hardware events

This product includes functions that can operate automatically in response to a change in a control input signal, control output signal, or counter match. These are called hardware events. One control input signal line and one control output signal line are provided for each channel.

Table 4.6. Hardware event

Item	Purpose	Condition
Control input signal *1	Preset	Rise (Low → High)
		Fall (High → Low)
	Zero-clear	Rise (Low → High)
		Fall (High → Low)
	Counter start/stop	Rise (Low → High)
		Fall (High → Low)
Control output signal *2	Count match (Register 0)	Count value = Comparison register 0
	Count match (Register 1)	Count value = Comparison register 1
	Abnormal input error	When phases A and B are changed at the same time
	Digital filter error	When a pulse faster than the digital filter setting is input
Count match	Preset	Count value = Comparison register 0
		Count value = Comparison register 1
	Zero clear	Count value = Comparison register 0
		Count value = Comparison register 1

*1 : When the control input signal is used as a general-purpose input, hardware events cannot be set as above.

*2 : When the above control output signals are set as hardware events, the output consists of a one-shot pulse output. The pulse width can be set by software to 10μsec, 1msec, 10msec, or 100msec. When using the control output signal as a general-purpose output, the output becomes a level output and the above hardware events cannot be assigned. The logic polarity of the output signal can be set by software.

Control input signal

One control input signal is provided for each channel. The control input signal can serve for one of the following applications. The application is software-selectable.

Table 4.7. Control input signal

Item	Purpose	Condition
Control input signal	General-purpose input	Software status(positive logic)
	Preset	Rise (Low → High)
		Fall (High → Low)
	Zero-clear	Rise (Low → High)
		Fall (High → Low)
	Counter start/stop	Rise (Low → High)
		Fall (High → Low)

General-purpose input

When not used for a hardware event, the control input pin can be used as the general-purpose input signal pin. The input logic is fixed as positive logic.

Preset

When the control input signal is set to presetting, the control input pin serves as the external trigger input pin for presetting. The rising or falling edge of the signal can be selected.

Zero-clear

When the control input signal is set to zero-clearing, the control input pin serves as the external trigger input pin for zero-clearing. The rising or falling edge of the signal can be selected.

A counter start/stop

When the control input signal is set to counter start/stop, the control input pin serves as the external trigger input pin for counter start/stop. The rising or falling edge of the signal can be selected for each of the counter start and counter stop.

Control output signal

One control output signal is provided for each channel. Each output signal can be used as a general-purpose output (level output) or as a one-shot pulse output for sending notification of hardware events to an external device.

Although all hardware events can be set to trigger a one-shot pulse output, in this case there is no way to determine which event caused the one-shot pulse to be output and therefore the status needs to be checked or similar.

The output polarity is set by software.

Table 4.8. Control output signal

Item	Purpose	Condition	Note
Control output signal	General-purpose output	Software command	Level output (Positive logic/Negative logic)
	Count match (Register 0)	Count value = Comparison register 0	A one shot pulse is outputted. (Positive logic/Negative logic) *
	Count match (Register 1)	Count value = Comparison register 1	
	Abnormal input error	When phases-A and B are changed at the same time	
	Digital filter error	When a pulse faster than the digital filter setting is input	

* The one-shot pulse width is set by software to 10 μ sec, 100 μ sec, 1 msec, 10 msec, or 100 msec.

General-purpose output

When not used as a hardware event, the control output pin can be used as a general-purpose output. In this case, the output becomes a level output rather than a one-shot pulse output. The logic polarity can be switched between positive and negative.

Count match (Register 0)

A one-shot pulse is output to indicate a count match (register 0) output when the count matches the value set in compare register 0. The logic polarity can be switched between positive and negative.

Count match (Register 1)

A one-shot pulse is output to indicate a count match (register 1) output when the count matches the value set in compare register 1. The logic polarity can be switched between positive and negative.

Abnormal input error

A one-shot pulse is output to indicate an abnormal input error when the A and B phases change simultaneously. The logic polarity can be switched between positive and negative.

Digital filter error

A one-shot pulse is output to indicate a digital filter error if a pulse faster than the digital filter time setting is input. The logic polarity can be switched between positive and negative.

Count match

Functions are provided to generate an interrupt, externally output a one-shot pulse, preset the counter value, or clear the counter to zero when the count matches the value in compare register 0 or compare register 1.

This product has two compare registers for each channel used to compare the count value. If two or more registers are required, use software to update the register values. Having two compare registers available allows upper and lower limits to be set.

Table 4.9. Count match

Item	Factor	Function
Count match	Count value = Comparison register 0 or Count value = Comparison register 1	Interrupt
		One-shot pulse output
		Preset
		Zero-clear

Given below are application examples using the count match feature.

< Example 1 > Move count values 100 - 200 in both ways. When the count value falls below 90 or exceeds 210, a one-shot pulse is output to an external device.

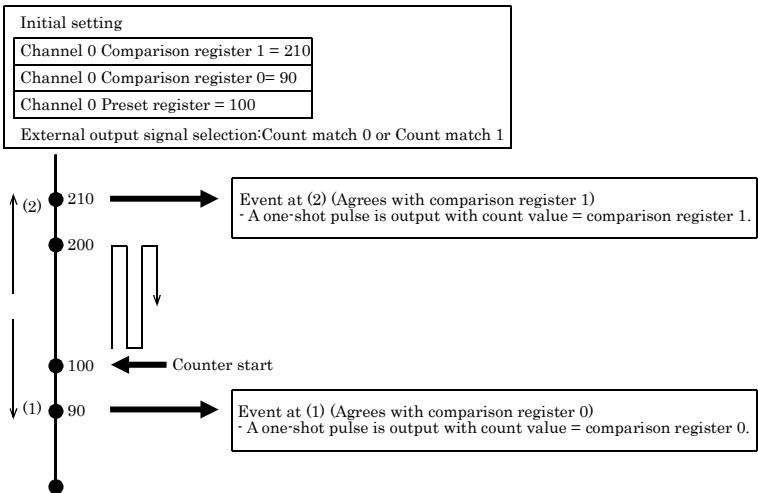


Figure 4.13. Example 1

< Example 2 > Start sampling with a count value of 500 and stop it with a count value of 1000.

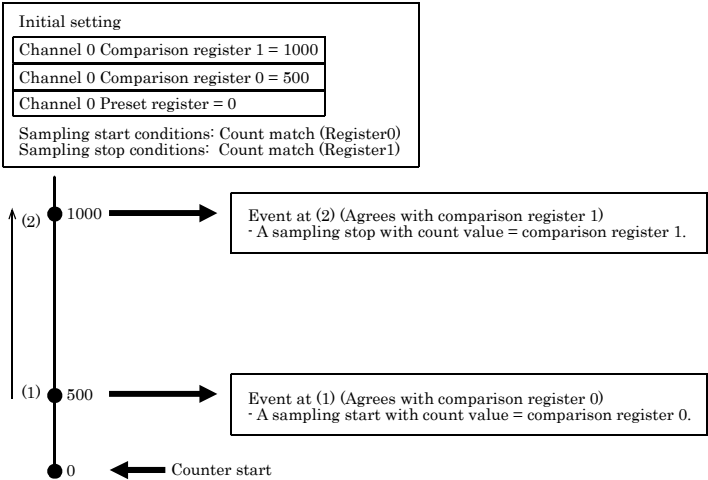


Figure 4.14. Example 2

< Example 3 > Set comparison values of 100, 200, 300, 400, 500, ... and generate interrupts in sequence.

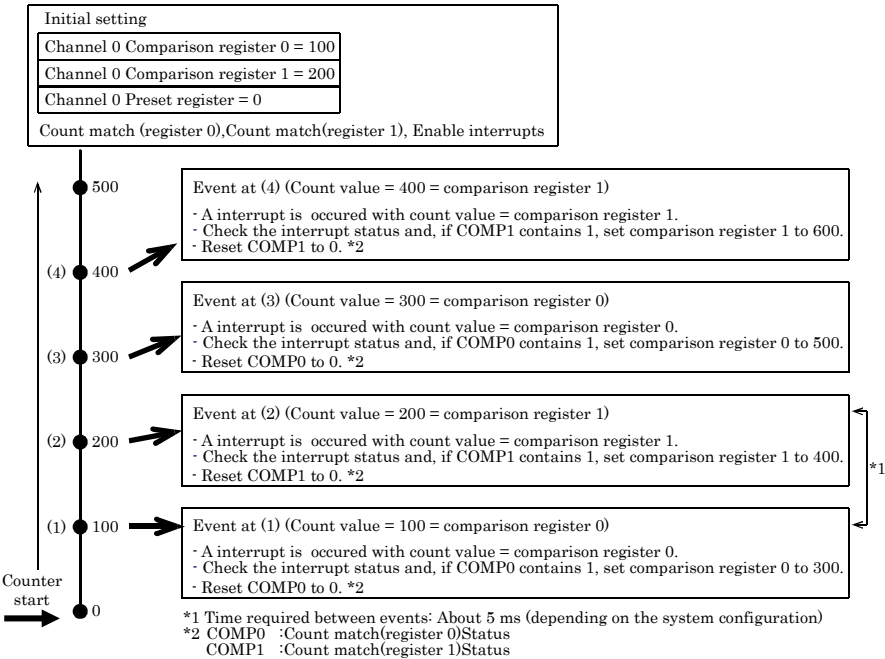


Figure 4.15. Example 3

Counter error

Counter errors are classified into two types: digital filter error and abnormal input error.

Digital filter error

A digital filter error occurs when a signal faster than the digital filter setting is input to phase A or phase B. Notification of the error is via a status (latch/clear), interrupt, or external output (one-shot pulse).

The board monitors the input signal level for digital filter errors using the filter source clock which has a period of 1/2 the setting value. The digital filter error is detected when the same level cannot be detected for two or more consecutive times.

However, a filter error does not occur if the frequency of the input clock is equal to or close to an integer multiple of the filter source clock.

The following are possible causes for a digital filter error.

- When a signal faster than the digital filter setting is input
- Noise is generated.

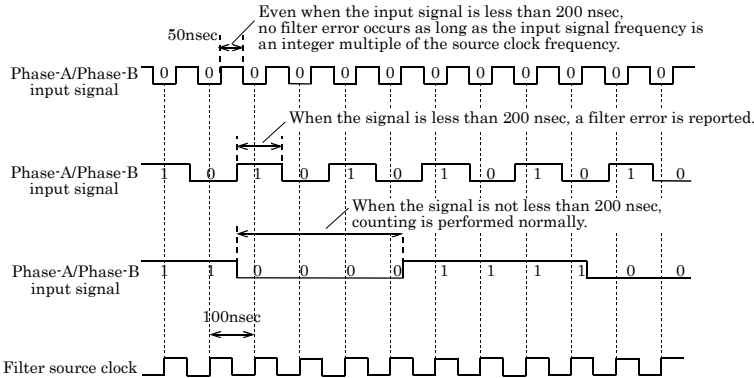


Figure 4.16. Filter error (Set to 0.2 μ sec)

Abnormal input error

An abnormal input error occurs when the counter input signal changes on the A and B phases simultaneously. Notification of the error is via a status (latch/clear), interrupt, or external output (one-shot pulse). If the digital filter is enabled, an abnormal input error occurs when both the A and B phases change during one period of the digital filter source clock. When the filter is disabled, an abnormal input error occurs when both the A and B phases change during one period of the board's 40MHz (25nsec) reference clock.

A possible cause of the abnormal input error is as follows.

- When the phase difference between phases-A and B is shorter than one digital filter source clock cycle (25 nsec with no filter set)
- Noise is generated.

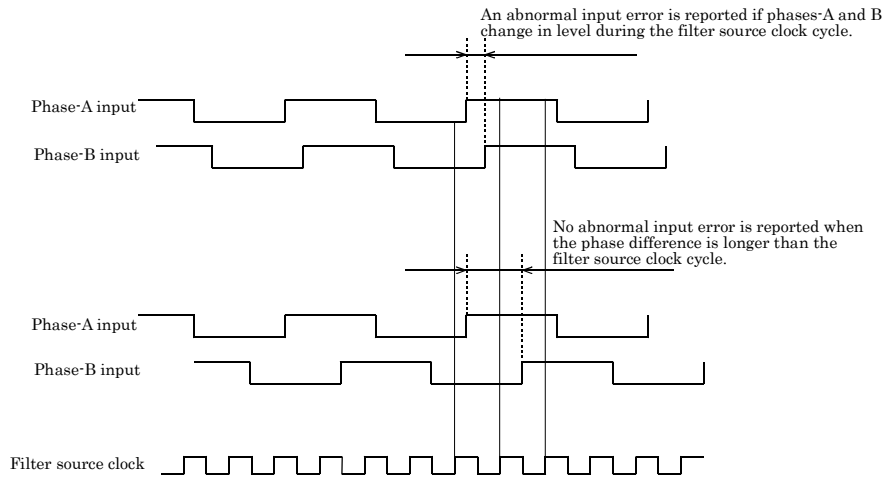


Figure 4.17. Abnormal input error

Sampling output signal

The start, stop, or clock signals used to control sampling on this product can be output from the interface connector.

Sampling start output signal

This outputs the sampling start trigger as a one-shot pulse signal (100nsec) with negative logic polarity. The logic polarity and pulse width cannot be modified.

Sampling stop output signal

This outputs the sampling stop trigger as a one-shot pulse signal (100nsec) with negative logic polarity. The logic polarity and pulse width cannot be modified.

Sampling clock output signal

This outputs the sampling clock trigger as a one-shot pulse signal (100nsec) with negative logic polarity. The logic polarity and pulse width cannot be modified.

Status input

This product has the following status.

Pulse signal input states

The phase-A, phase-B, and phase-Z input states and count directions can be checked by their status.

Control input signal states

The control input signal states can be checked by the status.

Error

Abnormal input error

An abnormal input error occurs when the counter input signal changes on the A and B phases simultaneously. A status is latched to indicate that this error has occurred and can be cleared by software.

Digital filter error

When a signal faster than the digital filter setting is input to phase-A or B, it is reported as a digital filter error. The status is latched and cleared by software.

Carry/Borrow

Carry

The 32-bit counter is set to [1] when incremented from its maximum value FFFFFFFFh - 0h.

Borrow

The counter is set to [1] when decremented from 0h - FFFFFFFFh.

Count match

A count match (to register 0), count match (to register 1), incremental count match, or decremental count match for each channel can be checked by the status.

Other functions

Digital filter

The digital filter is provided so that the counter works normally even when the pulse input to the counter, phase-A/B/Z signal, or control input signal has noise. When the digital filter detects the High (or Low) level maintained for the digital filter setting time, it outputs “High” (or “Low”) to the counter circuit. The setting range is set by software to “unused” or 0.1 μ sec - 1.6384 msec.

Note that, since all of these digital signals are input to the internal counter through the digital filter, a delay of the set time is required for them to be input when the digital filter is used.

Initially, the delay owing to the digital filter does not occur as it is not used by default.

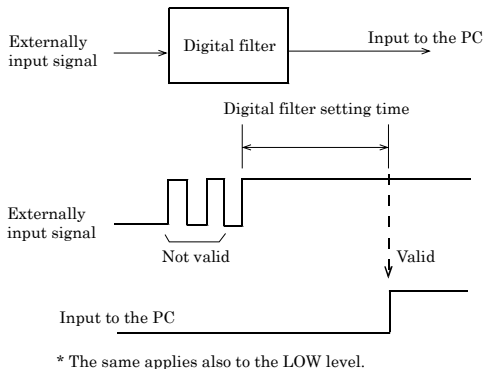


Figure 4.18. Digital filter



CAUTION

- The digital filter is initially disabled. (It remains disabled when left untouched.)
- The delay may be longer than the set time depending on the noise included.
- If the level changes at a frequency shorter than the set time, the level change is ignored and the input is not counted correctly.

Timer

The timer can generate an interrupt at software-set intervals. The setting range is 1 - 6553 msec. (in 1 ms increments).

5. About Software

CD-ROM Directory Structure

```
\
|—Autorun.exe           Installer Main Window
|  |—Readmej.html       Version information on each API-TOOL (Japanese)
|  |—Readmeu.html       Version information on each API-TOOL (English)
|  .
|  .
|—APIPAC                Each installer
|  |—AIO
|  |  |—DISK1
|  |  |—DISK2
|  |  |—.....
|  |  |—DISKN
|  |—AioWdm
|  |—CNT
|  |—DIO
|  |—.....
|  .
|—HELP                  HELP file
|  |—Aio
|  |—Cnt
|  |—.....
|  .
|—INF                   Each INF file for OS
|  |—WDM
|  |—Win2000
|  |—Win95
|  .
|—linux                 Linux driver file
|  |—cnt
|  |—dio
|  |—.....
|  .
|—Readme                Readme file for each driver
|  .
|  .
|—Release               Driver file on each API-TOOL
|  |—API_NT              (For creation of a user-specific install program)
|  |—API_W95
|  .
|  .
|—UsersGuide            Hardware User's Guide (PDF files)
```


About Software for Windows

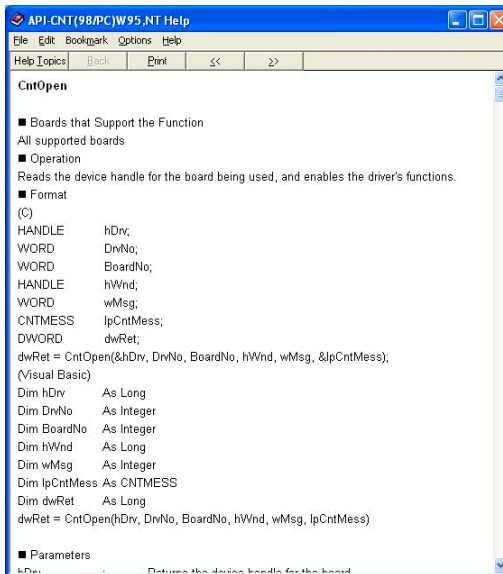
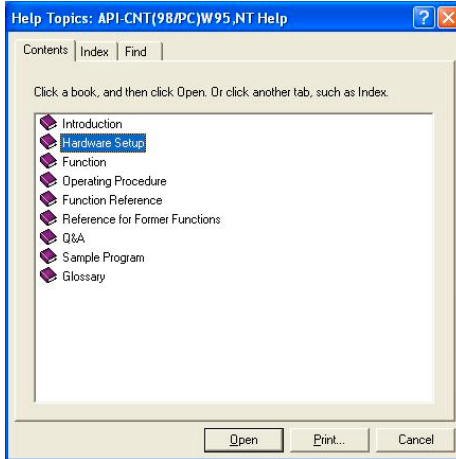
The bundled CD-ROM “Driver library API-PAC(W32)” contains the functions that provide the following features:

- Function to read the current count value of a specified channel.
- Function to read the current status register for a specified channel.
- Function to prevent chattering based on a digital filter using hardware capabilities.
- Function to preset or zero-clear the counter at the rising or falling edge of the control input signal.
- Function to output a one-shot pulse to the control output signal upon detection of a count match or error.
- Function to sample count values using bus mastering in sync with the specified external clock or internal clock.

For details, refer to the help file. The help file provides various items of information such as “Function Reference”, “Sample Programs”, and “FAQs”. Use them for program development and troubleshooting.

Accessing the Help File

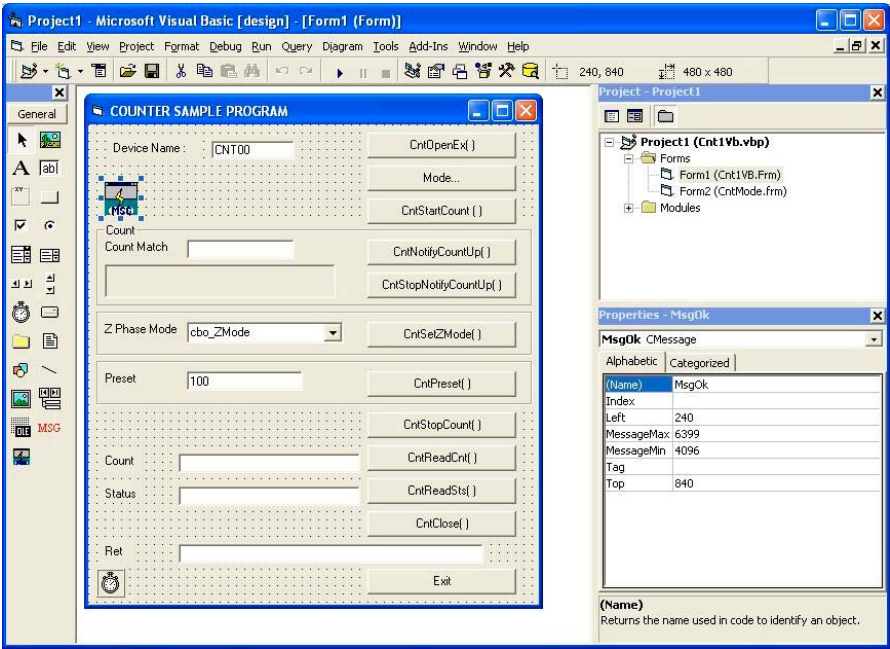
- (1) Click on the [Start] button on the Windows taskbar.
- (2) From the Start Menu, select “Programs” – “CONTEC API-PAC(W32)” – “CNT” – “API-CNT HELP” to display help information.



Using Sample Programs

Sample programs have been prepared for specific basic applications.
To use each sample program, enter its device name set by API-TOOL Configuration.

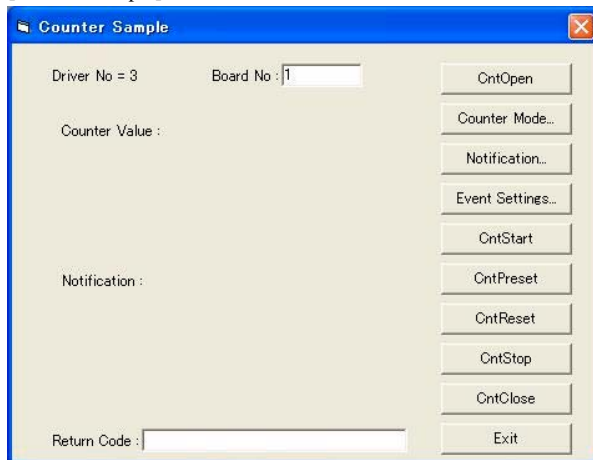
Use these sample programs as references for program development and operation check.
The sample programs are stored in \Program Files\CONTEC\API-PAC(W32)\Cnt\Samples or
\Program Files\CONTEC\API-PAC(W32)\Cnt\Samples\CntMaster(Sample program for
CNT32-8M(PCI)/CNT32-4MT(CB)/CNT-3204MT-LPE/).



Sample Programs - Examples

- Counter Sample : Execute basic operations such as input signal count processing and hardware event handling for four channels.
- Sampling Sample : Samples pulse signals at four channels, saves the resulting data to a text file, and displays it along with the sampling status.

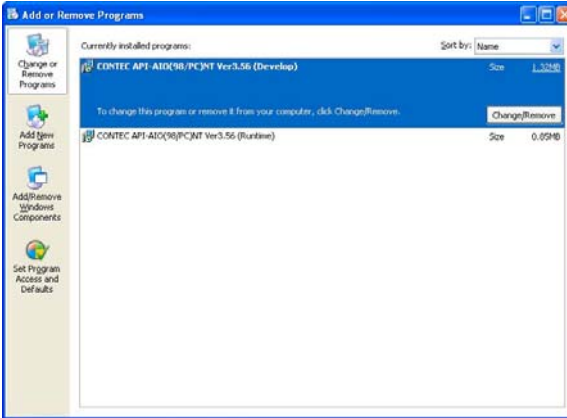
[Counter Sample]



Uninstalling the API Function Libraries

To uninstall API-PAC(W32), follow the procedure below.

- (1) Click on the [Start] button on the Windows taskbar. From the Start Menu, select “Settings” - “Control Panel”.
- (2) Double-click on “Add/Remove Programs” in the Control Panel.
- (3) Select “CONTEC API-CNT(98/PC)xx” from the application list displayed, then click on the [Change/Remove] button. Follow the on-screen instructions to uninstall the function libraries.



6. About Hardware

This chapter provides hardware specifications and hardware-related supplementary information.

For detailed technical information

For further detailed technical information (“Technical Reference” including the information such as an I/O map, configuration register, etc.), visit the Contec's web site (<http://www.contec.com/support/>) to call for it.

Hardware specification

Tables 6.1 list the hardware specifications of the board.

Table 6.1. Specification <1 / 3>

Item		Specification
Input		
	Counter	
	Channel count	4 channels
	Count system	Up/down counting (2-phase/Single-phase/Single-phase Input with Gate Control Attached)
	Max. count	FFFFFFFFh(binary data, 32Bit)
	Input type	Unisolated LVTTTL level input
	Input signal	Phase-A/UP 1 x 4 channels Phase-B/DOWN 1 x 4 channels Phase-Z/CLR 1 x 4 channels
	Response frequency	10MHz 50% duty
	Digital filter	0.1μsec · 1.6384msec or not used (can be independently set for each channel.)
	Timer	1msec · 6553msec 1msec unit
	Counter start trigger	Software/External start input/Sampling start trigger
	Counter stop trigger	Software/External stop input/Sampling stop trigger
	Sampling	
	Sampling start trigger	Software/External start input/Count match
	Sampling stop trigger	Software/External stop input/Specification number/Bus master transfer error/Count match
	Sampling clock	Sampling timer/External clock input
	Sampling timer	50nsec · 107sec 25nsec unit (can not be independently set for each channel.)
	External sampling start signal	Unisolated LVTTTL level input (Select Rise or Fall)
	External sampling stop signal	Unisolated LVTTTL level input (Select Rise or Fall)
	External sampling clock signal	Unisolated LVTTTL level input (Fall)
	Response frequency	10MHz 50% duty

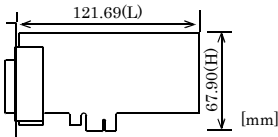
Table 6.1. Specification < 2 / 3 >

Item		Specification
Input		
Control	Control	
	Control input signal type	Unisolated LVTTTL level input
	Control input channel	1 x 4 channels
	Control input signal	<ul style="list-style-type: none"> - Preset (Select Rise or Fall) - Zero-clear (Select Rise or Fall) - Counter start/stop (Select Rise or Fall) - General-purpose input (positive logic) Software-selected from among the above four options
	Response time	100nsec (Max.)
	Interrupt event	Count match (8 points), Counter error (2 points), Sampling factor (6 points), Carry/Borrow (1 points), Timer (1 points)
Output		
Control	Control	
	Control output signal type	Unisolated LVTTTL level output
	Control output channel	1 x 4 channels
	Control output signal	<ul style="list-style-type: none"> - Count match 0 output(one-shot pulse output) - Count match 1 output(one-shot pulse output) - Digital filter error output(one-shot pulse output) - Abnormal input error output(one-shot pulse output) - General-purpose output(Level output) Software-selected from among the above five options (Positive/negative logic is selected with the software.)
	One shot output signal amplitude	Selected between 10μsec, 100μsec, 1msec, 10msec and 100 msec (Can be set for each channel, within precision + 1μsec)
	Response time	100nsec (Max.)
	Rated output current	I _{OL} =8mA(Max.) I _{OH} =-8mA(Max.)
	Test pulse	
	Test pulse output signal type	Unisolated LVTTTL level output
	Test pulse output point	One for each of phases A and B
	Output frequency	100kHz fixed
Sampling	Sampling	
	Sampling output signal type	Unisolated LVTTTL level output
	Output point	Sampling start trigger, sampling stop trigger, Sampling clock trigger 1 point each
	One-shot output signal width	Negative logic 100nsec width (fixed)
	Response speed	100nsec (Max.)
	Rated output current	I _{OL} = 8mA(Max.) I _{OH} = -8mA(Max.)
Bus master		
	DMA channel	1 channel
	Transfer bus width	32-Bit width
	Transfer data length	8 PCI Words length (Max.)
	Transfer rate	80MB/sec (Max.133MB/sec)
	FIFO	1K-DWord
	Scatter/Gather function	64MB
	Interrupt event	Bus master event (7 points)

Table 6.1. Specification < 3 / 3 >

Item	Specification
Common	
Max. board count for connection	16 boards including the master board
I/O address	Occupies 2 locations, any 32-bytets and 64-byte boundary
Power consumption	3.3VDC 450mA
Operating condition	0 - 50°C, 10 - 90%RH (No condensation)
Bus specification	PCI Express Base Specification Rev. 1.0a x1
Dimension (mm)	121.69(L) x 67.90(H)
Connector used	68 pin 0.8mm pitch connector HDRA-E68LFDT+[HONDA TSUSHIN KOGYO CO., LTD.] or equivalent to it
Weight	60g

Board Dimensions



The standard outside dimension (L) is the distance from the end of the board to the outer surface of the slot cover.

Block Diagram

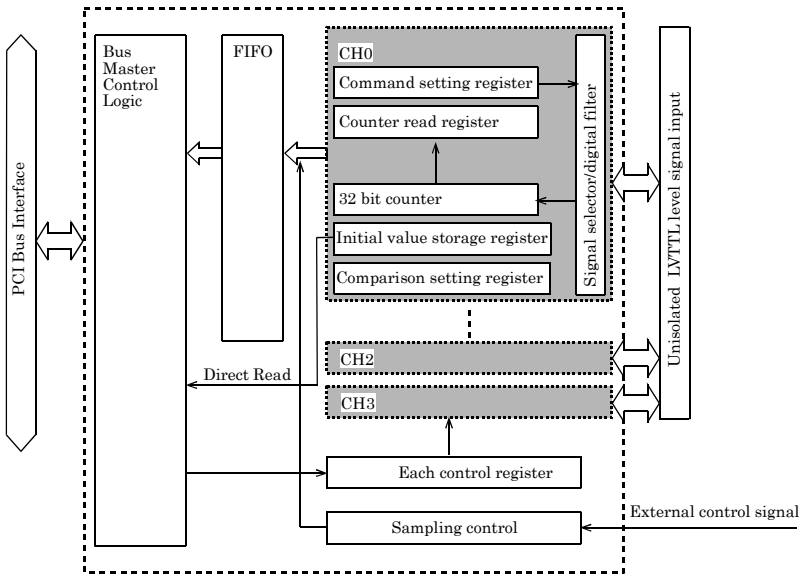


Figure 6.1. Block Diagram

CNT-3204MT-LPE

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