

FA-9500: Release note

***** 25/March/2016 *****

SOFT: Ver 8.10 *Updated

FPGA1:Ver.3.20

FPGA2:Ver.5.21

* Added new SNMP MIB objects. (Process/CC/Converter/Video System/Audio)

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<https://www.dropbox.com/s/87qw7x1izma21q0/2016-0325%20MU%28SOFT%3D8.10%20FPGA%3D3.20%2B5.21%2B1.02%29.zip?dl=0>

***** 21/July/2015 *****

SOFT: Ver 8.03

FPGA1:Ver.3.20

FPGA2:Ver.5.21

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* 27MHz genlock clock for the analog input and frame pulse phase was not good. Fixed.

ftp://ftp.for-a.info/FA-9500/2015-0721 MU(SOFT=8.03 FPGA=3.20+5.21).zip

***** 06/Jun/2014 *****

SOFT: Ver.8.03

FPGA1:Ver.3.20

FPGA2:Ver.5.20

<<< Bug Fix >>>

* There was a problem that audio outputs is going to be mute at a few - 30 minutes after power up. Fixed.

* Data related with 95ALA wasn't saved and loaded with event save/recall.

* Some data related to Dolby Master Gain and ANC configuration weren't properly processed with backup save/load. Fixed.

* REF status displayed the format of composite input when Input Lock mode.

It now displays "Not used" since the ref is not used when Input Lock mode.

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ftp://ftp.for-a.info/FA-9500/2014-0606%20MU(SOFT=8.03%20FPGA=3.20+5.20).zip

***** 25/Sep/2013 *****

SOFT: Ver.8.01

FPGA1:Ver.3.20

FPGA2:Ver.5.20

<<< Bug Fix >>>

- * he false detection of analog audio when there is no input.
- * If there are units which are V8.00 and previous version in the same network, it is possible that MAC address are duplicated.
- * Web GUI didn't display Change-Over function menu in GPI assignment list.

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[ftp://ftp.for-a.info/FA-9500/2014-0221%20MU\(SOFT=8.01%20FPGA=3.20+5.20+1.02\).zip](ftp://ftp.for-a.info/FA-9500/2014-0221%20MU(SOFT=8.01%20FPGA=3.20+5.20+1.02).zip)

***** 06/Jun/2013 *****

SOFT: Ver.8.00

FPGA1:Ver.3.20

FPGA2:Ver.5.20

<<< Bug Fix >>>

- * Waveform of CC608, Line21 of SD-SDI(NTSC) was not correct. The pulses were longer than standards.

Fixed as TV-Globo requested.

The pulse length were measured differently in Factory & TV-Globo. TV-Globo's value was a little longer.

So we had to shorten the pulses.

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- * FA-95ALA now supported.

[ftp://ftp.for-a.co.jp/Products_Update/FA-9500/2013-0601%20MU\(SOFT=8.00%20FPGA=3.20+5.20\)%20RU\(SOFT=6.00\).zip](ftp://ftp.for-a.co.jp/Products_Update/FA-9500/2013-0601%20MU(SOFT=8.00%20FPGA=3.20+5.20)%20RU(SOFT=6.00).zip)