2.1.2. Balanced AES, DARS and Analog Audio Connections

DARS/AES/ANALOG: These 16 pin terminal strips are for connecting the balanced version of the AES and DARS signals as well as two balanced analog audio signals. The output cables can be secured into the removable portion of the terminal strips using a small screwdriver. The removable part of the terminal strip is then inserted into the rear panel and secured using the hold down screws. The pinout of these connectors is shown in Table 2-1.

DARS +	AES2 +	AES1 +	GND	GND	GND	RIGHT -	RIGHT +
DARS -	AES2+	AES1 -	GND	GND	GND	LEFT -	LEFT +

Table 2-1: DARS/AES/ANALOG Terminal Strip Pin Definitions

2.1.3. Linear Time Code, Syncro and GPIO Connections

AUX I/O: These two 15 pin male 'D' connectors contain GPI inputs and outputs and two LTC outputs from the 5601MSC units and should be connected to the AUX I/O Connectors on the respective 5601MSC units using the straight through 15 pin cables provided. The pinout of the **AUX I/O** connector is as follows:

Pin #	Name	Description
1	LTC IN+	LTC + input to 5601MSC
2	LTC1+	LTC 1 + output from 5601MSC
3	LTC2+	LTC 2 + output from 5601MSC
4	GPO1	GPO 1 output from 5601MSC
5	GPO2	GPO 2 output from 5601MSC
6	GND	Signal Ground
7	GND	Signal Ground
8	STXA	Syncro TX from A
9	STXB	Syncro TX to B
10	GND	Signal Ground
11	LTC IN-	LTC - input to 5601MSC
12	LTC1-	LTC 1 – output from 5601MSC
13	LTC2-	LTC 2 - output from 5601MSC
14	GPI1	GPI 1 input to 5601MSC
15	GPI2	GPI 2 input to 5601MSC

Note that on the B connector, 8 is TX from B, and 9 is TX to A.

Table 2-2: AUX I/O Pin Definitions

LTC 1 OUT, LTC 2 OUT: These two male XLR connectors provide the balanced LTC1 and LTC2 time code outputs from the changeover.

ACO CTRL/STATUS: The top row of the 16 pin terminal strip has two GPI inputs to control the 5601ACO2 and two GPI Outputs that provide tallies to indicate the status of the 5601ACO2.